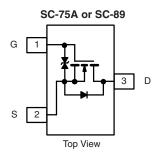




N-Channel 1.8 V (G-S) MOSFET

| PRODUCT SUMMARY | | | | |
|---------------------|---------------------------------|---------------------|--|--|
| V _{DS} (V) | $R_{DS(on)}\left(\Omega\right)$ | I _D (mA) | | |
| | 0.70 at V _{GS} = 4.5 V | 600 | | |
| 20 | 0.85 at V _{GS} = 2.5 V | 500 | | |
| | 1.25 at V _{GS} = 1.8 V | 350 | | |



| ORDERING INFORMATION | | | | |
|--|---------------------|-----------------|--|--|
| Part Number | Package | Marking Code | | |
| Si1012R-T1-GE3 (Lead (Pb)-free and Halogen-free) | SC-75A (SOT-416) | С | | |
| Si1012X-T1-GE3 (Lead (Pb)-free and Halogen-free) | SC-89 (SOT-490) | А | | |

FEATURES





Gate-Source ESD Protected: 2000 V

High-Side Switching

Low On-Resistance: 0.7 Ω Low Threshold: 0.8 V (typ.) Fast Switching Speed: 10 ns

Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories
- **Battery Operated Systems**
- **Power Supply Converter Circuits**
- Load/Power Switching Cell Phones, Pagers

BENEFITS

- · Ease in Driving Switches
- Low Offset (Error) Voltage
- Low-Voltage Operation
- **High-Speed Circuits**
- Low Battery Voltage Operation

| ABSOLUTE MAXIMUM RATINGS (| T _A = 25 °C, unless | otherwise no | ted) | | |
|---|-----------------------------------|-----------------|--------------|------|------|
| Parameter | Symbol | 5 s | Steady State | Unit | |
| Drain-Source Voltage | | V _{DS} | 20 | | V |
| Gate-Source Voltage | | V _{GS} | ± 6 | | |
| 2 11 2 17 17 20 h | T _A = 25 °C | I_ | 600 | 500 | |
| Continuous Drain Current (T _J = 150 °C) ^b | T _A = 85 °C | I _D | 400 | 350 | |
| Pulsed Drain Current ^a | | I _{DM} | 1000 | | mA |
| Continuous Source Current (Diode Conduction) ^b | | I _S | 275 | 250 | |
| M : D D: : :: ht 00.75 | T _A = 25 °C | | 175 | 150 | - mW |
| Maximum Power Dissipation ^b for SC-75 | T _A = 85 °C | | 90 | 80 | |
| | T _A = 25 °C | P _D | 275 | 250 | |
| Maximum Power Dissipation ^b for SC-89 | T _A = 85 °C | | 160 | 140 | |
| Operating Junction and Storage Temperature Range | T _J , T _{stg} | - 55 to 150 | | °C | |
| Gate-Source ESD Rating (HBM, Method 3015) | | ESD | 2000 | | V |

- a. Pulse width limited by maximum junction temperature.
- b. Surface mounted on FR4 board.





Si1012R/X

Vishay Siliconix



| SPECIFICATIONS (T _A = 25 °C, unless otherwise noted) | | | | | | | |
|--|---------------------|--|------|-------|-------|------|--|
| Parameter | Symbol | Test Conditions Min. | | Тур. | Max. | Unit | |
| Static | | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_{D} = 250 \mu A$ | 0.45 | | 0.9 | V | |
| Gate-Body Leakage | I _{GSS} | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$ | | ± 0.5 | ± 1.0 | μΑ | |
| Zava Cata Valtaga Dvain Current | I _{DSS} | V _{DS} = 20 V, V _{GS} = 0 V | | 0.3 | 100 | nA | |
| Zero Gate Voltage Drain Current | | $V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$ | | | 5 | μΑ | |
| On-State Drain Current ^a | I _{D(on)} | V _{DS} = 5 V, V _{GS} = 4.5 V | 700 | | | mA | |
| | R _{DS(on)} | $V_{GS} = 4.5 \text{ V}, I_D = 600 \text{ mA}$ | | 0.41 | 0.70 | | |
| Drain-Source On-State Resistance ^a | | $V_{GS} = 2.5 \text{ V}, I_D = 500 \text{ mA}$ | | 0.53 | 0.85 | Ω | |
| | | $V_{GS} = 1.8 \text{ V}, I_D = 350 \text{ mA}$ | | 0.70 | 1.25 | | |
| Forward Transconductance ^a | g _{fs} | V _{DS} = 10 V, I _D = 400 mA | | 1.0 | | S | |
| Diode Forward Voltage ^a | V_{SD} | I _S = 150 mA, V _{GS} = 0 V | | 0.8 | 1.2 | V | |
| Dynamic ^b | | | | | | | |
| Total Gate Charge | Q_g | | | 750 | | | |
| Gate-Source Charge | Q_{gs} | $V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 250 \text{ mA}$ | | 75 | | рС | |
| Gate-Drain Charge | Q_{gd} | | | 225 | | | |
| Turn-On Delay Time | t _{d(on)} | | | 5 | | | |
| Rise Time | t _r | V_{DD} = 10 V, R_L = 47 Ω | | 5 | | 200 | |
| Turn-Off Delay Time | t _{d(off)} | $I_D \cong 200 \text{ mA}, V_{GEN} = 4.5 \text{ V}, R_g = 10 \Omega$ | | 25 | | ns | |
| Fall Time | t _f | | | 11 | | | |

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

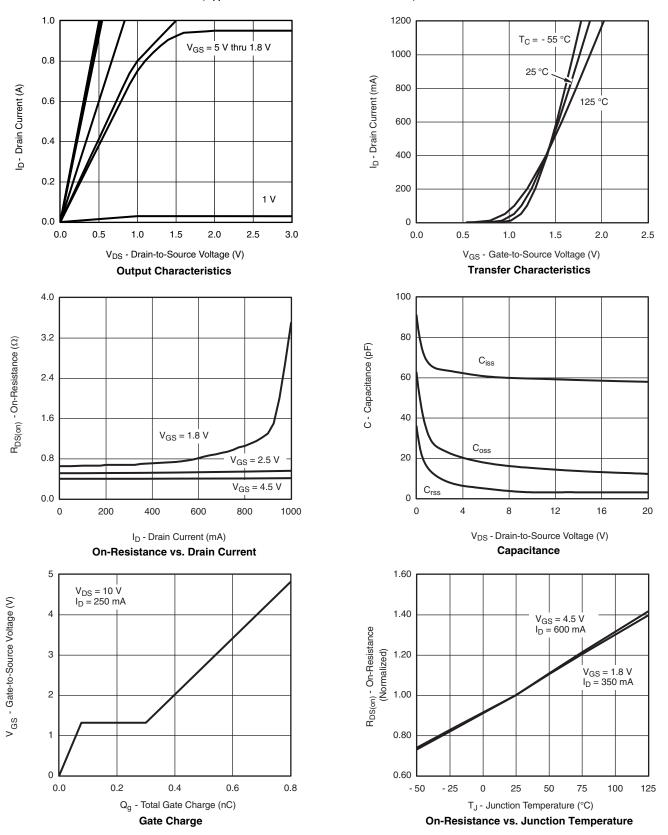
a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.





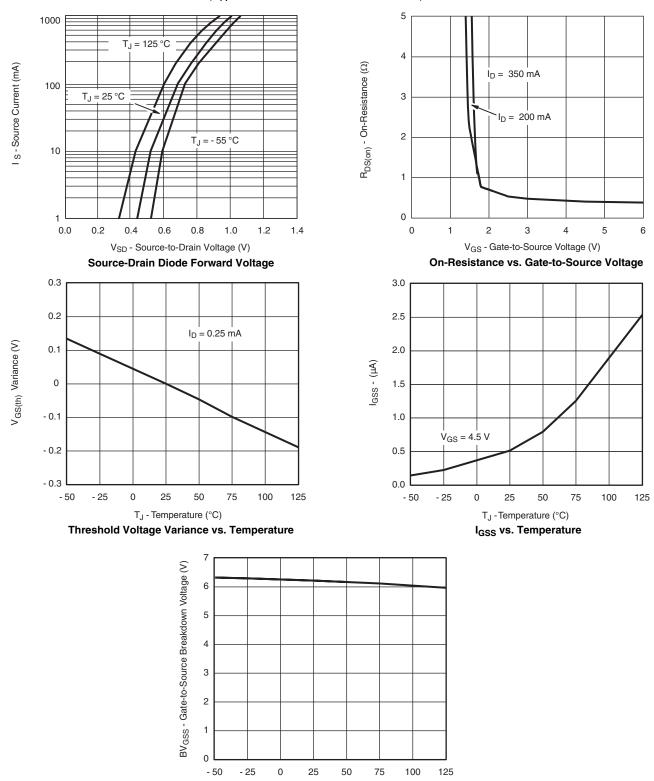
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



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TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

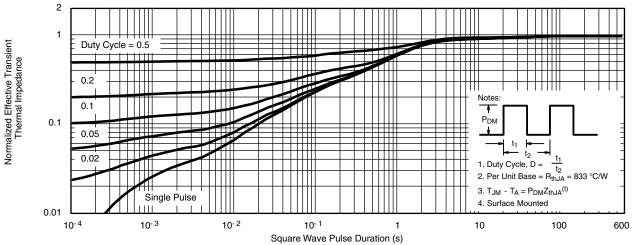


 T_J - Temperature (°C) BV_{GSS} vs. Temperature

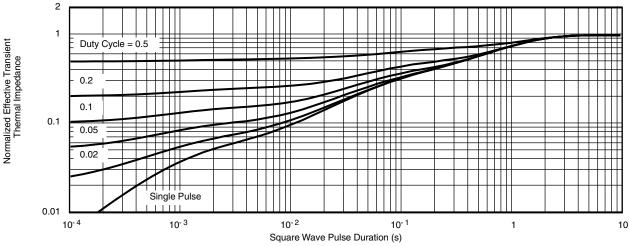




TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient (SC-75A)

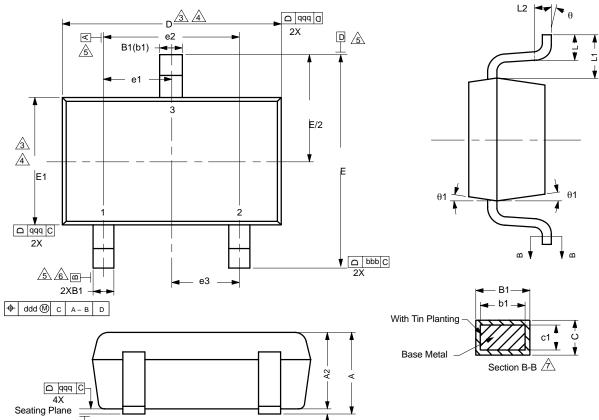


Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg271166.



SC-75A: 3-LEADS



NOTES:

Dimensions in Millimeters.

р



Dimension D does not include mold flash, protrusions or gate burrs. Mold flash protrusions or gate burrs shall not exceed 0.10 mm per end. Dimension E1 does not include Interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.10 mm per side.



4. Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, tie bar burrs, gate burrs and interelead flash, but including any mismatch between the top and bottom of the plastic body.



Datums A, B and D to be determined 0.10 mm from the lead tip.



Terminal positions are shown for reference only.

These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

| Dim | Tolerances |
|-----|------------|
| aaa | 0.10 |
| bbb | 0.10 |
| ccc | 0.10 |
| ddd | 0.10 |

| | M | | | | |
|---|----------|-------|------|------|--|
| Dim | Min | Nom | Max | Note | |
| Α | _ | - | 0.80 | | |
| A ₁ | 0.00 | - | 0.10 | | |
| A ₂ | 0.65 | 0.70 | 0.80 | | |
| B ₁ | 0.19 | - | 0.24 | 7 | |
| b ₁ | 0.17 | - | 0.21 | | |
| С | 0.13 | - | 0.15 | 7 | |
| c ₁ | 0.10 | - | 0.12 | 7 | |
| D | 1.55 | 1.575 | 1.60 | 3,4 | |
| Е | 1.50 | 1.60 | 1.70 | | |
| E ₁ | 0.73 | 0.76 | 0.80 | 3,4 | |
| e ₁ | | | | | |
| e ₂ | | | | | |
| e ₃ | | | | | |
| L | 0.15 | 0.205 | 0.30 | | |
| L ₁ | | | | | |
| L ₂ | 0.15 BSC | | | | |
| θ | 0° | - | 8° | | |
| θ1 | 4° | _ | 10° | | |
| ECN: S-03946-Rev. B, 09-Jul-01 DWG: 5868 | | | | | |

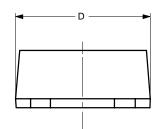
Document Number: 71348 www.vishay.com 06-Jul-01

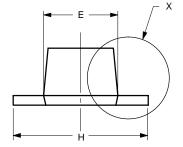


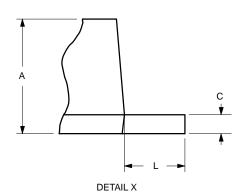


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SC89-3





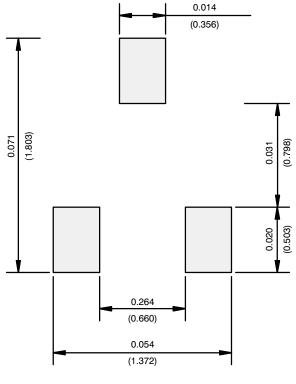


MILLIMETERS INCHES Dim Min Max Min Max 0.60 0.80 0.024 0.031 Α 0.23 0.33 0.009 0.013 b С 0.10 0.20 0.004 0.008 1.50 0.067 D 1.70 0.059 Ε 0.75 0.95 0.030 0.037 1.00 BSC 0.040 BSC е 0.50 BSC 0.020 BSC e₁ Н 1.50 0.059 0.067 0.30 0.012 0.020 L 0.50

ECN: S-03946—Rev. B, 09-Jul-01 DWG: 5869



RECOMMENDED MINIMUM PADS FOR SC-75A: 3-Lead



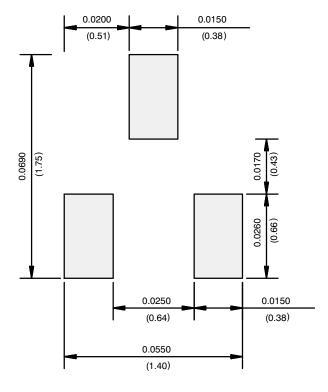
Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



RECOMMENDED MINIMUM PADS FOR SC-89: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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Document Number: 91000 www.vishay.com Revision: 11-Mar-11