

ECL, PECL & CLIPPED SINE WAVE CLOCK OSCILLATORS

TYPES DFN 14-E & DFN 14-O

ECL 10KH OR CLIPPED SINE WAVE OUTPUT

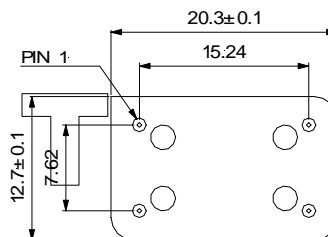
NEGATIVE OR POSITIVE SUPPLY VOLTAGE

OPTIONAL COMPLEMENTARY OUTPUTS

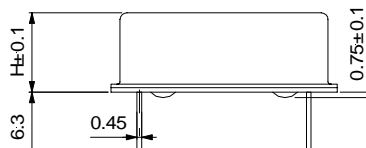
STANDARD DIL 14 PACKAGE

VARIOUS PIN-OUT OPTIONS

NO



Pin	DFN 14-E	DFN 14-O/EC
1	NC/Output B	NC/Output B
7	Vee	GND
8	Output A	Output A
14	GND	Vcc



RECOMMENDED

H = 7.00 mm

TYPE	DFN 14-E	DFN 14-EC	DFN 14-O
Frequency Range	16 to 175 MHz	16 to 175 MHz	16 to 175 MHz

ELECTRICAL SPECIFICATIONS	DFN 14-E	DFN 14-EC	DFN 14-O
supply voltage	-5.2 V ± 5 %	5 V ± 5 %	5 V ± 5 %
supply current	≤ 50 mA	≤ 50 mA	≤ 50 mA
output load	ECL 10KH (50 Ω to -2 V)	PECL 10 KH (50 Ω to 3 V)	50 Ω, square wave, AC coupled
duty cycle (ECL / PECL @ 50 % level)	40/60...60/40 %	40/60...60/40 %	40/60...60/40 % @ 0 V
rise/fall times (20 to 80 %)	≤ 2 ns	≤ 2 ns	
high/low levels/output amplitude	≥ -1.0 V / ≤ -1.6 V	≥ 3.97 V / ≤ 3.45 V	0 dBm ± 2 dB
start up	≤ 10 ms @ -4.94 V	≤ 10 ms @ 4.75 V	≤ 10 ms @ 4.75 V

FREQUENCY STABILITY		stability [ppm] and temperature code							
types	temperature range	stability	code	stability	code	stability	code	stability	code
all types	0 to -70°C	≤ ± 20	XB20	≤ ± 25	XB25	≤ ± 50	XB50	≤ ± 100	XB100
remarks	for codes XB20 and XB25 > 80 MHz please consult factory stability includes calibration at 25°C, temperature, ageing, Vcc and load changes 1 st yr.								

OPTIONS	CODE	DESCRIPTION
inverted pin-out	G	pin 14 : Vee, pin 7 : GND
complementary output	P	180° phase shifted

ORDERING CODE	type + option code + frequency + temperature code
Example	DFN 14-EC 77.760 MHz XB20