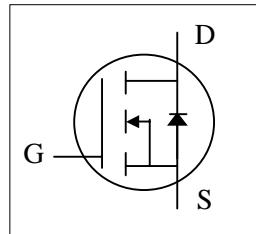
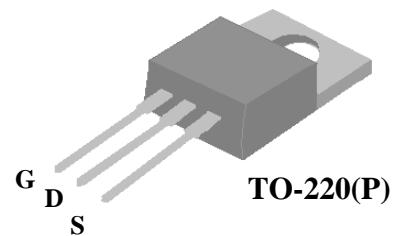




- ▼ 100% Avalanche Test
- ▼ Simple Drive Requirement
- ▼ Fast Switching Characteristic
- ▼ RoHS Compliant & Halogen-Free



BV_{DSS}	900V
$R_{DS(ON)}$	5.2Ω
I_D	3A



Description

AP03N90 series are specially designed as main switching devices for universal 90~265VAC off-line AC/DC converter applications.

The TO-220 package is widely preferred for commercial-industrial applications. The device is suited for switch mode power supplies, DC-AC converters and high current high speed switching circuits.

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	900	V
V_{GS}	Gate-Source Voltage	± 30	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	3	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	1.89	A
I_{DM}	Pulsed Drain Current ¹	10	A
$P_D @ T_C = 25^\circ C$	Total Power Dissipation	83.3	W
E_{AS}	Single Pulse Avalanche Energy ³	4.5	mJ
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Value	Units
R_{thj-c}	Maximum Thermal Resistance, Junction-case	1.5	°C/W
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient	62	°C/W



Electrical Characteristics@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	900	-	-	V
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ²	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=1.2\text{A}$	-	-	5.2	Ω
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	2	-	4	V
g_{fs}	Forward Transconductance	$\text{V}_{\text{DS}}=10\text{V}, \text{I}_D=1\text{A}$	-	2	-	S
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=720\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	25	μA
I_{GSS}	Gate-Source Leakage	$\text{V}_{\text{GS}}=\pm 30\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Q_{g}	Total Gate Charge ²	$\text{I}_D=1\text{A}$	-	18	29	nC
Q_{gs}	Gate-Source Charge	$\text{V}_{\text{DS}}=480\text{V}$	-	3.5	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	$\text{V}_{\text{GS}}=10\text{V}$	-	7	-	nC
$t_{\text{d(on)}}$	Turn-on Delay Time ²	$\text{V}_{\text{DD}}=300\text{V}$	-	20	-	ns
t_r	Rise Time	$\text{I}_D=1\text{A}$	-	14	-	ns
$t_{\text{d(off)}}$	Turn-off Delay Time	$\text{R}_G=50\Omega$	-	105	-	ns
t_f	Fall Time	$\text{V}_{\text{GS}}=10\text{V}$	-	24	-	ns
C_{iss}	Input Capacitance	$\text{V}_{\text{GS}}=0\text{V}$	-	800	1280	pF
C_{oss}	Output Capacitance	$\text{V}_{\text{DS}}=25\text{V}$	-	55	-	pF
C_{rss}	Reverse Transfer Capacitance	$f=1.0\text{MHz}$	-	4	-	pF
R_{g}	Gate Resistance	$f=1.0\text{MHz}$	-	4	6	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage ²	$\text{I}_S=1.2\text{A}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1.5	V
trr	Reverse Recovery Time ²	$\text{I}_S=1\text{A}, \text{V}_{\text{GS}}=0\text{V}$	-	320	-	ns
Qrr	Reverse Recovery Charge	$d\text{I}/dt=100\text{A}/\mu\text{s}$	-	1.3	-	μC

Notes:

1.Pulse width limited by max. junction temperature.

2.Pulse test

3.Starting $T_j=25^\circ\text{C}$, $\text{V}_{\text{DD}}=50\text{V}$, $L=1\text{mH}$, $\text{R}_G=25\Omega$

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

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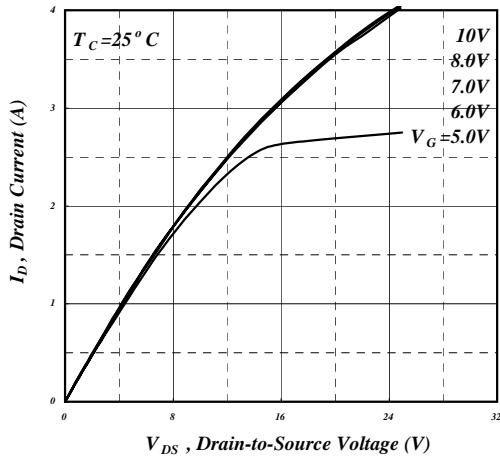


Fig 1. Typical Output Characteristics

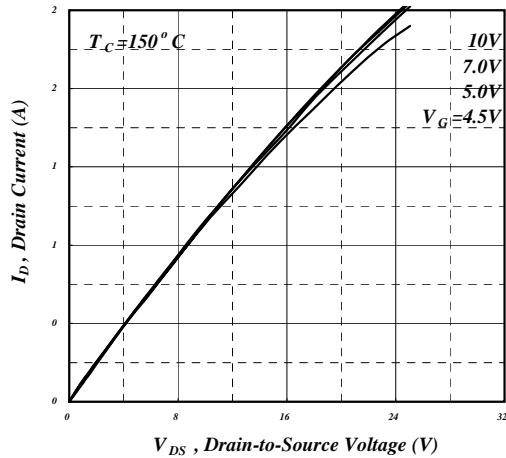


Fig 2. Typical Output Characteristics

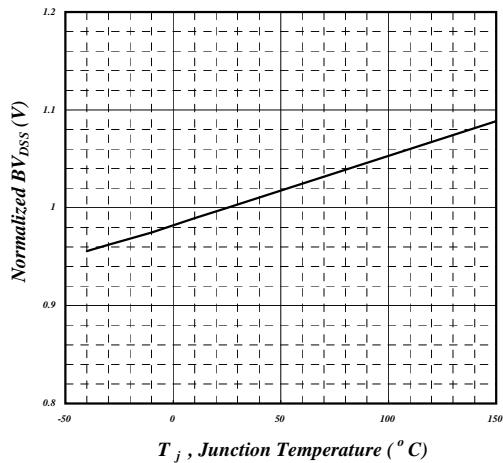
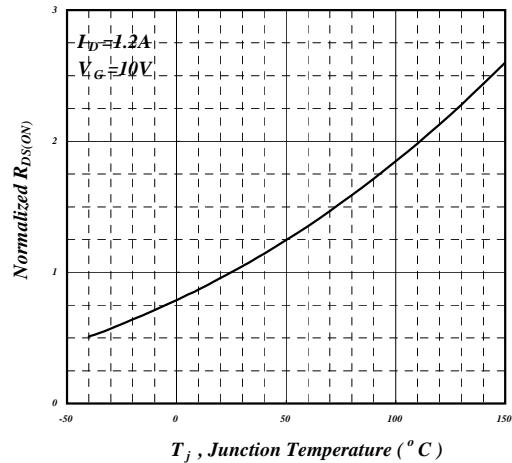
Fig 3. Normalized BV_{DSS} v.s. Junction Temperature

Fig 4. Normalized On-Resistance v.s. Junction Temperature

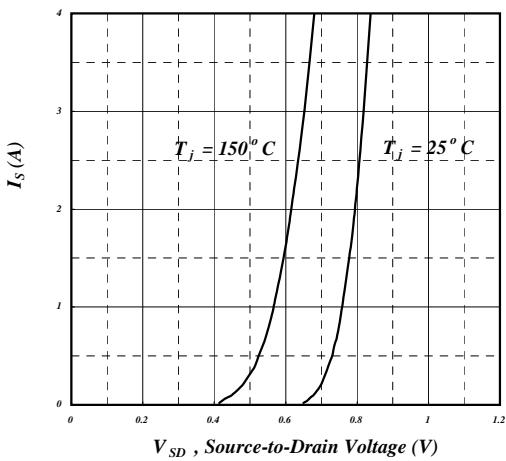


Fig 5. Forward Characteristic of Reverse Diode

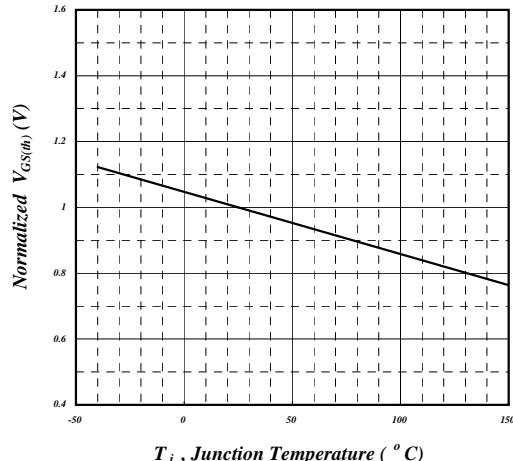


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

AP03N90P-HF

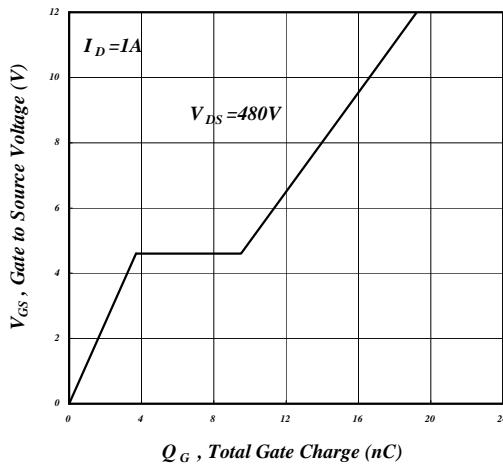


Fig 7. Gate Charge Characteristics

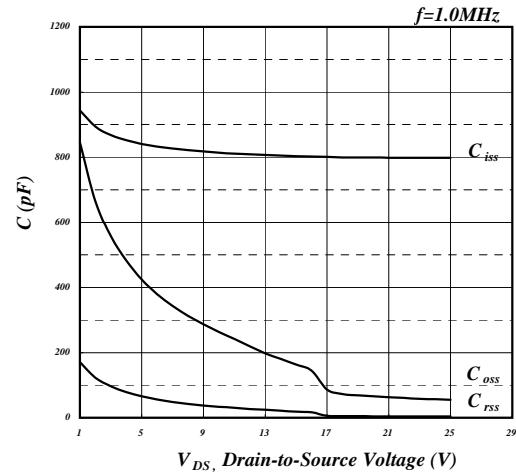


Fig 8. Typical Capacitance Characteristics

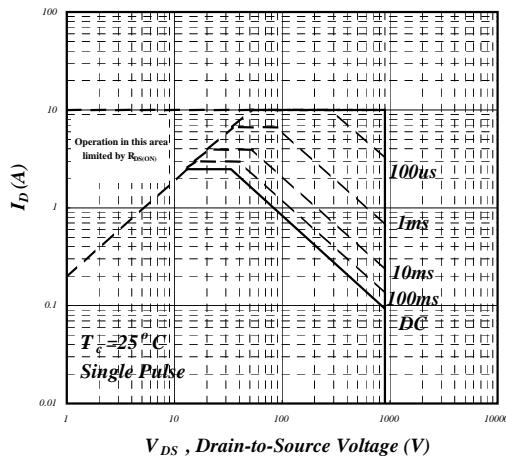


Fig 9. Maximum Safe Operating Area

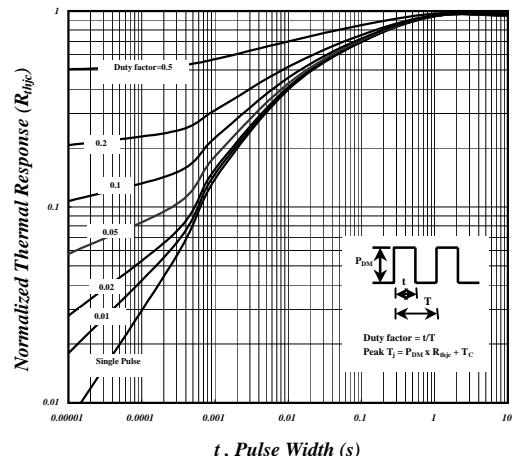


Fig 10. Effective Transient Thermal Impedance

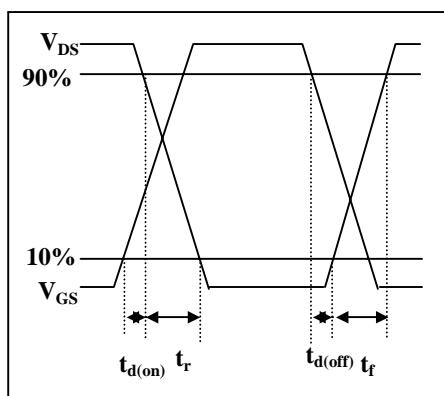


Fig 11. Switching Time Waveform

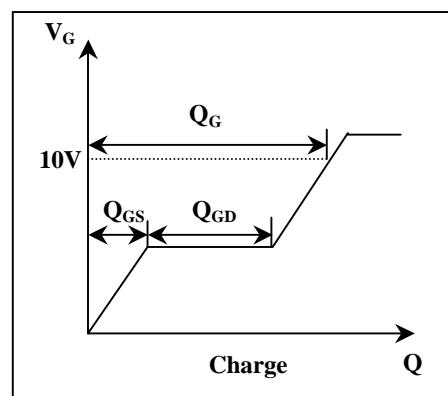


Fig 12. Gate Charge Waveform