

N-Channel 60 V (D-S) MOSFET

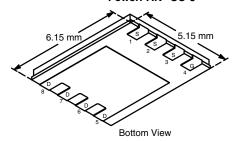
PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω) Max.	I _D (A) ^a	Q _g (Typ.)			
	0.0035 at V _{GS} = 10 V	60				
60	$0.0045 \text{ at V}_{GS} = 6 \text{ V}$	60	20.5 nC			
	0.0060 at V _{GS} = 4.5 V	60				

PowerPAK® SO-8

FEATURES

- TrenchFET® Power MOSFET
- 100 % R_g and UIS Tested
- Low Q_q for High Efficiency
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912



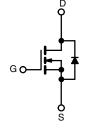


Ordering Information:

SiR688DP-T1-GE3 (Lead (Pb)-free and Halogen-free)

APPLICATIONS

- Primary Side Switch
- Synchronous Rectifier
- DC/DC Converter
- DC/AC Inverters



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	(T _A = 25 °C, unle	ess otherwise not	ed)	
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	v	
Continuous Drain Current (T _J = 150 °C)	$T_{C} = 25 ^{\circ}\text{C}$ $T_{C} = 70 ^{\circ}\text{C}$ $T_{A} = 25 ^{\circ}\text{C}$ $T_{A} = 70 ^{\circ}\text{C}$	I _D	60 ^a 60 ^a 29.2 ^{b, c} 23.2 ^{b, c}	
Pulsed Drain Current (t = 300 μs)	•	I _{DM}	100	A
Continuous Source-Drain Diode Current	$T_C = 25 ^{\circ}\text{C}$ $T_A = 25 ^{\circ}\text{C}$	I _S —	60 ^a 4.9 ^{b, c}	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	30	
Single Pulse Avalanche Energy		E _{AS}	45	mJ
Maximum Power Dissipation	$T_{C} = 25 ^{\circ}\text{C}$ $T_{C} = 70 ^{\circ}\text{C}$ $T_{A} = 25 ^{\circ}\text{C}$ $T_{A} = 70 ^{\circ}\text{C}$	P _D	83 53 5.4 ^{b, c} 3.4 ^{b, c}	w
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	- 55 to 150	00	
Soldering Recommendations (Peak Temperature		260	- °C	

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, f}	t ≤ 10 s	R _{thJA}	18	23	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R_{thJC}	1	1.5	0/11	

Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.
- d. See solder profile (www.vishay.com/doc?73257). The PowerPAK SO-8 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- e. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.
- f. Maximum under steady state conditions is 65 °C/W.



SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static					•		
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	60			V	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 6		mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1.3		2.7	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zoro Coto Voltago Droin Current	_	V _{DS} = 60 V, V _{GS} = 0 V			1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α	
		V _{GS} = 10 V, I _D = 20 A		0.0029	0.0035	1	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 6 \text{ V}, I_D = 15 \text{ A}$		0.0035	0.0045	Ω	
		$V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$		0.0043	0.0060		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 20 \text{ A}$		70		S	
Dynamic ^b							
Input Capacitance	C _{iss}			3105			
Output Capacitance	C _{oss}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		1345		pF	
Reverse Transfer Capacitance	C _{rss}			95			
Total Gate Charge	Qg	$V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 10 \text{ A}$		44	66		
Total Gate Charge				20.5	31	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = 30 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 10 \text{ A}$		8.6		110	
Gate-Drain Charge	Q_{gd}			4.7			
Gate Resistance	R_g	f = 1 MHz	0.4	2.2	4.4	Ω	
Turn-On Delay Time	t _{d(on)}			15	30		
Rise Time	t _r	V_{DD} = 30 V, R_L = 3 Ω		8	16]	
Turn-Off Delay Time	Off Delay Time $t_{d(off)}$ $I_D \cong 10$			31	60		
Fall Time	t _f			8	16] no	
Turn-On Delay Time	t _{d(on)}			43	85	ns	
Rise Time	t _r	$V_{DD} = 30 \text{ V}, R_L = 3 \Omega$		120	220		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ 10 A, V_{GEN} = 4.5 V, R_g = 1 Ω		32	60		
Fall Time	t _f			9	18		
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	$T_C = 25 ^{\circ}C$			60	Α	
Pulse Diode Forward Current ^a	I _{SM}				100		
Body Diode Voltage	V_{SD}	I _S = 5 A		0.74	1.1	V	
Body Diode Reverse Recovery Time	t _{rr}			43	86	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 10 A, dl/dt = 100 A/μs, T _J = 25 °C		37	74	nC	
Reverse Recovery Fall Time	t _a	$\frac{t_a}{t_b}$		17		ns	
Reverse Recovery Rise Time	t _b			26			

Notes:

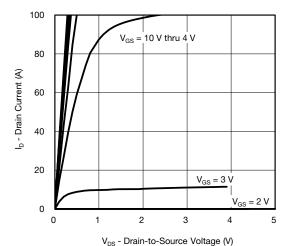
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

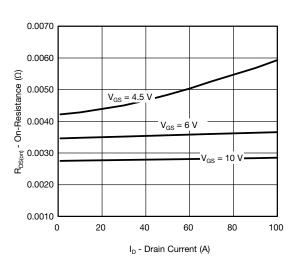
b. Guaranteed by design, not subject to production testing.



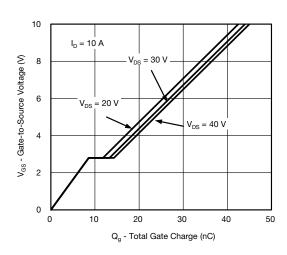
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



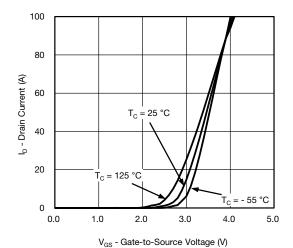
Output Characteristics



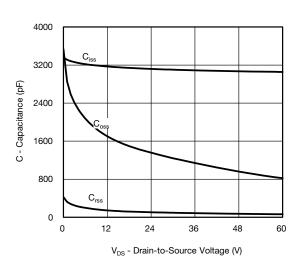
On-Resistance vs. Drain Current and Gate Voltage



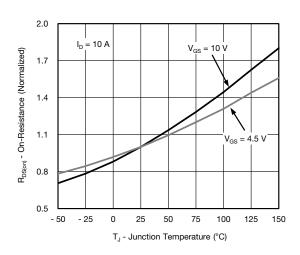
Gate Charge



Transfer Characteristics



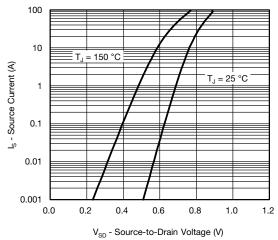
Capacitance



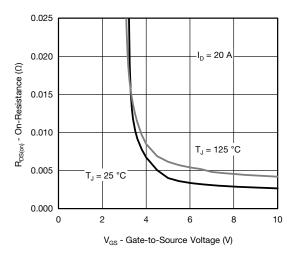
On-Resistance vs. Junction Temperature

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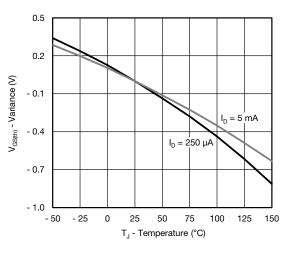
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



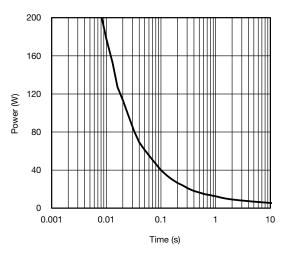
Source-Drain Diode Forward Voltage



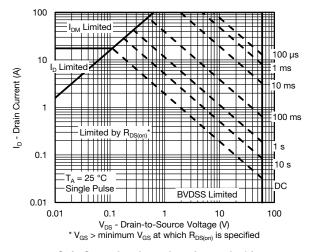
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



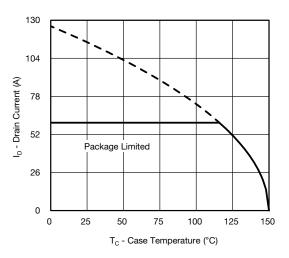
Single Pulse Power, Junction-to-Ambient



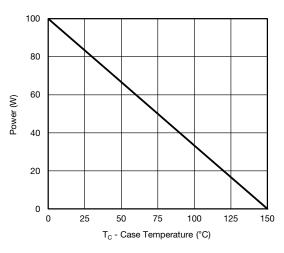
Safe Operating Area, Junction-to-Ambient



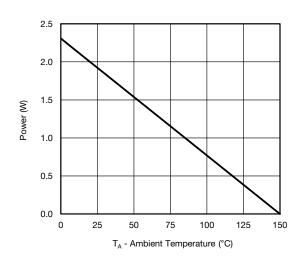
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating*





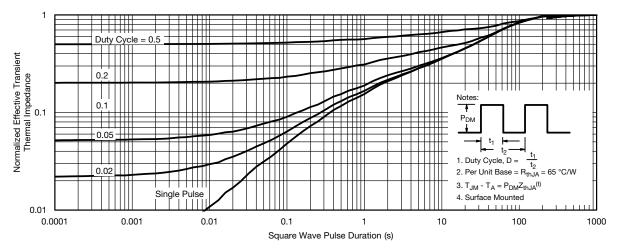


Power, Junction-to-Ambient

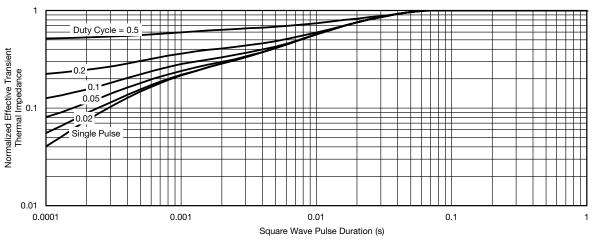
^{*} The power dissipation P_D is based on $T_{J(max.)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?63263.



DWG: 5881

PowerPAK® SO-8, (Single/Dual)



	3. Dimensions exclusive of mold flash and cutting burrs.							
DIM.		MILLIMETERS			INCHES			
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.		
A	0.97	1.04	1.12	0.038	0.041	0.044		
A1		-	0.05	0	-	0.002		
b	0.33	0.41	0.51	0.013	0.016	0.020		
С	0.23	0.28	0.33	0.009	0.011	0.013		
D	5.05	5.15	5.26	0.199	0.203	0.207		
	4.00	4.00	F 00	0.400	0.400	0.407		

Α	0.97	1.04	1.12	0.038	0.041	0.044	
A1		-	0.05	0	-	0.002	
b	0.33	0.41	0.51	0.013	0.016	0.020	
С	0.23	0.28	0.33	0.009	0.011	0.013	
D	5.05	5.15	5.26	0.199	0.203	0.207	
D1	4.80	4.90	5.00	0.189	0.193	0.197	
D2	3.56	3.76	3.91	0.140	0.148	0.154	
D3	1.32	1.50	1.68	0.052	0.059	0.066	
D4		0.57 typ.			0.0225 typ.		
D5		3.98 typ.		0.157 typ.			
E	6.05	6.15	6.25	0.238	0.242	0.246	
E1	5.79	5.89	5.99	0.228	0.232	0.236	
E2 (for AL product)	3.30	3.48	3.66	0.130	0.137	0.144	
E2 (for other product)	3.48	3.66	3.84	0.137	0.144	0.151	
E3	3.68	3.78	3.91	0.145	0.149	0.154	
E4 (for AL product)		0.58 typ.		0.023 typ.			
E4 (for other product)		0.75 typ.			0.030 typ.		
е		1.27 BSC		0.050 BSC			
K (for AL product)		1.45 typ.			0.057 typ.		
K (for other product)		1.27 typ.			0.050 typ.		
K1	0.56	-	=	0.022	-	=	
Н	0.51	0.61	0.71	0.020	0.024	0.028	
L	0.51	0.61	0.71	0.020	0.024	0.028	
L1	0.06	0.13	0.20	0.002	0.005	0.008	
θ	0°	-	12°	0°	-	12°	
W	0.15	0.25	0.36	0.006	0.010	0.014	
M	0.125 typ.			0.005 typ.			
ECN: C13-0702-Rev. K, 20)-May-13			•			

Revison: 20-May-13 Document Number: 71655



RECOMMENDED MINIMUM PADS FOR PowerPAK® SO-8 Single



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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