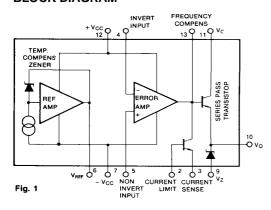
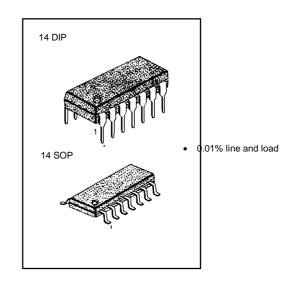
## PRECISION VOLTAGE REGULATOR

## **FEATURES**

- Positive or Negative Supply Operation. regulation
- Output voltage adjustable from 2 to 37 volts.
- Output current to 150mA without external pass transistor

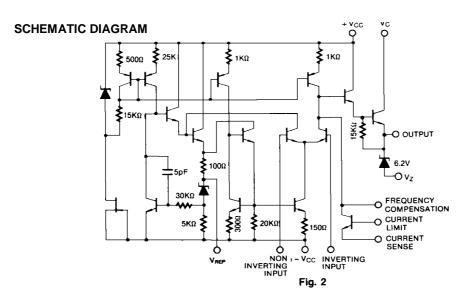
## **BLOCK DIAGRAM**





## **ORDERING INFORMATION**

Device	Package	Operating Temperature
KA723	14 DIP	0 ~ +70 ℃
KA723D	14 SOP	0~+/00
KA723I	14 DIP	-25 ~ +85 ℃
KA723ID	14 SOP	-25 ~ +05 (





# **ABSOLUTE MAXIMUM RATINGS**

Characteristic		Symbol	Value	Unit
Pulse Voltage from V + to V - (50ms)		$V_{I(P)}$	50	$V_{PEAK}$
Continuous Voltage from V + to V -		Vi	40	V
Input-Output Voltage Differential		V <sub>I</sub> - V <sub>O</sub>	40	V
Maximum Output Current		Ιο	150	mA
Differential Input Voltage		V <sub>ID</sub>	±5	V
Voltage Between Non-Inverting Input and V —		V <sub>IE</sub>	8	V
Current from V <sub>Z</sub>		Iz	25	mA
Current from V <sub>REF</sub>		I <sub>REF</sub>	15	mA
Power Dissipation		P <sub>D</sub>	1000	m/W
Operating Temperature Range	KA723	_	0 ~ +70	$^{\circ}$
	KA723I	$T_OPR$	-25 ~ +85	$^{\circ}$
Storage Temperature Range		T <sub>STG</sub>	-65 ~ + 150	$^{\circ}$

# **ELECTRICAL CHARACTERISTICS**

(unless otherwise specified,  $T_A$  = 25,  $^{\circ}$ C,  $V_I$  =  $V_{CC}$  = 12V,  $V_O$  = +5V,  $I_L$  =1 .0mA,  $R_{SC}$  = 0,  $C_I$  = 100pF,  $C_{REF}$ =0 and devider impedance as seen by error Amplifier  $\leq$  10K $\Omega$  connected as shown in figure 3)

Characteristic	Symbol	Test Conditions	Min	Тур	Max	Unit	
	ΔVo	V <sub>I</sub> = 12V to 15V		0.01	0.1	- %	
1. 5 1.		$V_1 = 12V \text{ to } 40V$		0.1	0.5		
Line Regulation		$T_{MIN} \le T_A \le T_{MAX}$			0.3		
		$V_1 = 12V \text{ to } 15V$					
	⊿Vo	$I_O = 1mA$ to $50mA$		0.03	0.2	%	
Load Regulation		$T_{MIN} \le T \le T_{MAX}$			0.6		
		$I_0 = 1 \text{ to } 50\text{mA}$					
Ripple Rejection	RR	$f = 100Hz$ to $10KHz$ , $C_{REF} = 0$		74		٩D	
Rippie Rejection		f = 100Hz to 10KHz, $C_{REF}$ =5 $\mu$ F		86		dB	
Average Temperature Coefficient of	⊿V₀/⊿T	$T_{MIN} \le T \le T_{MAX}$		0.003	0.015	%/℃	
Output Voltage	2 00/2 1	I MIN⊸ I ⊸ I MAX		0.003	0.013	767 C	
Short Circuit Current Limit	I <sub>SC</sub>	$R_{SC} = 10 \Omega$ , $V_{O} = 0$		65		mA	
Reference Voltage	$V_{REF}$		6.80	7.15	7.50	V	
0	V <sub>N</sub>	$f = 100Hz$ to $10KHz$ , $C_{REF} = 0$		20		μV <sub>rms</sub>	
Output Noise Voltage		f = 100Hz to 10KHz, $C_{REF}$ =5 $\mu$ F		2.5		μ <b>v</b> rms	
Long-term Stability	ST			0.1		%/ 1000HR	
Standby Current Drain	I <sub>D</sub>	$I_{L} = 0, \ V_{I} = 30V$		2.0	4.0	mA	
Input Voltage Range	VI		9.5		40	V	
Output Voltage Range	Vo		2.0		37	V	
Input-Output Voltage Differential	$V_D$		3.0		38	V	

\* Note: T<sub>MIN</sub> = 0 °C for KA723 = -25 °C for KA723I

 $T_{MAX} = 70 \,^{\circ}\mathbb{C}$  for KA723 = 85  $^{\circ}\mathbb{C}$  for KA723I



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E<sup>2</sup>CMOS<sup>™</sup> PowerTrench<sup>™</sup>

FACT<sup>TM</sup> QS<sup>TM</sup>

 $\begin{array}{lll} \mathsf{FACT} \ \mathsf{Quiet} \ \mathsf{Series^{\mathsf{TM}}} & \mathsf{Quiet} \ \mathsf{Series^{\mathsf{TM}}} \\ \mathsf{FAST}^{\circledast} & \mathsf{Super} \mathsf{SOT^{\mathsf{TM}}}\text{--3} \\ \mathsf{FASTr^{\mathsf{TM}}} & \mathsf{Super} \mathsf{SOT^{\mathsf{TM}}}\text{--6} \\ \mathsf{GTO^{\mathsf{TM}}} & \mathsf{Super} \mathsf{SOT^{\mathsf{TM}}}\text{--8} \\ \mathsf{Hi} \mathsf{SeC^{\mathsf{TM}}} & \mathsf{TinyLogic^{\mathsf{TM}}} \\ \end{array}$ 

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