

### FEATURES

- Extensional mode resonator, 760kHz to 1.35MHz
- Designed for low power applications
- Ideal microprocessor clock crystal
- Low ageing
- Full military testing available



### DESCRIPTION

CX2SM crystals consist of a high quality extensional mode resonator in a rugged, hermetically sealed ceramic package.

### SPECIFICATION

Specifications stated are typical at 25°C unless otherwise indicated. Specifications may change without notice.

Frequency Range:	760.0kHz to 1.35MHz
Standard Calibration Tolerance*:	±500ppm (0.05%) ±1000ppm (0.1%) ±10000ppm (1.0%)
Load Capacitance:	7pF
Motional Resistance (R1):	5kΩ maximum
Motional Capacitance (C1):	1.2fF
Quality Factor (Q):	150k
Shunt Capacitance (C0):	1.0pF
Drive Level:	3μW maximum
Turning Point (T0**):	35°C
Temperature Coefficient (k):	-0.035ppm/°C²
Ageing First Year:	±5ppm maximum
Shock, Survival:	1000g, 0.3ms, ½ sine
Vibration, Survival:	10g rms, 20~1000Hz random
Operating Temperature Range:	-10°C to +70°C (Commercial) -40°C to +85°C (Industrial) -55°C to +125°C (Military)
Storage Temperature Range:	-55° to +125°C
Maximum Process Temperature:	+260°C for 20 seconds

\* Tighter frequency calibration is available.

\*\* Other turning point is available.

### Turning Point Temperature

Note: Frequency f at temperature T is related to frequency fo at turning point temperature To by:

$$\frac{f-f_0}{f_0} = k(T-T_0)^2$$

### HOW TO ORDER CX2SM CRYSTALS

**CX2 - S - C - SM1 - 1.0M , 500 / I**

'S' if special, custom design. Otherwise leave blank

Blank = glass lid  
C = ceramic lid

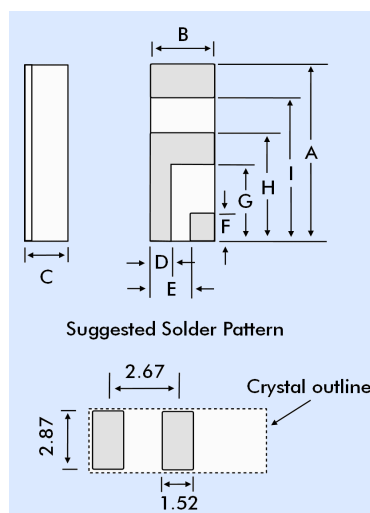
Terminations  
SM1 = Gold plated \*  
SM2 = Solder plated  
SM3 = Solder dipped  
SM4 = Solder plated \*  
SM5 = Solder dipped \*  
\* = Lead free

Frequency  
K = kHz  
M = MHz

Calibration Tolerance @25°C (in ppm)

Temp. Range  
C = -10° ~ +70°C  
I = -40° ~ +85°C  
M = -55° ~ +125°C  
S = Customer specified

### OUTLINE & DIMENSIONS



Dim.	Typ.	Max.
A	6.60	6.99
B	2.39	2.74
C	see below	
D	0.89	1.14
E	1.50	1.75
F	1.27	1.52
G	2.67	2.92
H	3.94	4.19
I	5.33	5.59

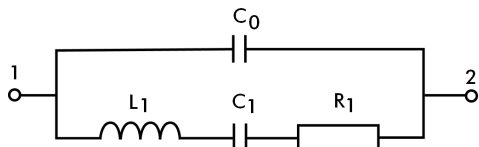
Dim. C	Glass Lid	Ceramic Lid
SM1	1.65	1.91
SM2	1.70	1.96
SM3	1.78	2.03
SM4	1.70	1.96
SM5	1.78	2.03

### PACKAGING OPTIONS

CX2SM crystals are available either tray packed (<250pcs) or tape and reel (>250 pieces).

16mm tape, 178mm or 330mm reels (EIA 418).

**CRYSTAL EQUIVALENT CIRCUIT**



R1 Motional Resistance      L1 Motional Inductance  
 C1 Motional Capacitance    C0 Shunt Capacitance

**TYPICAL APPLICATION FOR A PIERCE OSCILLATOR**

The low profile CX miniature crystal is ideal for use in small, high density, battery operated portable products. The CX crystal designed in a Pierce oscillator (single inverter) circuit provides very low current consumption and high stability. A conventional Pierce oscillator is shown above. The crystal is effectively inductive and in a Pi network circuit with C<sub>D</sub> and C<sub>G</sub> provides the additional phase shift to sustain oscillation. The oscillation frequency (f<sub>o</sub>) is 15 to 250ppm above the crystal's resonant frequency (f<sub>s</sub>).

**Drive Level**

R<sub>A</sub> is used to limit the crystal's drive level by forming a voltage divider between R<sub>A</sub> and C<sub>D</sub>. R<sub>A</sub> also stabilizes the oscillator against changes in the amplifier's output resistance (R<sub>O</sub>). R<sub>A</sub> should be increased for higher voltage operation.

**Load Capacitance**

The CX crystal calibration tolerance is influenced by the effective circuit capacitances, specified as the load capacitance (C<sub>L</sub>). C<sub>L</sub> is approximately equal to:

$$C_L = \frac{C_D \times C_G}{C_D + C_G} + C_S$$

Note: C<sub>D</sub> and C<sub>G</sub> include stray layout-induced capacitance to ground and C<sub>S</sub> is the stray shunt capacitance between the crystal terminal. In practice, the effective value of C<sub>L</sub> will be less than that calculated from C<sub>D</sub>, C<sub>G</sub> and C<sub>S</sub> values because of the effect of the amplifier output resistance. C<sub>S</sub> should be minimized.

The oscillation frequency (f<sub>o</sub>) is approximately equal to:

$$f_o = f_s \left[ 1 + \frac{C_1}{2(C_o + C_L)} \right]$$

Where

- F<sub>s</sub> = Series resonant frequency of the crystal
- C<sub>1</sub> = Motional Capacitance
- C<sub>o</sub> = Shunt Capacitance

**TERMINATIONS - PLATING**

Designation	Termination
SM1	Gold Plated (Lead Free)
SM2	Solder Plated
SM3	Solder Dipped
SM4	Solder Plated (Lead Free)
SM5	Solder Dipped (Lead Free)

**Turning Point Temperature**

Note: Frequency f at temperature T is related to frequency F<sub>0</sub> at turning point temperature T<sub>0</sub> by:

$$\frac{f-f_o}{f_o} = k(T-T_o)^2$$