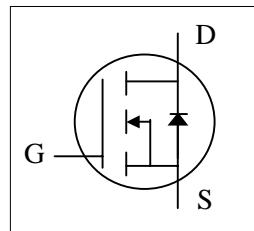
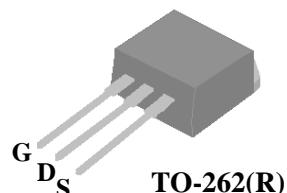
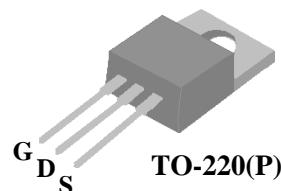




- ▼ 100% Avalanche Rated
- ▼ Fast Switching Characteristics
- ▼ Simple Drive Requirement



$BV_{DSS}$	700V
$R_{DS(ON)}$	0.85Ω
$I_D$	8.3A



## Description

AP09N70 series are specially designed as main switching devices for universal 90~265VAC off-line AC/DC converter applications. The TO-262 type provide high blocking voltage to overcome voltage surge and sag in the toughest power system with the best combination of fast switching, ruggedized design and cost-effectiveness.

The TO-220 and TO-262 package is widely preferred for all commercial-industrial applications. The device is suited for switch mode power supplies, DC-AC converters and high current high speed switching circuits.

## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	700	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	8.3	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	5.2	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	40	A
$P_D @ T_C = 25^\circ C$	Total Power Dissipation	156	W
$E_{AS}$	Single Pulse Avalanche Energy <sup>2</sup>	32	mJ
$I_{AR}$	Avalanche Current	8	A
$E_{AR}$	Repetitive Avalanche Energy	32	mJ
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Value	Unit
$R_{thj-c}$	Maximum Thermal Resistance, Junction-case	0.8	°C/W
$R_{thj-a}$	Maximum Thermal Resistance, Junction-ambient	62	°C/W



### Electrical Characteristics@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	700	-	-	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance <sup>3</sup>	$V_{\text{GS}}=10\text{V}$ , $I_D=4\text{A}$	-	-	0.85	$\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$ , $I_D=250\mu\text{A}$	2	-	4	V
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_D=4.5\text{A}$	-	4.5	-	S
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=600\text{V}$ , $V_{\text{GS}}=0\text{V}$	-	-	25	$\mu\text{A}$
	Drain-Source Leakage Current ( $T_j=125^\circ\text{C}$ )	$V_{\text{DS}}=480\text{V}$ , $V_{\text{GS}}=0\text{V}$	-	-	500	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage	$V_{\text{GS}}=\pm 30\text{V}$ , $V_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	nA
$Q_g$	Total Gate Charge <sup>3</sup>	$I_D=9\text{A}$	-	44	-	nC
$Q_{\text{gs}}$	Gate-Source Charge	$V_{\text{DS}}=480\text{V}$	-	11	-	nC
$Q_{\text{gd}}$	Gate-Drain ("Miller") Charge	$V_{\text{GS}}=10\text{V}$	-	12	-	nC
$t_{\text{d(on)}}$	Turn-on Delay Time <sup>3</sup>	$V_{\text{DD}}=300\text{V}$	-	19	-	ns
$t_r$	Rise Time	$I_D=9\text{A}$	-	21	-	ns
$t_{\text{d(off)}}$	Turn-off Delay Time	$R_G=10\Omega$ , $V_{\text{GS}}=10\text{V}$	-	56	-	ns
$t_f$	Fall Time	$R_D=34\Omega$	-	24	-	ns
$C_{\text{iss}}$	Input Capacitance	$V_{\text{GS}}=0\text{V}$	-	2660	-	pF
$C_{\text{oss}}$	Output Capacitance	$V_{\text{DS}}=25\text{V}$	-	170	-	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance	f=1.0MHz	-	10	-	pF

### Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$I_S$	Continuous Source Current ( Body Diode )	$V_D=V_G=0\text{V}$ , $V_S=1.5\text{V}$	-	-	8.3	A
$I_{\text{SM}}$	Pulsed Source Current ( Body Diode ) <sup>1</sup>		-	-	40	A
$V_{\text{SD}}$	Forward On Voltage <sup>3</sup>	$T_j=25^\circ\text{C}$ , $I_S=9\text{A}$ , $V_{\text{GS}}=0\text{V}$	-	-	1.5	V

#### Notes:

- 1.Pulse width limited by Maximum junction temperature.
- 2.Starting  $T_j=25^\circ\text{C}$  ,  $V_{\text{DD}}=50\text{V}$  ,  $L=1\text{mH}$  ,  $R_G=25\Omega$  ,  $I_{\text{AS}}=8\text{A}$ .
- 3.Pulse test

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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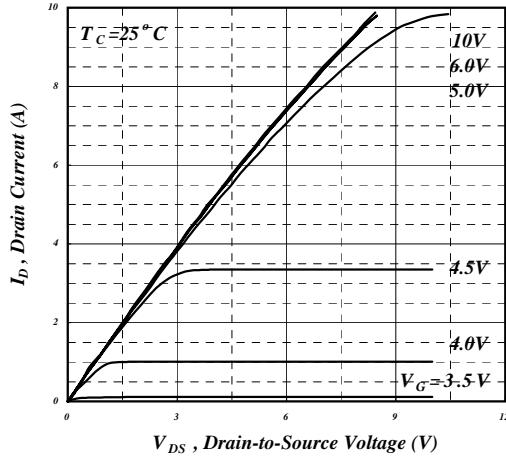


Fig 1. Typical Output Characteristics

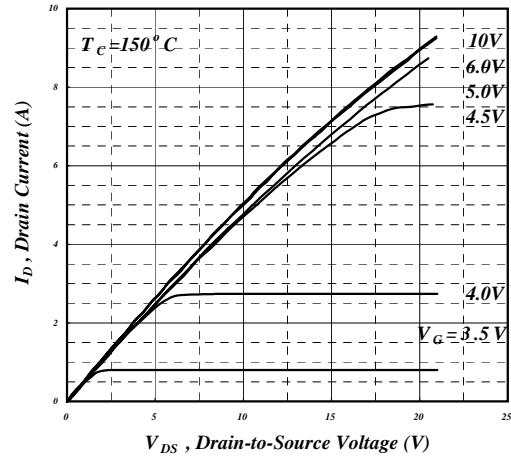


Fig 2. Typical Output Characteristics

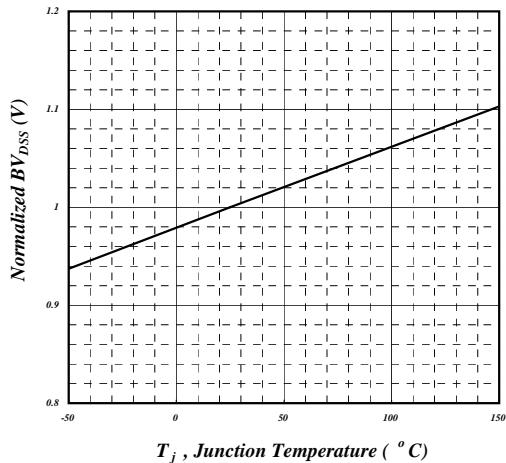
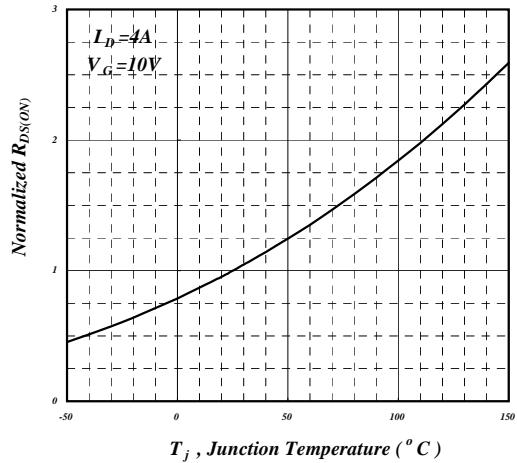
Fig 3. Normalized  $BV_{DSS}$  v.s. Junction Temperature

Fig 4. Normalized On-Resistance v.s. Junction Temperature

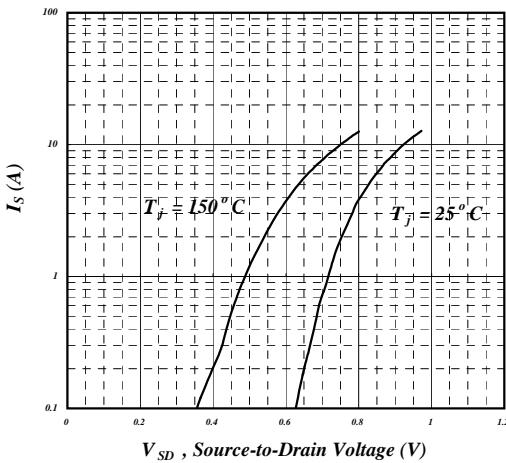


Fig 5. Forward Characteristic of Reverse Diode

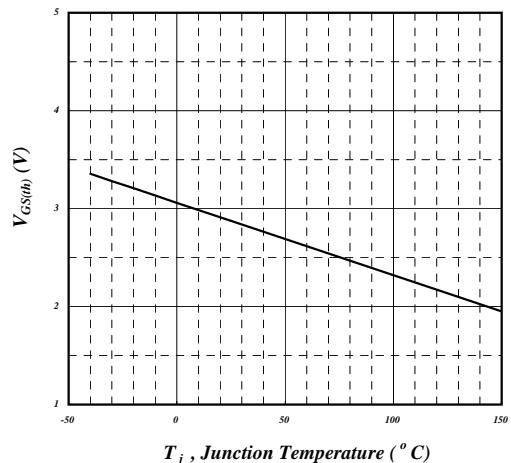


Fig 6. Gate Threshold Voltage v.s. Junction Temperature



## AP09N70P/R-H

