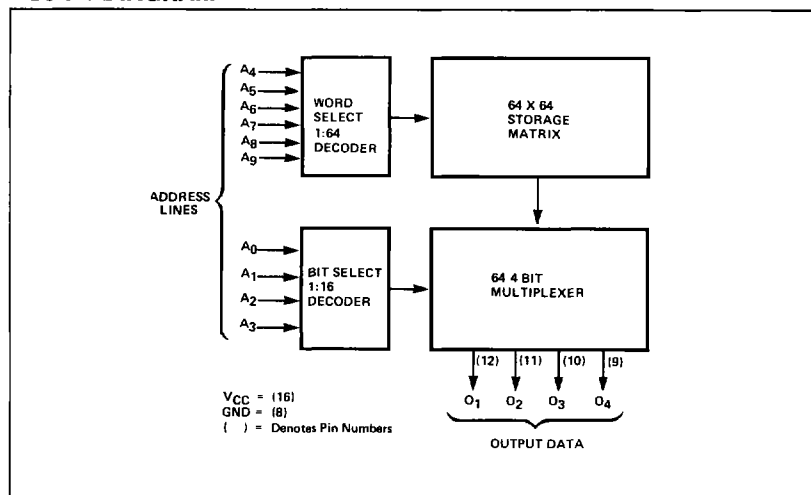
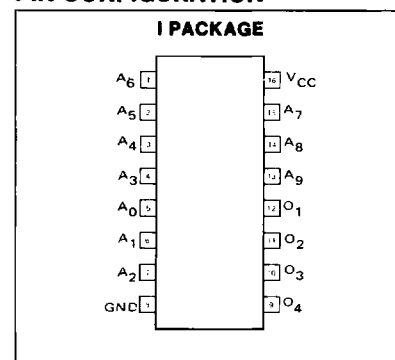


BLOCK DIAGRAM



PIN CONFIGURATION

ELECTRICAL CHARACTERISTICS $0 \leq T_A \leq 75^\circ\text{C}$, $4.75 \leq V_{CC} \leq 5.25\text{V}$

PARAMETERS	LIMITS			UNITS
	MIN	TYP	MAX	
Access Time—Address to Output		50	70	ns

NOTES:

1. Positive current is defined as into the terminal referenced.
2. No more than one output should be grounded at the same time.
3. Manufacturer reserves the right to make design and process changes and improvements.
4. Applied voltages must not exceed 5.0V. Input currents must not exceed $\pm 30\text{mA}$. Output currents must not exceed $\pm 100\text{mA}$. Storage temperature must be between -60°C to $+150^\circ\text{C}$.
5. Rise and fall time for this test must be less than 5ns. Input amplitudes are 2.8V and all measurements are made at 1.5V.

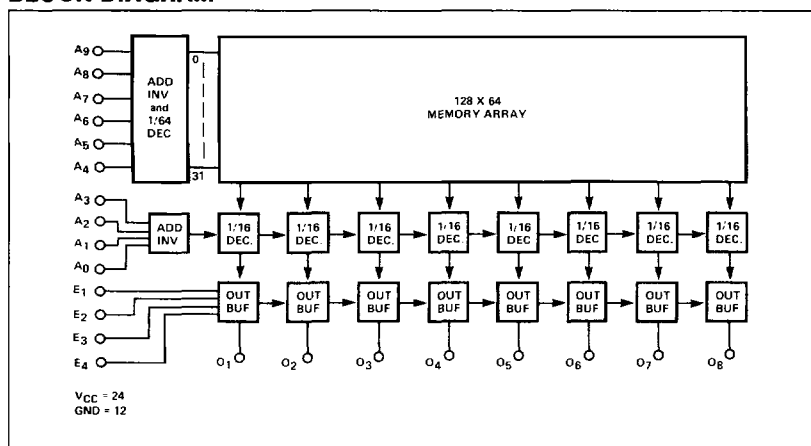
8192-BIT BIPOLAR ROM (1024x8)

82S280/281

N82S280/281-I • S82S280/281-I

OBJECTIVE SPECIFICATION

BLOCK DIAGRAM



PIN CONFIGURATION

