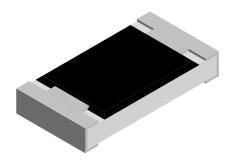
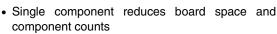


# Thick Film Resistor/Capacitor Chip, Surface Mount



#### **FEATURES**





• Choice of dielectric characteristics X7R or Y5U



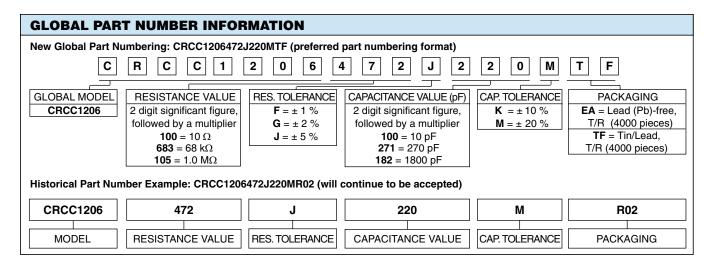
- · Wrap around termination
- Thick film resistor/capacitor element
- Inner electrode protection
- Flow and reflow solderable
- Automatic placement capability, standard size

STANDARD ELECTRICAL SPECIFICATIONS												
	SIZE		RE	SISTOR CHARAC	TERIS	TICS		CAPACITOR CHARACTERISTICS				
GLOBAL MODEL	INCH	METRIC	POWER RATING P <sub>70</sub> °C W	TEMPERATURE COEFFICIENT ± ppm/°C	RES. TOL. ± %	$\begin{array}{c} \textbf{RES.} \\ \textbf{RANGE} \\ \Omega \end{array}$	DIELECTRIC	TEMPERATURE COEFFICIENT %	CAP. TOL. ± %	CAP. VOLTAGE V <sub>DC</sub>	CAP. RANGE pF	
CRCC1206	1206	3216	0.125	200	5	10 to 1M	X7R	± 15	20	50	10 to 270	
CRCC1206	1206	3216	0.125	200	5	10 to 1M	Y5U	+ 22, - 56	20	50	270 to 1800	
• Technolog	tempe	rature ran k film	ge: - 55 °(	C to + 125 °C				CITOR  'ating temperature range: X7R - 55 °C to + 125 °C  Y5U - 30 °C to + 85 °C  mum dissipation factor: 2.5 %				

#### **Notes**

- Packaging: See appropriate catalog or web page.
- Power rating depends on the maximum temperature at the solder point, the component placement density and the substrate material.

TECHNICAL SPECIFICATIONS									
PARAMETER	UNIT	RESISTOR	X7R CAPACITOR	Y5U CAPACITOR					
Rated dissipation at 70 °C	W	0.125	-	-					
Capacitor voltage rating	V	-	50	50					
Dielectric withstanding voltage (5 s, 50 mA Charge)	V <sub>DC</sub>	-	125	125					
Category temperature range	°C	- 55/+ 125	- 55/+ 125	- 30/+ 85					
Insulation resistance	Ω	> 10 <sup>10</sup>	> 10 <sup>10</sup>	> 10 <sup>10</sup>					
Weight/1000 pieces	g	0.65	2	5.5					

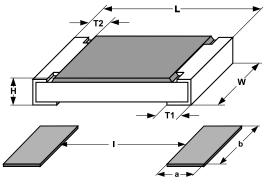


Vishay Dale

# Thick Film Resistor/Capacitor Chip, Surface Mount



### **DIMENSIONS**



	120	_												
<b>Rated Power in %</b> 80									 					
оме	100	H			+			+	┪				+	
ted P	80	Ц							1	\				
Ba														
	60	H			$\dagger$			+	Ť	1			$\dagger$	
	40	Ц							<u> </u>		\			
	20													
		$\prod$							T					
	0 - 5	<u>⊔</u> 55	- 2	<u> </u>	0	2	 5	50	İ	75 10	00 1	25 <sup>-</sup>	150 17	<b>]</b> 75
D	Derating 70 Ambient Temperature in °C													

	SIZE	DIMENSIONS in millimeters						
INCH	METRIC	L	W	Н	T1	T2		
1206	3216	3.2 ±	1.6 ±	0.55 ±	0.5 ±	0.5 ±		
1206	3210	0.15	0.15	0.15	0.25	0.25		

	SIZE		SOLDER PAD DIMENSIONS in millimeters							
	DIZE	REFLO	W SOL	DERING	WAVE SOLDERING					
INCH	METRIC	а	b	ı	а	b	ı			
1206	3216	0.9	1.7	2.0	1.1	1.7	2.2			

#### **SCHEMATIC**



PERFORMANCE								
TEST	CONDITIONS OF TEST	TEST RESULTS (T	TEST RESULTS (TYPICAL TEST LOTS)					
1231	CONDITIONS OF TEST	R	С					
Endurance test at 70 °C MIL-Std-202 method 108	1000 h at 70 °C, 1.5 h "ON", 0.5 h "OFF"	± (5 % + 2 Ω)	± 20 %					
Dielectric withstanding voltage MIL-Std-202 method 301	125 V <sub>DC</sub> , 5 s, 50 mA charge	No physic	No physical damage					
Thermal shock MIL-Std-202 method 107	100 cycles, - 55 °C to + 125 °C	± (5 % + 2 Ω)	± 20 %					
Moisture MIL-Std-202 method 106	Omit steps 7A and B	± (5 % + 2 Ω)	± 20 %					
Resistance to soldering heat EIA 575	10 s at 260 °C solder bath temperature	± (5 % + 2 Ω)	± 20 %					
High temperature exposure EIA 575	125 °C for 100 h	± (5 % + 2 Ω)	± 20 %					
Low temperature operation EIA 575	1 h at - 55 °C then 45 min at 50 V	± (5 % + 2 Ω)	± 20 %					
Solderability and leaching EIA 575 3.12	Condition C	95 % coverage						

### **APPLICABLE SPECIFICATIONS**

- IPC standards
- EIA 575

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