



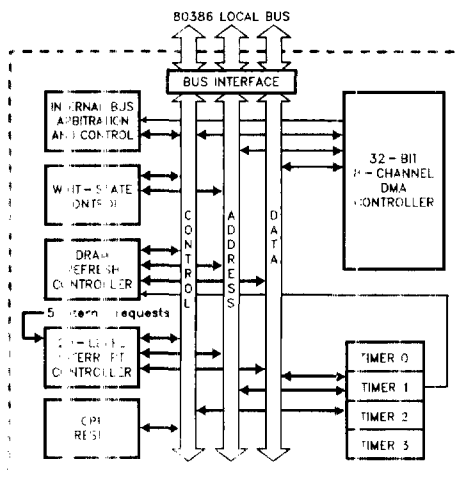
82380

HIGH PERFORMANCE 32-BIT DMA CONTROLLER WITH INTEGRATED SYSTEM SUPPORT PERIPHERALS

- **High Performance 32-Bit DMA Controller**
 - 50 MBytes/sec Maximum Data Transfer Rate at 25 MHz
 - 8 Independently Programmable Channels
- **20-Source Interrupt Controller**
 - Individually Programmable Interrupt Vectors
 - 15 External, 5 Internal Interrupts
 - 82C59A Superset
- **Four 16-Bit Programmable Interval Timers**
 - 82C54 Compatible
- **Programmable Wait State Generator**
 - 0 to 15 Wait States Pipelined
 - 1 to 16 Wait States Non-Pipelined
- **DRAM Refresh Controller**
- **80386 Shutdown Detect and Reset Control**
 - Software/Hardware Reset
- **High Speed CMOS III Technology**
- **132-Pin PGA Package**
(See Packaging Handbook Order 240800-001, Package Type A)
- **Optimized for use with the 80386 Microprocessor**
 - Resides on Local Bus for Maximum Bus Bandwidth
 - 16, 20, and 25 MHz Clock

The 82380 is a multi-function support peripheral that integrates system functions necessary in an 80386 environment. It has eight channels of high performance 32-bit DMA with the most efficient transfer rates possible on the 80386 bus. System support peripherals integrated into the 82380 provide Interrupt Control, Timers, Wait State generation, DRAM Refresh Control, and System Reset logic.

The 82380's DMA Controller can transfer data between devices of different data path widths using a single channel. Each DMA channel operates independently in any of several modes. Each channel has a temporary data storage register for handling non-aligned data without the need for external alignment logic.



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82380 Internal Block Diagram

The complete document for this product can be ordered by calling 1-800-548-4725. It is also available on Intel's "Data-on-Demand" CD-ROM product; contact your local Intel field sales office or Intel technical distributor.