

# **ASM3P2863A**

# Low Power Peak EMI Reducing Solution

### **Features**

- Generates an EMI optimized clock signal at the output.
- Integrated loop filter components.
- Operates with a 3.3V / 2.5V supply.
- Operating current less than 4mA.
- Low power CMOS design.
- Input frequency: 12MHz
- Generates a 1X low EMI spread spectrum clock of the input frequency.
- Frequency deviation: ±0.4%(Typ) @ 12MHz Input Frequency
- Available in 6-pin TSOT-23, 8-pin SOIC and 8-pin TSSOP packages.

### **Product Description**

The ASM3P2863A is a versatile spread spectrum frequency modulator designed specifically for a wide range of clock frequencies. The ASM3P2863A reduces electromagnetic interference (EMI) at the clock source, allowing system wide reduction of EMI of all clock dependent signals. The ASM3P2863A allows significant system cost savings by reducing the number of circuit board layers, ferrite beads and shielding that are traditionally required to pass EMI regulations.

The ASM3P2863A uses the most efficient and optimized modulation profile approved by the FCC and is implemented by using a proprietary all digital method.

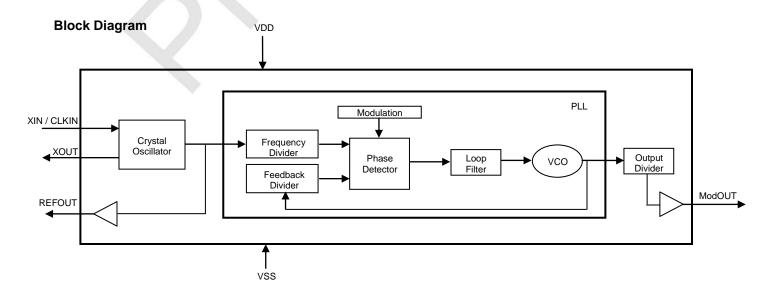
The ASM3P2863A modulates the output of a single PLL in order to "spread" the bandwidth of a synthesized clock, and more importantly, decreases the peak amplitudes of its harmonics. This results in significantly lower system EMI compared to the typical narrow band signal produced by oscillators and most frequency generators. Lowering EMI by increasing a signal's bandwidth is called 'spread spectrum clock generation.'

### **Applications**

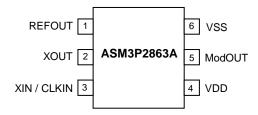
The ASM3P2863A is targeted towards all portable devices with very low power requirements like MP3 players, Notebooks and Digital still cameras.

**Key Specifications** 

Description	Specification
Supply voltages	$V_{DD} = 2.5 V / 3.3 V$
Cycle-to-Cycle Jitter	200pS ( typ)
Output Duty Cycle	45/55% (worst case)
Modulation Rate Equation	F <sub>IN</sub> /256
Frequency Deviation	±0.4 % (Typ) @ 12MHz



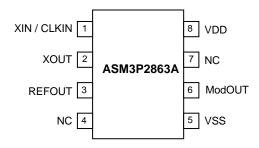
# Pin Configuration (6-pin TSOT-23 Package)



# **Pin Description**

20	0011ptio11		
Pin#	Pin Name	Туре	Description
1	REFOUT	0	Buffered output of the input frequency.
2	XOUT	0	Crystal connection. If using an external reference, this pin must be left unconnected.
3	XIN / CLKIN	I	Crystal connection or external reference frequency input. This pin has dual functions. It can be connected either to an external crystal or an external reference clock.
4	VDD	Р	Power supply for the entire chip.
5	ModOUT	0	Spread spectrum clock output.
6	VSS	Р	Ground connection.

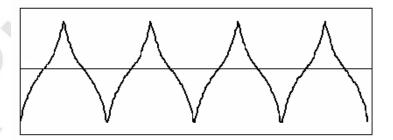
# Pin Configuration (8-pin SOIC and TSSOP Packages)



# **Pin Description**

Pin#	Pin Name	Туре	Description
1	XIN / CLKIN	I	Crystal connection or external reference frequency input. This pin has dual functions. It can be connected either to an external crystal or an external reference clock.
2	XOUT	0	Crystal connection. If using an external reference, this pin must be left unconnected.
3	REFOUT	0	Buffered output of the input frequency.
4	NC	-	No connect.
5	VSS	Р	Ground connection.
6	ModOUT	0	Spread spectrum clock output.
7	NC	-	No connect.
8	VDD	Р	Power supply for the entire chip.

## **Modulation Profile**



# **Specifications**

Description	Specification	
Input Frequency	12MHz	
Modulation Equation	F <sub>IN</sub> /256	
Frequency Deviation	±0.4% (Typ) @ 12MHz	

**Absolute Maximum Ratings** 

Symbol	Parameter	Rating	Unit
VDD, V <sub>IN</sub>	Voltage on any input pin with respect to Ground	-0.5 to +4.6	V
T <sub>STG</sub>	Storage temperature	-65 to +125	°C
Ts	Max. Soldering Temperature (10 sec)	260	°C
TJ	Junction Temperature	150	°C
$T_DV$	Static Discharge Voltage	2	KV
lote: These are s	(As per JEDEC STD22- A114-B) stress ratings only and are not implied for functional use. Exposure to absolute maximum ratings f	or prolonged periods of time	may affect

DC Electrical Characteristics for 2.5V Supply
(Test condition: All parameters are measured at room temperature (+25°C) unless otherwise stated.)

Symbol	Parameter	Min	Тур	Max	Unit
V <sub>IL</sub>	Input low voltage	VSS-0.3		0.8	V
V <sub>IH</sub>	Input high voltage	2.0		VDD+0.3	V
I <sub>IL</sub>	Input low current			-35	μΑ
I <sub>IH</sub>	Input high current			35	μΑ
I <sub>XOL</sub>	XOUT output low current (@ 0.5V, VDD = 2.5V)	_	3		mA
I <sub>XOH</sub>	XOUT output high current (@ 1.8V, VDD = 2.5V)		3		mA
V <sub>OL</sub>	Output low voltage (VDD = 2.5V, I <sub>OL</sub> = 8mA)	7		0.6	V
V <sub>OH</sub>	Output high voltage (VDD = 2.5V, I <sub>OH</sub> = 8mA)	1.8			V
I <sub>DD</sub>	Static supply current <sup>1</sup>		0.8		mA
I <sub>CC</sub>	Dynamic supply current (2.5V, 12MHz and no load)		3		mA
VDD	Operating voltage	2.375	2.5	2.625	V
t <sub>ON</sub>	Power-up time (first locked cycle after power-up)			5	mS
Z <sub>OUT</sub>	Output impedance		50		Ω
Note: 1. XIN /	CLKIN pin is pulled low.			•	•

AC Electrical Characteristics for 2.5V Supply

Symbol	l Parameter		Тур	Max	Unit
CLKIN	Input frequency		12		MHz
ModOUT	Output frequency		12		MHz
f <sub>d</sub>	Frequency Deviation		±0.4		%
t <sub>LH</sub> <sup>1</sup>	Output rise time (measured from 0.7V to 1.7V)	0.5	1.5	1.7	nS
t <sub>HL</sub> <sup>1</sup>	Output fall time (measured from 1.7V to 0.7V)	0.5	1.0	1.2	nS
t <sub>JC</sub>	Jitter (Cycle-to-Cycle)		200	300	pS
t <sub>D</sub>	Output duty cycle	45	50	55	%
Note: 1. t <sub>LH</sub> and t <sub>HL</sub> are m	easured into a capacitive load of 15pF.	<u> </u>			

DC Electrical Characteristics for 3.3V Supply (Test condition: All parameters are measured at room temperature (+ 25°C) unless otherwise stated.)

Symbol	Parameter	Min	Тур	Max	Unit
V <sub>IL</sub>	Input low voltage	VSS-0.3		0.8	V
V <sub>IH</sub>	Input high voltage	2.0		VDD+0.3	V
I <sub>IL</sub>	Input low current			-35	μA
I <sub>IH</sub>	Input high current			35	μA
I <sub>XOL</sub>	XOUT output low current (@ 0.4V, V <sub>DD</sub> = 3.3V)		3		mA
I <sub>XOH</sub>	XOUT output high current (@ 2.5V, V <sub>DD</sub> = 3.3V)		3		mA
$V_{OL}$	Output low voltage (VDD = 3.3V, I <sub>OL</sub> = 8mA)			0.4	V
$V_{OH}$	Output high voltage (VDD = 3.3V, I <sub>OH</sub> = 8mA)	2.5	1		V
$I_{DD}$	Static supply current <sup>1</sup>		1		mA
Icc	Dynamic supply current (3.3V, 12MHz and no load)		3.5		mA
VDD	Operating Voltage	3.0	3.3	3.6	V
t <sub>ON</sub>	Power-up time (first locked cycle after power-up)			5	mS
Z <sub>OUT</sub>	Output impedance		45		Ω
Note: 1. XIN	CLKIN pin is pulled low.			•	•

**AC Electrical Characteristics for 3.3V Supply** 

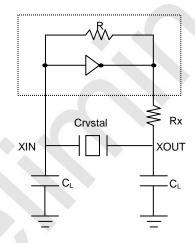
Symbol	Parameter	Min	Тур	Max	Unit
CLKIN	Input frequency		12		MHz
ModOUT	Output frequency		12		MHz
$f_d$	Frequency Deviation		±0.4		%
t <sub>LH</sub> <sup>1</sup>	Output rise time (measured from 0.8 to 2.0V)	0.5	1.4	1.6	nS
t <sub>HL</sub> 1	Output fall time (measured at 2.0V to 0.8V)	0.4	1.0	1.2	nS
t <sub>JC</sub>	Jitter (Cycle-to-Cycle)		200	300	pS
t <sub>D</sub>	Output duty cycle	45	50	55	%
te: 1. t <sub>LH</sub> and t <sub>HL</sub> are m	easured into a capacitive load of 15pF.			ı	

# **Crystal Specifications**

Fundamental AT cut parallel resonant crystal					
Nominal frequency	12MHz				
Frequency tolerance	±50ppm or better at 25°C				
Operating temperature range	-25°C to +85°C				
Storage temperature	-40°C to +85°C				
Load capacitance (C <sub>P</sub> )	18pF				
Shunt capacitance	7pF maximum				
ESR	$25\Omega$				

Note: C<sub>L</sub> is Load Capacitance and Rx is used to prevent oscillations at overtone frequency of the Fundamental frequency.

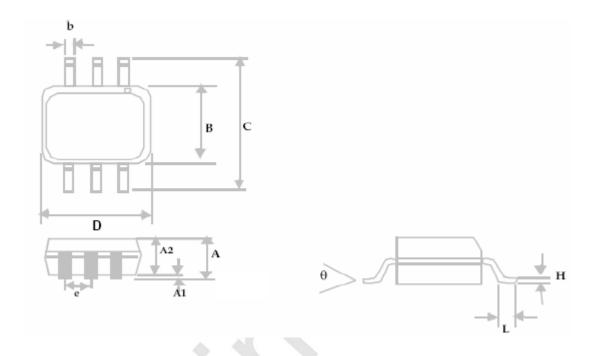
# **Typical Crystal Interface Circuit**



$$\begin{split} C_L &= 2^*(C_P - C_S), \\ \text{Where } C_P &= \text{Load capacitance of crystal from crystal vendor datasheet.} \\ C_S &= \text{Stray capacitance due to } C_{\text{IN},} \text{ PCB, Trace, etc.} \end{split}$$

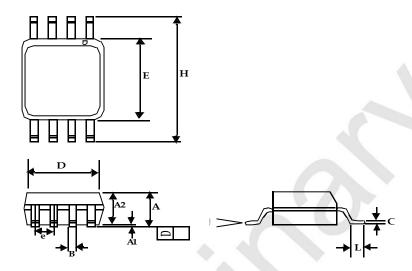
# **Package Information**

# 6-pin TSOT-23 Package



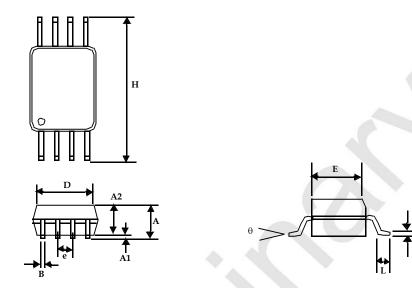
		Dimensions			
Symbol	Inches		Millim	neters	
	Min	Max	Min	Max	
Α		0.04		1.00	
A1	0.00	0.004	0.00	0.10	
A2	0.033	0.036	0.84	0.90	
b	0.012	0.02	0.30	0.50	
Н	0.005	BSC	0.127	BSC	
D	0.114	BSC	2.90	BSC	
В	0.06	BSC	1.60	BSC	
е	0.0374 BSC		0.950	BSC	
С	0.11 BSC 2.80 BSC		BSC		
L	0.0118	0.02	0.30	0.50	
θ	0°	4°	0°	4°	

# 8-Pin SOIC Package



	Dimensions			
Symbol	Inches			neters
	Min	Max	Min	Max
A1	0.004	0.010	0.10	0.25
Α	0.053	0.069	1.35	1.75
A2	0.049	0.059	1.25	1.50
В	0.012	0.020	0.31	0.51
С	0.007	0.010	0.18	0.25
D	0.193	BSC	4.90	BSC
E	0.154	BSC	3.91	BSC
е	0.050 BSC		1.27	BSC
Н	0.236 BSC 6.00 BSC		BSC	
L	0.016	0.050	0.41	1.27
θ	0°	8°	0° 8°	

# 8-Pin TSSOP Package

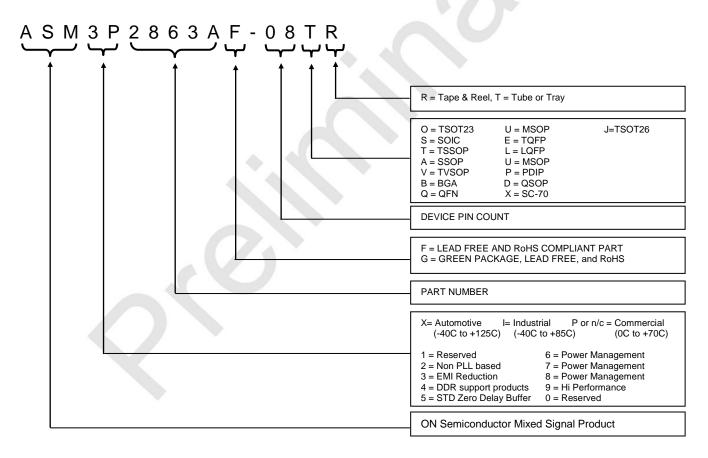


	Dimensions				
Symbol	Inches		Millimeters		
	Min	Max	Min	Max	
Α		0.043		1.10	
A1	0.002	0.006	0.05	0.15	
A2	0.033	0.037	0.85	0.95	
В	0.008	0.012	0.19	0.30	
С	0.004	0.008	0.09	0.20	
D	0.114	0.122	2.90	3.10	
E	0.169	0.177	4.30	4.50	
е	0.026 BSC		0.65 BSC		
Н	0.252 BSC		6.40 BSC		
L	0.020	0.028	0.50	0.70	
θ	0°	8°	0°	8°	

## **Ordering Information**

Part Number	Marking	Package Type	Temperature
ASM3P2863AF-06OR	V4LL	6-Pin TSOT-23, TAPE & REEL, Pb Free	Commercial
ASM3P2863AF-08TT	3P2863AF	8-Pin TSSOP, TUBE, Pb Free	Commercial
ASM3P2863AF-08TR	3P2863AF	8-Pin TSSOP, TAPE & REEL, Pb Free	Commercial
ASM3P2863AF-08ST	3P2863AF	8-Pin SOIC, TUBE, Pb Free	Commercial
ASM3P2863AF-08SR	3P2863AF	8-Pin SOIC, TAPE & REEL, Pb Free	Commercial
ASM3P2863AG-06OR	V3LL	6-Pin TSOT-23, TAPE & REEL, Green	Commercial
ASM3P2863AG-08TT	3P2863AG	8-Pin TSSOP, TUBE, Green	Commercial
ASM3P2863AG-08TR	3P2863AG	8-Pin TSSOP, TAPE & REEL, Green	Commercial
ASM3P2863AG-08ST	3P2863AG	8-Pin SOIC, TUBE, Green	Commercial
ASM3P2863AG-08SR	3P2863AG	8-Pin SOIC, TAPE & REEL, Green	Commercial

## **Device Ordering Information**



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