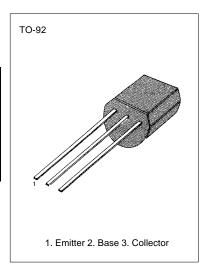
# PNP EPITAXIAL SILICON TRANSISTOR

# LOW FREQUENCY, LOW NOISE AMPLIFIER

Complement to SS9014

# ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C)

Characteristic	Symbol	Rating	Unit
Collector-Base Voltage Collector-Emitter Voltage Emitter-Base Voltage Collector Current Collector Dissipation Junction Temperature Storage Temperature	V <sub>CEO</sub> V <sub>EEO</sub> V <sub>EBO</sub> P <sub>C</sub> T <sub>J</sub> T <sub>STG</sub>	-50 -45 -5 -100 450 150 -55 ~ 150	V V mA mW °C °C



## ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C)

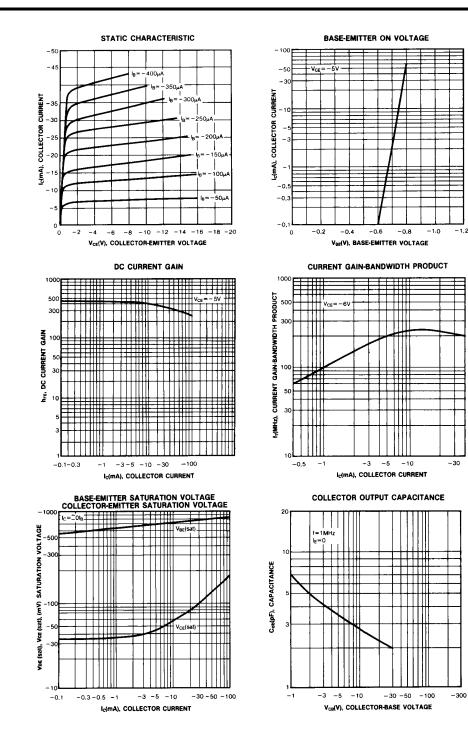
Characteristic	Symbol	Test Conditions	Min	Тур	Max	Unit
Collector-Base Breakdown Voltage Collector-Emitter Breakdown Voltage Emitter-Base Breakdown Voltage Collector Cut-off Current Emitter Cut-off Current DC Current Gain Collector-Base Saturation Voltage Base-Emitter Saturation Voltage Base-Emitter On Voltage Output Capacitance Current Gain-Bandwidth Product Noise Figure	$\begin{array}{c} BV_{CBO}\\ BV_{CEO}\\ BV_{EBO}\\ I_{CBO}\\ I_{EBO}\\ h_{FE}\\ V_{CE}\left(sat\right)\\ V_{BE}\left(sat\right)\\ V_{BE}\left(on\right)\\ C_{OB}\\ f_{T}\\ NF \end{array}$	$\begin{array}{l} l_{\rm C}=-100\mu A, \ l_{\rm E}=0 \\ l_{\rm C}=-1mA, \ l_{\rm B}=0 \\ l_{\rm E}=-100\mu A, \ l_{\rm C}=0 \\ V_{\rm CB}=-50V, \ l_{\rm E}=0 \\ V_{\rm CE}=-5V, \ l_{\rm C}=-1mA \\ l_{\rm C}=-100mA, \ l_{\rm B}=-5mA \\ l_{\rm C}=-100mA, \ l_{\rm B}=-5mA \\ V_{\rm CE}=-5V, \ l_{\rm C}=-2mA \\ V_{\rm CE}=-5V, \ l_{\rm C}=-2mA \\ V_{\rm CE}=-5V, \ l_{\rm C}=-10mA \\ V_{\rm CE}=-5V, \ l_{\rm C}=-2mA \\ V_{\rm CE}=-5V, \ l_{\rm C}=-2mA \\ V_{\rm CE}=-5V, \ l_{\rm C}=-10mA \\ I_{\rm C}=-10MA, \ I_{\rm B}=0 \\ f=1MHz \\ I_{\rm CE}=-5V, \ l_{\rm C}=-0.2mA \\ f=1KHz, \ R_{\rm S}=1K\Omega \end{array}$	-50 -45 -5 60 -0.6 100	200 -0.2 -0.82 -0.65 4.5 190 0.7	-50 -50 600 -0.7 -1.0 -0.75 7.0	>>>A nA >>F MB

### hFE CLASSIFICATION

Classification	A	в	С
h <sub>FE</sub>	60-150	100-300	200-600



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#### **Definition of Terms**

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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