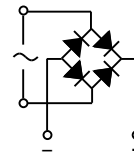
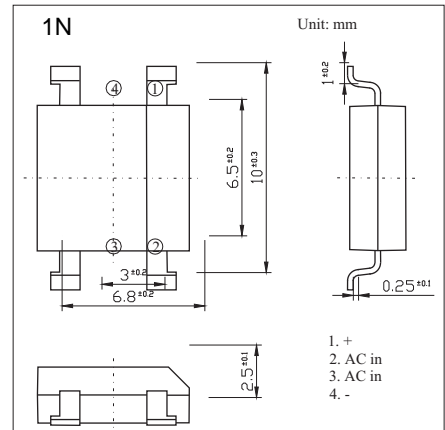


S1NB05 - S1NB100

■ Features

- Glass passivated chip junctions
- Surge overload rating to 30 Amps peak
- Reliable low cost molded plastic construction
- Ideal for printed circuit board applications



■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	S1NB05	S1NB10	S1NB20	S1NB40	S1NB60	S1NB80	S1NB100	Unit	
Peak Repetitive Reverse Voltage	VRMM									
Working Peak Reverse Voltage	VRWM	50	100	200	400	600	800	1000	V	
DC Blocking Voltage	VDC									
RMS Reverse Voltage	VRMS	35	70	140	280	420	560	700	V	
Average Forward Rectified Current TA=@ 40°C	Io	1.0								A
Non-Repetitive Peak Forward Surge Current, 8.3 ms Single half-sine-wave Superimposed on Rated Load (JEDECmethod)	IFSM	50								A
Forward Voltage (per element) @ IF = 1.0A	VF	1.05								V
Reverse Current(per element) @ Rated VR, TA = 25°C	IR	5								μA
		500								
Minimum Insulation Breakdown Voltage (Circuit to Case)	Viso	2400								V
Typical Thermal Resistance, Junction to Ambient (Note 1)	RθJA	40								°C/W
Operating and Storage Temperature Range	Tj, Tstg	-55 to +150								°C

Notes: 1. Device mounted on PCB with 0.5 × 0.5" (13 × 13mm).



S1NB05 - S1NB100

■ Typical Characteristics

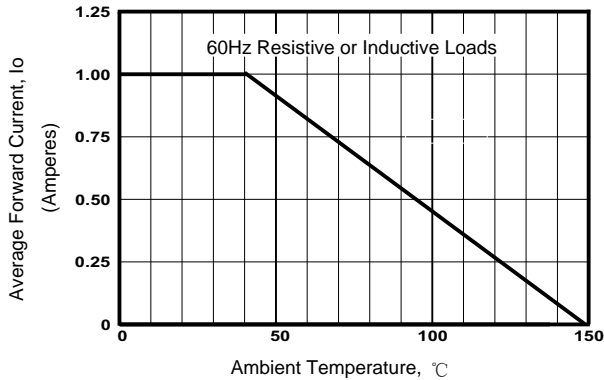


FIGURE 1. FORWARD CURRENT DERATING CURVE

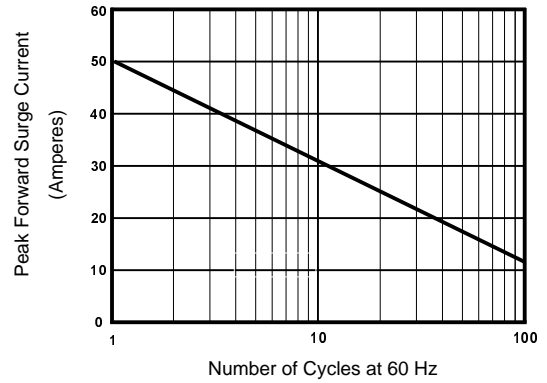


FIGURE 2. MAXIMUM NON-REPETITIVE SURGE CURRENT

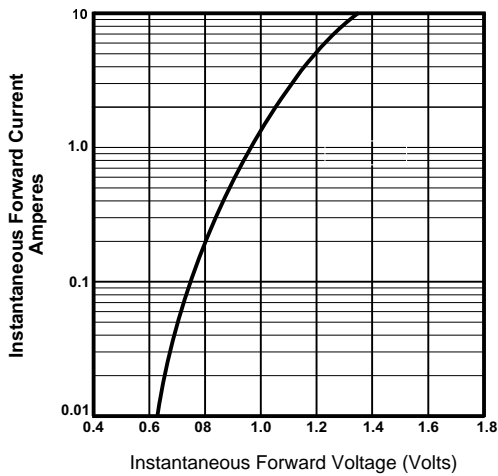


FIGURE 3. TYPICAL FORWARD CHARACTERISTIC PER DIODE

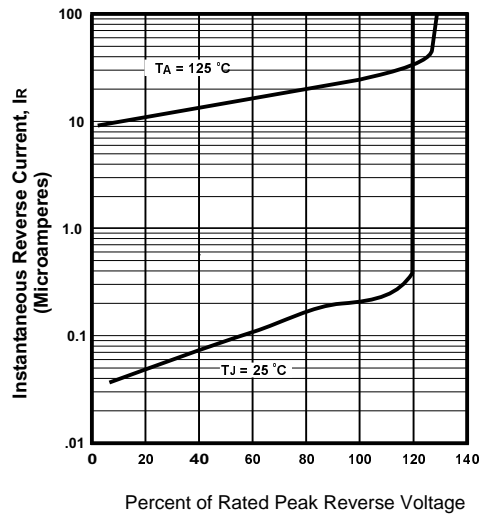


FIGURE 4. TYPICAL REVERSE CHARACTERISTICS

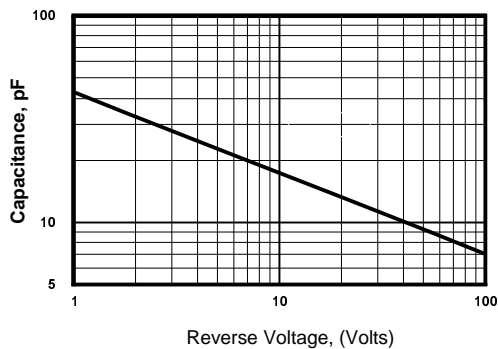


FIGURE 5. TYPICAL JUNCTION CAPACITANCE PER DIODE