

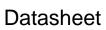
# **CR08AS-12A**

600V - 0.8A - Thyristor Low Power Use

#### Features

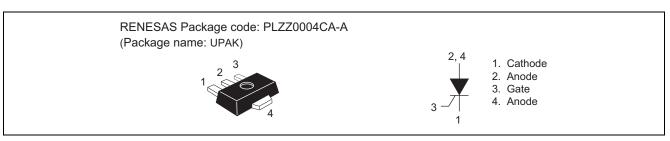
- $I_{T (AV)} : 0.8 A$
- V<sub>DRM</sub> : 600 V
- I<sub>GT</sub>: 100 μA

## Outline



R07DS0489EJ0300 Rev.3.00 May 22, 2013

- Non-Insulated Type
- Planar Type
- Surface Mounted type



# Applications

Solid state relay, strobe flasher, igniter, and hybrid IC

## **Maximum Ratings**

Parameter	Symbol	Voltage class	Unit	
raiameter	Symbol	12		
Repetitive peak reverse voltage	V <sub>RRM</sub>	600	V	
Non-repetitive peak reverse voltage	V <sub>RSM</sub>	720	V	
DC reverse voltage	V <sub>R(DC)</sub>	480	V	
Repetitive peak off-state voltage Note1	V <sub>DRM</sub>	600	V	
DC off-state voltage Note1	V <sub>D(DC)</sub>	480	V	

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I <sub>T(RMS)</sub>	1.26	А	
Average on-state current	I <sub>T(AV)</sub>	0.8	А	Commercial frequency, sine half wave 180° conduction, Ta=51°C <sup>Note2</sup>
Surge on-state current	I <sub>TSM</sub>	10	А	60Hz sine half wave, 1full cycle, peak value, non-repetitive
I <sup>2</sup> t for fusing	l <sup>2</sup> t	0.42	A <sup>2</sup> s	Value corresponding to 1 cycle of half wave 60 Hz, surge on-state current
Peak gate power dissipation	P <sub>GM</sub>	0.5	W	
Average gate power dissipation	P <sub>G(AV)</sub>	0.1	W	
Peak gate forward voltage	V <sub>FGM</sub>	6	V	
Peak gate reverse voltage	V <sub>RGM</sub>	6	V	
Peak gate forward current	I <sub>FGM</sub>	0.3	А	
Junction temperature	Tj	- 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	_	50	mg	Typical value

Notes: 1. With gate to cathode resistance  $R_{GK}$  = 1  $k\Omega$ 

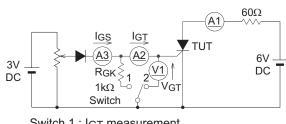


# **Electrical Characteristics**

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak reverse current	I <sub>RRM</sub>	—	—	0.5	mA	Tj = 125°C, V <sub>RRM</sub> applied R <sub>GK</sub> = 1 k $\Omega$
Repetitive peak off-state current	I <sub>DRM</sub>	_	_	0.5	mA	Tj = 125°C, V <sub>DRM</sub> applied R <sub>GK</sub> = 1 k $\Omega$
On-state voltage	V <sub>TM</sub>	_	—	1.5	V	Tj = 25°C, $I_{TM}$ = 2.5 A instantaneous value
Gate trigger voltage	V <sub>GT</sub>	—	—	0.8	V	$Tj = 25^{\circ}C, V_D = 6 V,$ $I_T = 0.1 A^{Note3}$
Gate non-trigger voltage	V <sub>GD</sub>	0.2	—	—	V	$Tj = 125^{\circ}C, V_D = 1/2 V_{DRM}$ $R_{GK} = 1 k\Omega$
Gate trigger current	I <sub>GT</sub>	1	—	100	μA	$\label{eq:transform} \begin{array}{l} Tj = 25^{\circ}C, \ V_{D} = 6 \ V, \\ I_{T} = 0.1 \ A^{Note3} \end{array}$
Holding current	I <sub>H</sub>	—	1.5	3	mA	$Tj = 25^{\circ}C, V_{D} = 12 V$ $R_{GK} = 1 k\Omega$
Thermal resistance	R <sub>th(j-a)</sub>			65	°C/W	Junction to ambient Note2

Notes: 2. Soldering with ceramic plate (25 mm  $\times$  25 mm  $\times$  t0.7 mm).

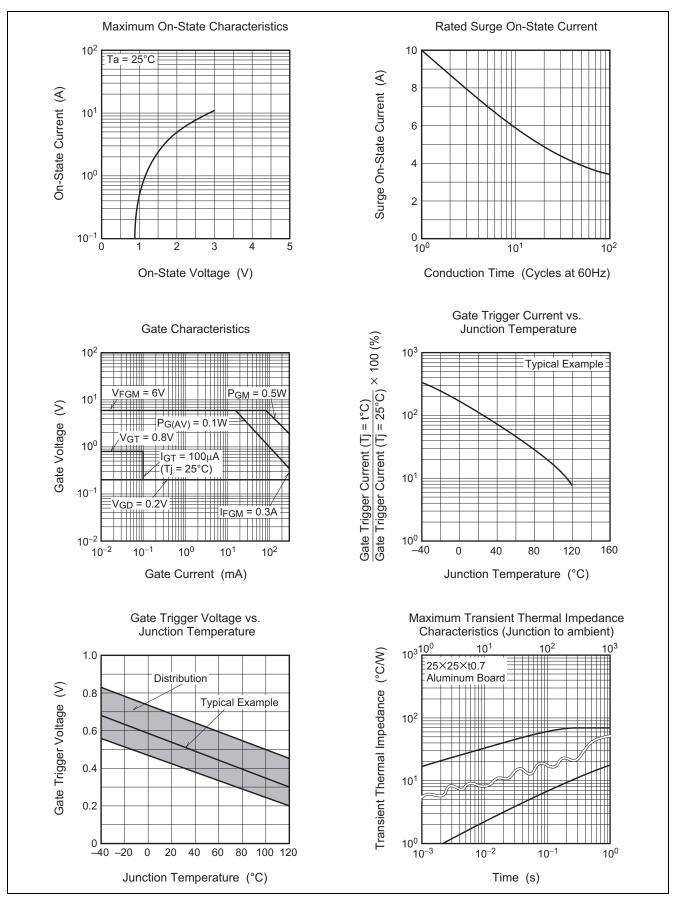
3. IGT, VGT measurement circuit.



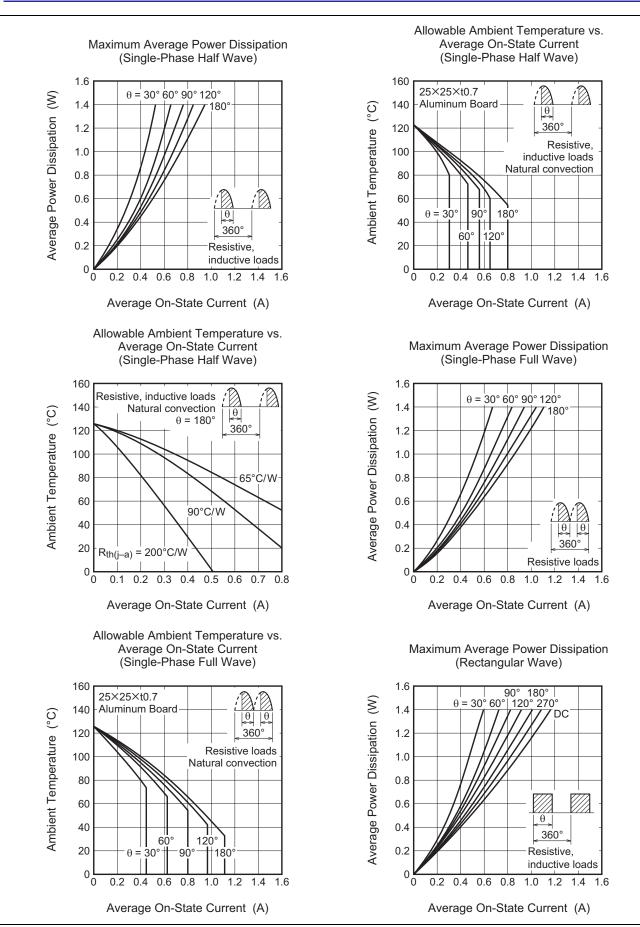
$$\label{eq:switch 1: IGT} \begin{split} & \text{Switch 1: IGT measurement} \\ & \text{Switch 2: VGT measurement} \\ & (\text{Inner resistance of voltage meter is about } 1 k\Omega) \end{split}$$



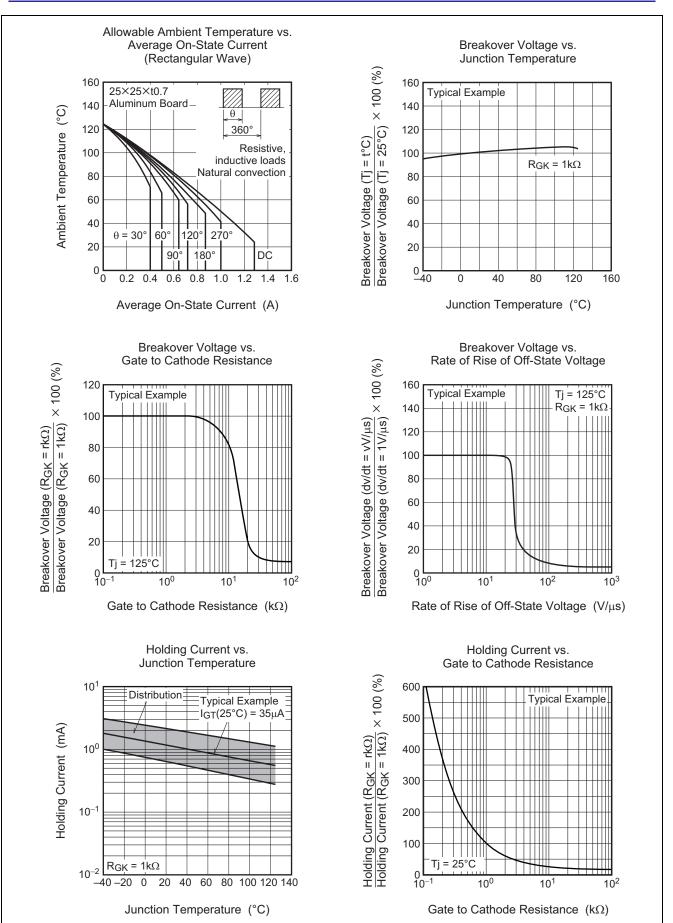
## **Performance Curves**

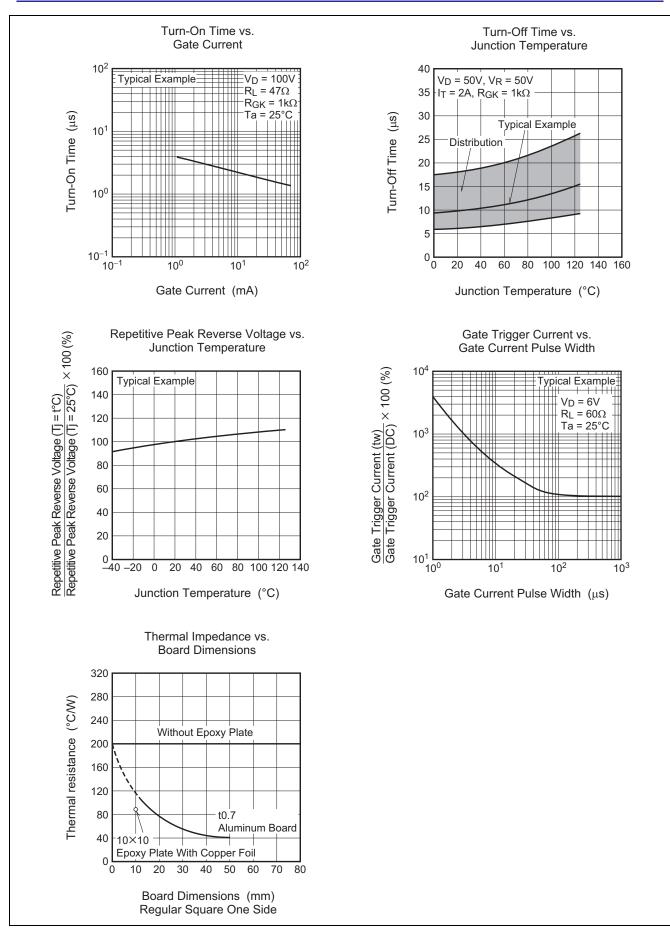




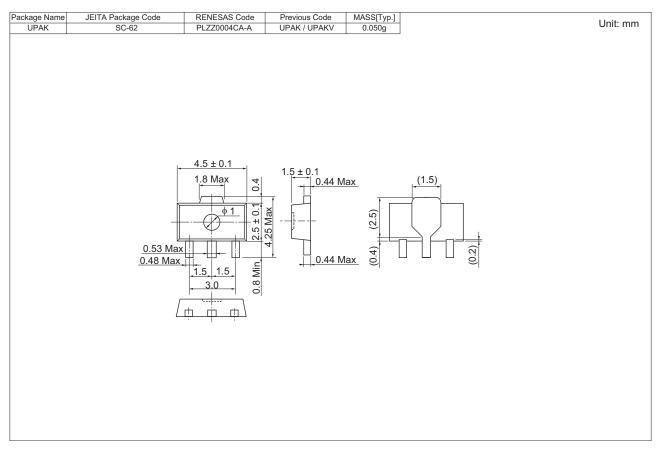








## **Package Dimensions**



## **Ordering Information**

Orderable Part Number	Packing	Quantity	Remark
CR08AS-12A-T14 #B10	Embossed Tape	4000 pcs.	Taping direction "T1"

Note : Please confirm the specification about the shipping in detail.



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