Mechanical Shock

Solderability

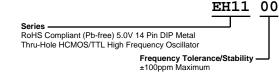
Vibration

**Resistance to Solvents** 

**Temperature Cycling** 

**Resistance to Soldering Heat** 





TS -40.390M Nominal Frequency

Pin 1 Connection
 Tri-State (High Impedance)

40.390MHz

• Duty Cycle 50 ±10(%)

Package — J Operating Temperature Range — 0°C to +70°C

ELECTRICAL SPECIFICA	ΓΙΟΝS	
Nominal Frequency	40.390MHz	
Frequency Tolerance/Stability	±100ppm Maximum (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operation Temperature Range, Supply Voltage Change, Output Load Change, 1st Year Aging at 25°C, Shock, and Vibration)	
Aging at 25°C	±5ppm/year Maximum	
Operating Temperature Range	0°C to +70°C	
Supply Voltage	5.0Vdc ±10%	
Input Current	50mA Maximum (No Load)	
Output Voltage Logic High (Voh)	2.4Vdc Minimum with TTL Load, Vdd-0.4Vdc Minimum with HCMOS Load (IOH = -16mA)	
Output Voltage Logic Low (Vol)	0.4Vdc Maximum with TTL Load, 0.5Vdc Maximum with HCMOS Load (IOL = +16mA)	
Rise/Fall Time	6nSec Maximum (Measured at 0.8Vdc to 2.0Vdc with TTL Load, 20% to 80% of waveform with HCMOS Load)	
Duty Cycle	50 ±10(%) (Measured at 1.4Vdc with TTL Load or at 50% of wavform with HCMOS Load)	
Load Drive Capability	10TTL Load or 50pF HCMOS Load Maximum	
Output Logic Type	CMOS	
Pin 1 Connection	Tri-State (High Impedance)	
Tri-State Input Voltage (Vih and Vil)	+2.2Vdc Minimum to enable output, +0.8Vdc Maximum to disable output (High Impedance), No Connect t enable output.	
Absolute Clock Jitter	±250pSec Maximum, ±100pSec Typical	
One Sigma Clock Period Jitter	±50pSec Maximum, ±30pSec Typical	
Start Up Time	10mSec Maximum	
Storage Temperature Range	-55°C to +125°C	
ENVIRONMENTAL & MEC	HANICAL SPECIFICATIONS	
Fine Leak Test	MIL-STD-883, Method 1014, Condition A	
Gross Leak Test	MIL-STD-883, Method 1014, Condition C	
Lead Integrity	MIL-STD-883, Method 2004	

MIL-STD-202, Method 213, Condition C

MIL-STD-883, Method 2007, Condition A

MIL-STD-202, Method 210

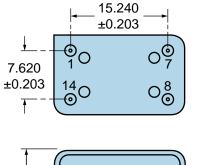
MIL-STD-202, Method 215

MIL-STD-883, Method 2003

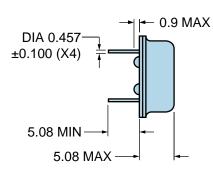
MIL-STD-883, Method 1010



### **MECHANICAL DIMENSIONS (all dimensions in millimeters)**

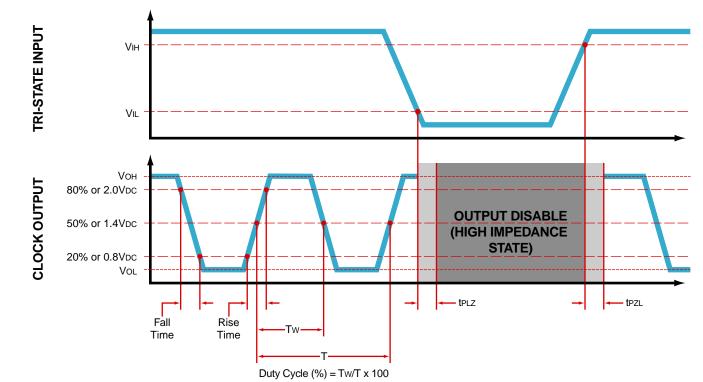






PIN	CONNECTION
1	Tri-State (High Impedance)
7	Ground/Case Ground
8	Output
14	Supply Voltage
LINE	MARKING
1	ECLIPTEK
2	EH11TS EH11=Product Series
3	40.390M
4	XXYZZ XX=Ecliptek Manufacturing Code Y=Last Digit of the Year ZZ=Week of the Year

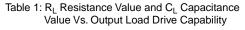
**OUTPUT WAVEFORM & TIMING DIAGRAM** 

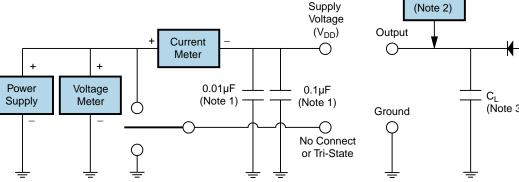




### Test Circuit for TTL Output

Output Load Drive Capability	R <sub>L</sub> Value (Ohms)	C <sub>L</sub> Value (pF)
10TTL	390	15
5TTL	780	15
2TTL	1100	6
10LSTTL	2000	15
1TTL	2200	3





Probe (Note 2) Output Ground CL (Note 3)

Frequency

Counter

Oscilloscope

Note 1: An external 0.1µF low frequency tantalum bypass capacitor in parallel with a 0.01µF high frequency ceramic bypass capacitor close to the package ground and V<sub>DD</sub> pin is required.

Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>300MHz) passive probe is recommended.

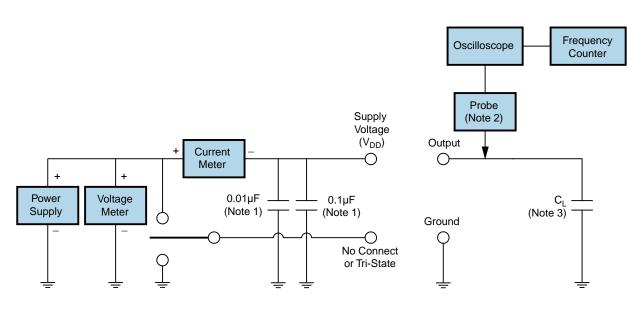
Note 3: Capacitance value  $C_L$  includes sum of all probe and fixture capacitance.

Note 4: Resistance value RL is shown in Table 1. See applicable specification sheet for 'Load Drive Capability'.

Note 5: All diodes are MMBD7000, MMBD914, or equivalent.



### **Test Circuit for CMOS Output**



Note 1: An external 0.1µF low frequency tantalum bypass capacitor in parallel with a 0.01µF high frequency ceramic bypass capacitor close to the package ground and V<sub>DD</sub> pin is required.

Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>300MHz) passive probe is recommended.

Note 3: Capacitance value  $\dot{C}_{L}$  includes sum of all probe and fixture capacitance.



# **Recommended Solder Reflow Methods**

EH1100TS-40.390M



### High Temperature Solder Bath (Wave Solder)

$T_s$ MAX to $T_L$ (Ramp-up Rate)	3°C/second Maximum
Preheat	
- Temperature Minimum (T <sub>s</sub> MIN)	150°C
- Temperature Typical (T <sub>s</sub> TYP)	175°C
- Temperature Maximum (T <sub>s</sub> MAX)	200°C
- Time (t <sub>s</sub> MIN)	60 - 180 Seconds
Ramp-up Rate (T <sub>L</sub> to T <sub>P</sub> )	3°C/second Maximum
Time Maintained Above:	
- Temperature (T∟)	217°C
- Time (t <sub>L</sub> )	60 - 150 Seconds
Peak Temperature (T <sub>P</sub> )	260°C Maximum for 10 Seconds Maximum
Target Peak Temperature (T <sub>P</sub> Target)	250°C +0/-5°C
Time within 5°C of actual peak (t <sub>p</sub> )	20 - 40 seconds
Ramp-down Rate	6°C/second Maximum
Time 25°C to Peak Temperature (t)	8 minutes Maximum
Moisture Sensitivity Level	Level 1
Additional Notes	Temperatures shown are applied to back of PCB board and device leads only. Do not use this method for product with the Gull Wing option.



## **Recommended Solder Reflow Methods**

EH1100TS-40.390M



### Low Temperature Infrared/Convection 185°C

•	
T <sub>s</sub> MAX to T <sub>L</sub> (Ramp-up Rate)	5°C/second Maximum
Preheat	
- Temperature Minimum (T <sub>s</sub> MIN)	N/A
<ul> <li>Temperature Typical (T<sub>s</sub> TYP)</li> </ul>	150°C
<ul> <li>Temperature Maximum (T<sub>s</sub> MAX)</li> </ul>	N/A
- Time (t <sub>s</sub> MIN)	60 - 120 Seconds
Ramp-up Rate (T⊾ to T <sub>P</sub> )	5°C/second Maximum
Time Maintained Above:	
- Temperature (T∟)	150°C
- Time (t∟)	200 Seconds Maximum
Peak Temperature (T <sub>P</sub> )	185°C Maximum
Target Peak Temperature (T <sub>P</sub> Target)	185°C Maximum 2 Times
Time within 5°C of actual peak ( $t_p$ )	10 seconds Maximum 2 Times
Ramp-down Rate	5°C/second Maximum
Time 25°C to Peak Temperature (t)	N/A
Moisture Sensitivity Level	Level 1
Additional Notes	Temperatures shown are applied to body of device. Use this method only for product with the Gull Wing option.



## **Recommended Solder Reflow Methods**

EH1100TS-40.390M



### Low Temperature Solder Bath (Wave Solder)

T <sub>s</sub> MAX to T <sub>L</sub> (Ramp-up Rate)	5°C/second Maximum	
Preheat		
- Temperature Minimum (T <sub>s</sub> MIN)	N/A	
<ul> <li>Temperature Typical (T<sub>s</sub> TYP)</li> </ul>	150°C	
- Temperature Maximum (T <sub>s</sub> MAX)	N/A	
- Time (t <sub>s</sub> MIN)	30 - 60 Seconds	
Ramp-up Rate (T <sub>L</sub> to T <sub>P</sub> )	5°C/second Maximum	
Time Maintained Above:		
- Temperature (T∟)	150°C	
- Time (t∟)	200 Seconds Maximum	
Peak Temperature (T <sub>P</sub> )	245°C Maximum	
Target Peak Temperature (T <sub>P</sub> Target)	245°C Maximum 1 Time / 235°C Maximum 2 Times	
Time within 5°C of actual peak (t <sub>p</sub> )	5 seconds Maximum 1 Time / 15 seconds Maximum 2 Times	
Ramp-down Rate	5°C/second Maximum	
Time 25°C to Peak Temperature (t)	N/A	
Moisture Sensitivity Level	Level 1	
Additional Notes	Temperatures shown are applied to back of PCB board and device leads only. Do not use this method for product with the Gull Wing option.	

#### Low Temperature Manual Soldering

185°C Maximum for 10 seconds Maximum, 2 times Maximum. (Temperatures listed are applied to device leads only. This method can be utilized with both Gull Wing and Non-Gull Wing devices.)

#### **High Temperature Manual Soldering**

260°C Maximum for 5 seconds Maximum, 2 times Maximum. (Temperatures listed are applied to device leads only. This method can be utilized with both Gull Wing and Non-Gull Wing devices.)