



75 Watt ASD-Q Single Series DC/DC Converters



Description

The 75 Watt single ASD-Q series of DC/DC Converters provide precisely regulated dc outputs. All outputs are fully isolated from the inputs, allowing the output to be used with positive or negative polarity and various grounding options. The ASD-Q Series utilizes an insulated metal substrate design in an industry standard 1/4 brick case size to meet the most rigorous requirements of COTS and thermally challenging industrial applications.

Standard features include remote sensing, output trim, and remote on/off. Threaded-through holes are provided to allow easy mounting or add a heat sink for extended temperature use.

Features

- Small size 1.45"x2.28"x0.52", industry standard 1/4 brick
- Excellent thermal performance with metal baseplate
- High Efficiency
- Fast over voltage protection
- Pulse-by-pulse current limiting, dead short current limiting
- Over-temperature protection
- Auto-softstart
- Very Low noise
- Low profile magnetics run cooler
- Constant frequency for normal operation
- Wide input voltage range
- Remote Sense with high regulation
- Remote ON/OFF
- Super energy saving, 6 mA input idle current
- Output trim with very low temperature coefficient
- Water Washable, wide humidity applications
- Good shock and vibration damping

Selection Chart

Model	Input Range VDC		I in ADC @ nom	V out VDC	I out ADC
	Min	Max	Typ		
ASD75-24S3.3Q	18	36	3.31	3.3	20
ASD75-24S5Q	18	36	3.63	5	15
ASD75-24S12Q	18	36	3.59	12	6.25
ASD75-24S15Q	18	36	3.55	15	5
ASD75-24S24Q	18	36	3.55	24	3.13
ASD75-48S3.3Q	36	75	1.65	3.3	20
ASD75-48S5Q	36	75	1.80	5	15
ASD75-48S12Q	36	75	1.78	12	6.25
ASD75-48S15Q	36	75	1.76	15	5
ASD75-48S24Q	36	75	1.76	24	3.13

Default ON/OFF logic is positive.
Add -N to the model number to order negative ON/OFF logic.



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Unless otherwise stated, these specifications apply for ambient temperature $T_A=23 \pm 2^\circ\text{C}$, nominal input voltage, and rated full load. (1)

Input Parameters							
Model		ASD75-24S3.3Q	ASD75-24S5Q	ASD75-24S12Q	ASD75-24S15Q	ASD75-24S24Q	Units
Voltage Range	MIN	18					VDC
	TYP	24					
	MAX	36					
Input Overvoltage* 100 mSec	MAX	50					VDC
Input Ripple Rejection (120Hz)	TYP	60					dB
Undervoltage Lockout		Yes					
Input Reverse Voltage Protection		Yes					
Input Current No Load 100% Load	TYP	50	50	50	50	50	mA A
	TYP	3.3	3.6	3.6	3.6	3.6	
Inrush Current	MAX	0.2					A ² S
Reflected Ripple, 12 μ H Source Impedance (3)	TYP	10					mA P-P
Efficiency	TYP	82	84	86	87	87	%
Switching Frequency	TYP	360					kHz
Recommended Fuse		(2)					AMPS

Input Parameters							
Model		ASD75-48S3.3Q	ASD75-48S5Q	ASD75-48S12Q	ASD75-48S15Q	ASD75-48S24Q	Units
Voltage Range	MIN	36					VDC
	TYP	48					
	MAX	75					
Input Overvoltage* 100 mSec	MAX	85					VDC
Input Ripple Rejection (120Hz)	TYP	60					dB
Undervoltage Lockout		Yes					
Input Reverse Voltage Protection		Yes					
Input Current No Load 100% Load	TYP	80	80	80	80	80	mA A
	TYP	1.7	1.8	1.8	1.8	1.8	
Inrush Current	MAX	0.2					A ² S
Reflected Ripple, 12 μ H Source Impedance (3)	TYP	10					mA P-P
Efficiency	TYP	82	84	86	87	87	%
Switching Frequency	TYP	360					kHz
Recommended Fuse		(2)					AMPS

* Absolute Maximum Ratings. Caution: Stresses in excess of the Absolute Maximum Ratings can cause permanent damage to the device (see Note 1).



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Output Parameters							
Model		ASD75-24S3.3Q ASD75-48S3.3Q	ASD75-24S5Q ASD75-48S5Q	ASD75-24S12Q ASD75-48S12Q	ASD75-24S15Q ASD75-48S15Q	ASD75-24S24Q ASD75-48S24Q	Units
Output Voltage		3.3	5	12	15	24	VDC
Output Voltage Setpoint Accuracy	MAX	±1					%
Turn On Overshoot	TYP	0					%
Temperature Coefficient	TYP MAX	0.005 0.01	0.003 0.005				%/°C
Noise (8)	TYP	20	20	40	50	70	mV RMS
Ripple	TYP	30	30	75	100	150	mV P-P
Load Current (4)	MIN MAX	5 100					%
Load Transient Overshoot (7)	TYP	2					%
Load Transient Recovery Time (6)	TYP	0.8					mSec
Load Regulation (5)	TYP MAX	0.02 0.2					%
Line Regulation	TYP MAX	0.01 0.1					%
Overvoltage Protection (OVP) Threshold	MIN MAX	115 135					%
OVP Type - Non-latching Open Loop Overvoltage Clamp							
Output Current Limit	TYP	120					%
$V_{out}=90\%$ of $V_{out-nom}$							
Output Short Circuit Current	TYP MAX	150 160					%
$V_{out} = 0.1 \text{ V}$							

NOTES:

- (1) Refer to the Astrodyne Application Notes for the definition of terms, measurement circuits, and other information.
- (2) Refer to the Astrodyne Application Notes for information on fusing. For inrush current, refer to the specifications above.
- (3) 33 μF capacitor connected to two "Input" pins. Then place current sensor in series with 12 μH inductor between 33 μF and the source. The reflected ripple current is measured over 5 Hz to 20 MHz bandwidth (current sensor is located between the converter input pin and the 12 μH inductor).
- (4) Optimum performance is obtained when this power supply is operated within the minimum to maximum load specifications. No damage to module will occur when the output is operated at less than minimum load, but the output voltage may contain a low frequency component that may exceed output noise specifications.
- (5) Load regulation is defined as the output voltage change when changing load current from maximum to minimum. The voltage is measured at the output pin.
- (6) Load Transient Recovery Time is defined as the time for the output to settle from a 50 to 75% or 25% step load change to a 1% error band of output voltage (rise time of step = 2 μ Sec).
- (7) Load Transient Overshoot is defined as the peak overshoot during a transient as defined in the Note 6 above.
- (8) Noise is measured per the Astrodyne Application Notes. Output noise is measured with a 10 μF tantalum capacitor in parallel with a 0.1 μF ceramic capacitor connected across the output to CMN. Measurement bandwidth is 0-20 MHz.
- (9) When an external On/Off switch is used, such as open collector switch, logic high requires the switch to be high-impedance. Switch leakage currents greater than 20 μA may be sufficient to trigger the On/Off to the logic-low state.
- (10) Most switches would be suitable for logic On/Off control, in case there is a problem, you can make following estimation and then leave some margin.
When open collector is used for logic high, "Open Circuit Voltage at On/Off Pin", "Output Resistance" and "External Leakage Current Allowed for Logic High" are used to estimate the high impedance requirement of open collector.
When switch is used for logic low, "Open Circuit Voltage at On/Off Pin", "Output Resistance" and "LOW Logic Level" are used to estimate the low impedance requirement of switch.
- (11) Thermal impedance is tested with the converter mounted vertically and facing another printed circuit board 1/2 inch away. If converter is mounted horizontally with no obstructions, thermal impedance is approximately 10 $^\circ\text{C}/\text{W}$.
If heat sink is needed, apply a very thin layer of thermally conductive grease on the metal base of converter, then properly tighten the screws.
- (12) Water Washability - These DC/DC converters are designed to withstand most solder/wash processes. Careful attention should be used when assessing the applicability in your specific manufacturing process. Converters are not hermetically sealed.
- (13) Torque fasteners into threaded mounting inserts at 12 in. oz. or less. Greater torque may result in damage to unit and void the warranty.



