Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type  $(\pi - MOSVII)$ 

# **TK12X53D**

## **Switching Regulator Applications**

• Low drain-source ON resistance: RDS (ON) =  $0.5 \Omega$  (typ.)

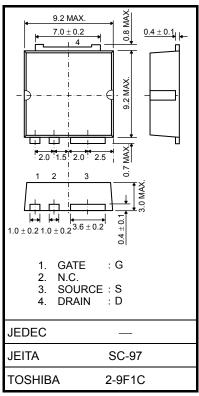
• High forward transfer admittance:  $|Y_{fs}| = 6.0 \text{ S}$  (typ.)

• Low leakage current:  $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 525 \text{ V)}$ 

• Enhancement-mode:  $V_{th} = 2.0 \text{ to } 4.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$ 

### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	525	V
Gate-source voltage		$V_{GSS}$	±30	V
Drain current	DC (Note 1)	I <sub>D</sub>	12	Α
	Pulse (Note 1)	I <sub>DP</sub>	48	A
Drain power dissipation (Tc = 25°C)		PD	150	W
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	378	mJ
Avalanche current		I <sub>AR</sub>	12	Α
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	15	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C



Weight: 0.74 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

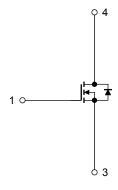
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	0.833	°C/W

Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 4.5 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 12 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.



Internal Connection

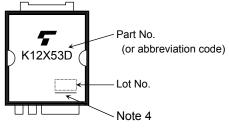
# **Electrical Characteristics (Ta = 25°C)**

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS}=\pm30~V,~V_{DS}=0~V$	_	_	±1	μА
Drain cut-off current		I <sub>DSS</sub>	V <sub>DS</sub> = 525 V, V <sub>GS</sub> = 0 V	_	_	10	μА
Drain-source brea	akdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	525	_	_	V
Gate threshold vo	oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source ON	resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6 A	_	0.50	0.58	Ω
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 6 A	1.5	6.0	_	S
Input capacitance		C <sub>iss</sub>		_	1350	_	
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	6	_	pF
Output capacitance		C <sub>oss</sub>		_	135	_	
Switching time	Rise time	t <sub>r</sub>	10 V ID = 6 A VOUT		22		
	Turn-on time	t <sub>on</sub>	$\begin{array}{c c} 0 \text{ V} & & \\ \hline 50 \Omega & \\ \end{array} \begin{array}{c} R_L = 33 \Omega \\ V_{DD} \approx 200 \text{ V} \end{array}$		55	_	. ns
	Fall time	t <sub>f</sub>			15		
	Turn-off time	t <sub>off</sub>	Duty $\leq$ 1%, $t_W = 10 \mu s$	_	100	_	
Total gate charge		Qg			25		
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 12 \text{ A}$	_	16	_	nC
Gate-drain charge		Q <sub>gd</sub>		_	9		

# **Source-Drain Ratings and Characteristics (Ta = 25°C)**

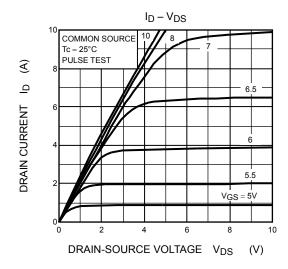
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	12	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	48	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 12 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 12 \text{ A}, V_{GS} = 0 \text{ V},$	_	1300	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> /dt = 100 A/μs	_	12	_	μС

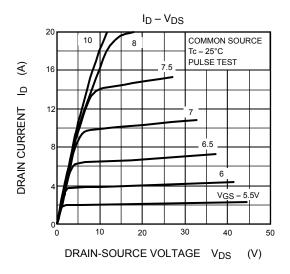
## **Marking**

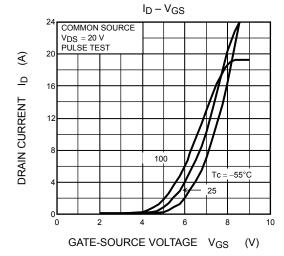


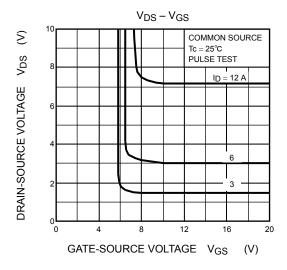
Note 4 : A line under a Lot No. identifies the indication of product Labels  $\hbox{[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]}$ 

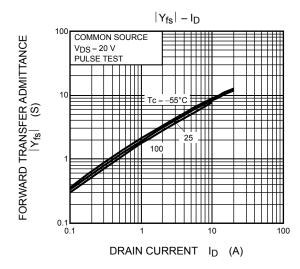
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

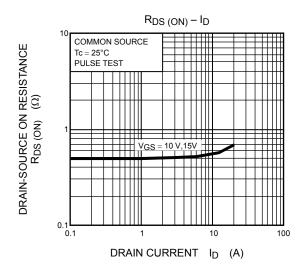




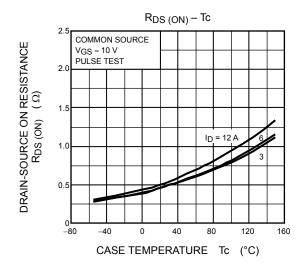


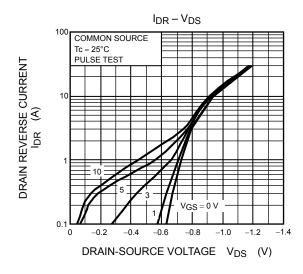


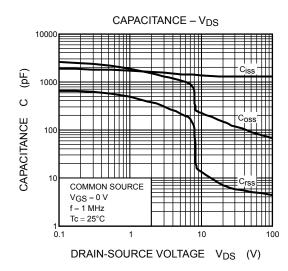


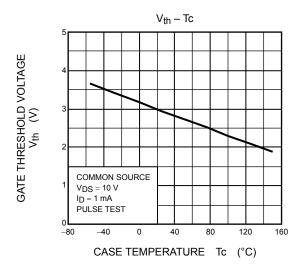


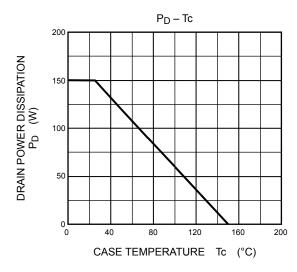
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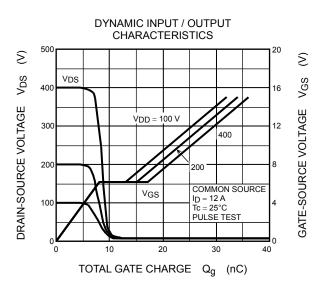


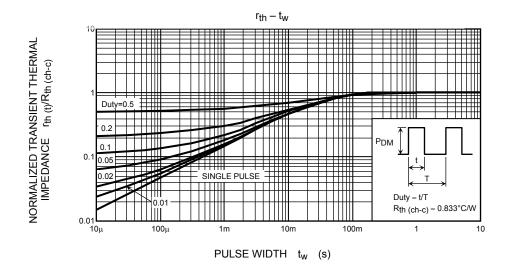


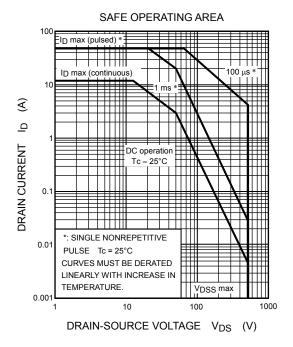


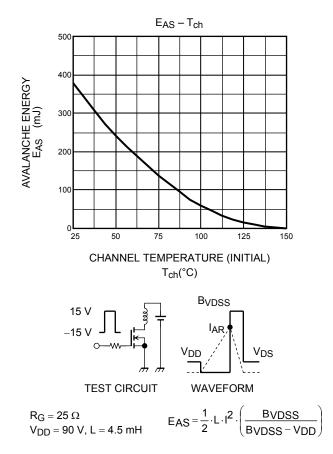












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