

## MCH6413

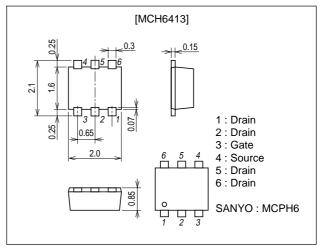
# **Ultrahigh-Speed Switching Applications**

#### **Features**

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · 2.5V drive.

#### **Package Dimensions**

unit : mm 2193A



### **Specifications**

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		20	V
Gate-to-Source Voltage	VGSS		±12	V
Drain Current (DC)	ID		2	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	8	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (900mm <sup>2</sup> X0.8mm)	1	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	ç

#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Uill
Drain-to-Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =1mA, V <sub>GS</sub> =0	20			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =20V, V <sub>GS</sub> =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0			±10	μΑ
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	0.4		1.3	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =1A	1.4	2.4		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)1	I <sub>D</sub> =1A, V <sub>GS</sub> =4V		145	190	mΩ
	R <sub>DS</sub> (on)2	I <sub>D</sub> =0.5A, V <sub>GS</sub> =2.5V	·	185	260	mΩ
	RDS(on)3	ID=0.1A, VGS=1.8V		245	370	mΩ

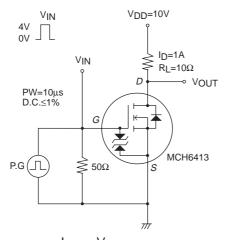
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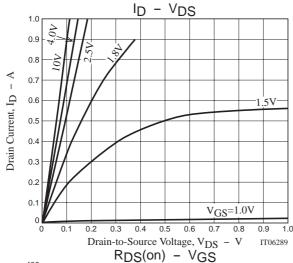
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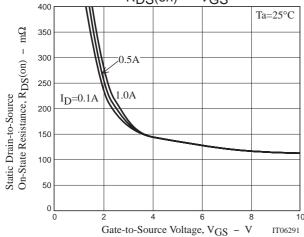
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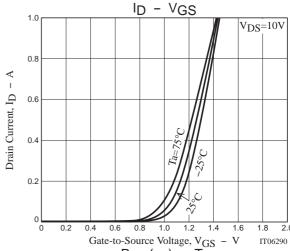
Parameter	Cumbal	Conditions	Ratings			Llois
	Symbol		min	typ	max	Unit
Input Capacitance	Ciss	V <sub>DS</sub> =10V, f=1MHz		120		pF
Output Capacitance	Coss	V <sub>DS</sub> =10V, f=1MHz		31		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =10V, f=1MHz		25		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit		9		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit		29		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit		18		ns
Fall Time	tf	See specified Test Circuit		22		ns
Total Gate Charge	Qg	V <sub>DS</sub> =10V, V <sub>GS</sub> =4V, I <sub>D</sub> =2A		2.3		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =10V, V <sub>GS</sub> =4V, I <sub>D</sub> =2A		0.50		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =10V, V <sub>GS</sub> =4V, I <sub>D</sub> =2A		0.73		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =2A, V <sub>G</sub> S=0		0.97	1.2	V

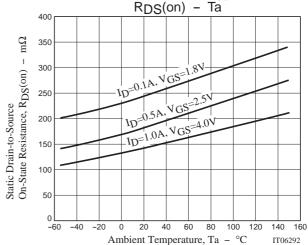
#### **Switching Time Test Circuit**











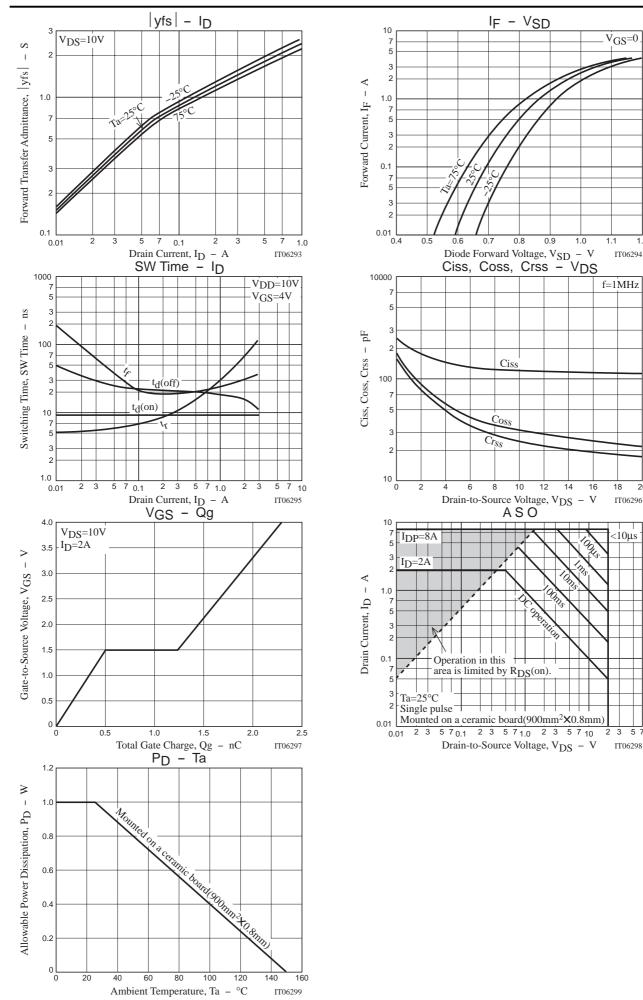
 $V_{GS}=0$ 

IT06294

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<10µs

f=1MHz



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