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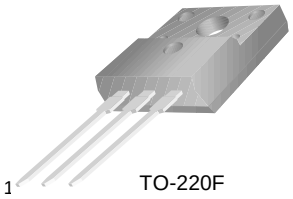
KSC5405F

High Voltage Power Switching Applications



1 TO-220F

1.Base 2.Collector 3.Emitter

NPN Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{CES}	Collector-Base Voltage	1000	V
V_{CEO}	Collector-Emitter Voltage	450	V
V_{EBO}	Emitter-Base Voltage	9	V
I_C	Collector Current (DC)	5	A
I_{CP}	Collector Current (Pulse)	10	A
I_B	Base Current (DC)	2	A
I_{BP}	Base Current (Pulse)	4	A
P_C	Collector Dissipation (T_C=25°C)	40	W
T_J	Junction Temperature	150	°C
T_{STG}	Storage Temperature	- 65 ~ 150	°C

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
V_{CEO} (sus)	*Collector-Emitter Sustaining Voltage	I_C = 100mA, I_B = 0	450			V
I_{CES}	Collector Cut-off Current	V_{CE} = 1000V, V_{BE} = 0			1	mA
I_{EBO}	Emitter Cut-off Current	V_{BE} = 9V, I_C = 0			10	mA
h_{FE}	DC Current Gain	V_{CE}=5V, I_C=0.6A	10		40	
V_{CE}(sat)	Collector-Emitter Saturation Voltage	I_C = 2.5A, I_B = 0.5A			1.5	V
V_{BE}(sat)	Base-Emitter Saturation Voltage:	I_C = 2.5A, I_B = 0.5A			1.3	V
t_{ON}	Turn On Time	V_{CC} = 250V, I_C = 2.5A I_{B1} = -I_{B2} = 0.5A R_L=100Ω			1	µs
t_{STG}	Storage Time				4	µs
t_F	Fall Time				0.8	µs

* Pulsed Test: PW = 300uS, duty cycle = 1.5%

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Typical Characteristics

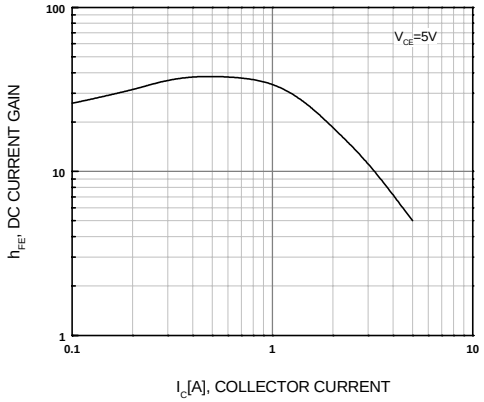

Figure 1. DC current Gain

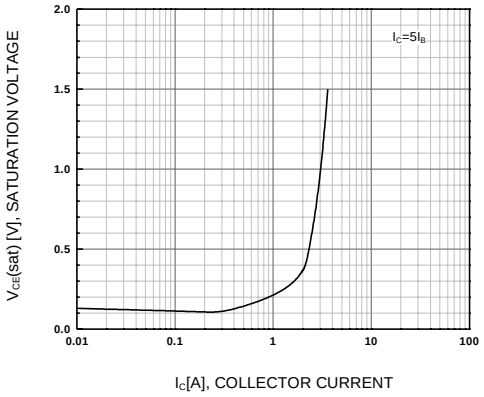

Figure 2. Collector-Emitter Saturation Voltage

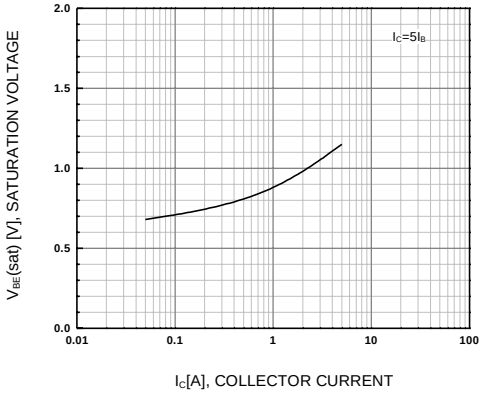

Figure 3. Base-Emitter Saturation Voltage

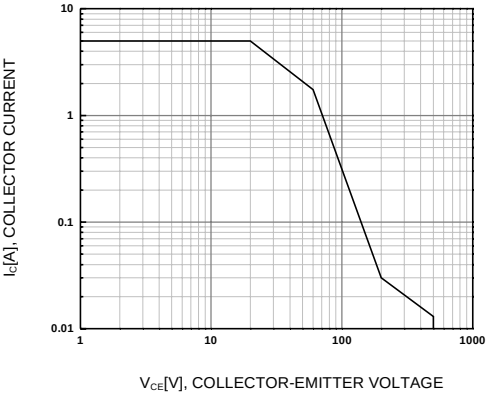

Figure 4. Safe Operating Area

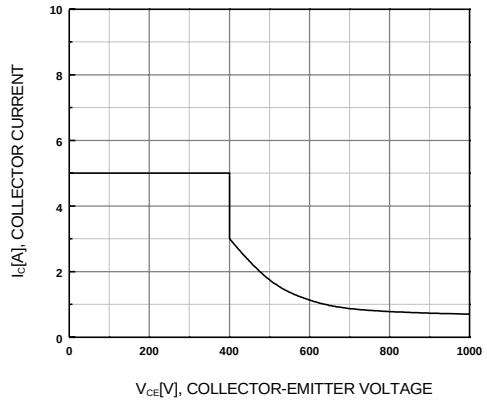

Figure 5. Reverse Bias Safe Operating Area

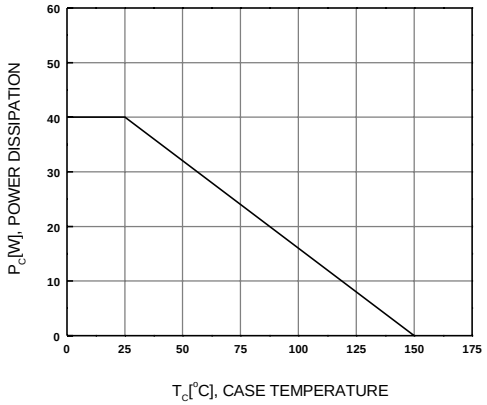

Figure 6. Power Derating

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Package Demensions

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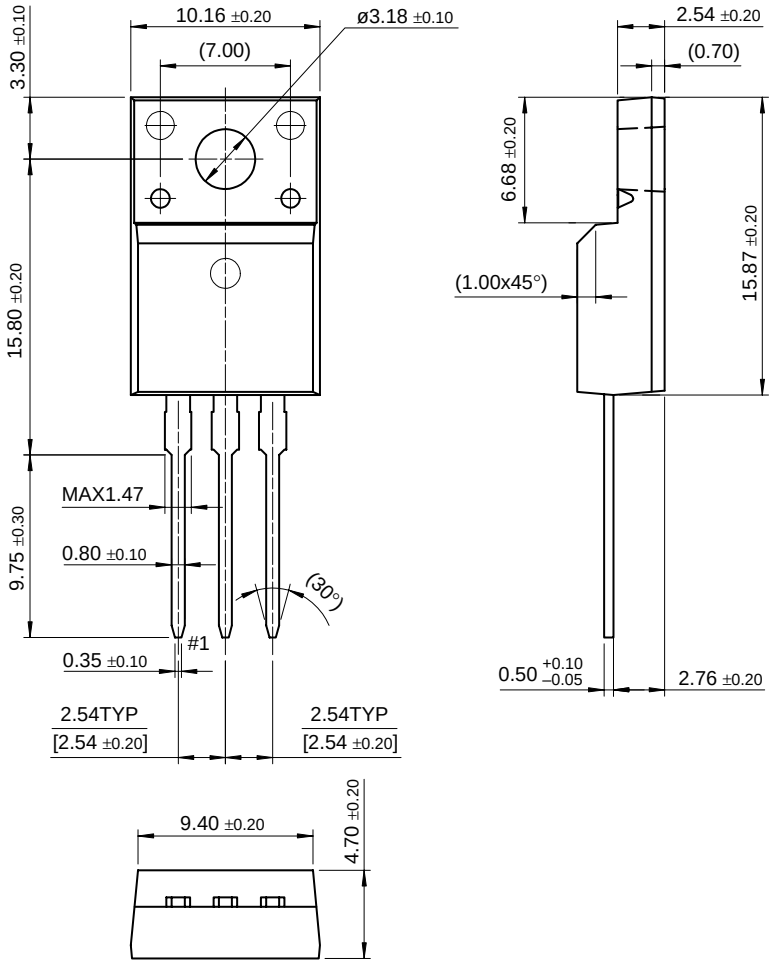

Dimensions in Millimeters

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NPN Silicon Transistor

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Product status/pricing/packaging

Product	Product status	Pricing*	Package type	Leads	Packing method
KSC5405FTU	Full Production	$0.465	TO-220F	3	RAIL

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