

# 2SK3133(L), 2SK3133(S)

Silicon N Channel MOS FET  
High Speed Power Switching

## HITACHI

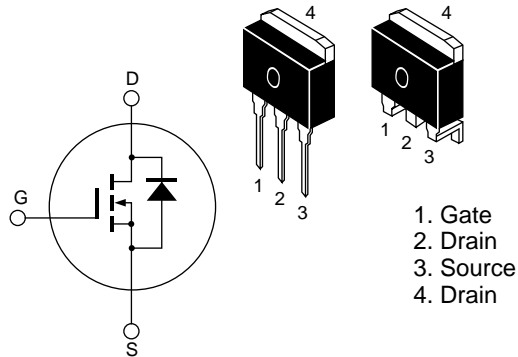
Target Specification  
ADE-208-720A (Z)  
2nd. Edition  
Mar. 2001

### Features

- Low on-resistance  
 $R_{DS(on)} = 7m\Omega$  typ.
- Low drive current
- 4V gate drive device can be driven from 5V source

### Outline

LDPAK



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	50	A
Drain peak current	I <sub>D(pulse)</sub> <sup>Note 1</sup>	200	A
Body-drain diode reverse drain current	I <sub>DR</sub>	50	A
Channel dissipation	P <sub>ch</sub> <sup>Note 2</sup>	50	W
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature	T <sub>stg</sub>	−55 to +150	°C

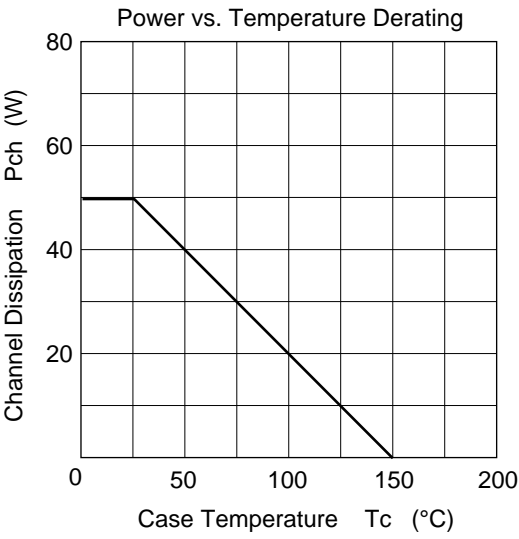
Note: 1. PW ≤ 10μs, duty cycle ≤ 1 %  
2. Value at Tc = 25°C

**Electrical Characteristics** ( $T_a = 25^\circ\text{C}$ )

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	—	—	V	$I_D = 10\text{mA}$ , $V_{GS} = 0$
Gate to source leak current	$I_{GSS}$	—	—	$\pm 0.1$	$\mu\text{A}$	$V_{GS} = \pm 20\text{V}$ , $V_{DS} = 0$
Zero gate voltage drain current	$I_{DSS}$	—	—	10	$\mu\text{A}$	$V_{DS} = 30\text{V}$ , $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.5	—	3.0	V	$I_D = 1\text{mA}$ , $V_{DS} = 10\text{V}$ <small>Note 1</small>
Static drain to source on state resistance	$R_{DS(on)}$	—	7	10	$\text{m}\Omega$	$I_D = 25\text{A}$ , $V_{GS} = 10\text{V}$ <small>Note 1</small>
		—	12	18	$\text{m}\Omega$	$I_D = 25\text{A}$ , $V_{GS} = 4.5\text{V}$ <small>Note 1</small>
Forward transfer admittance	$ y_{fs} $	TBD	TBD	—	S	$I_D = 25\text{A}$ , $V_{DS} = 10\text{V}$ <small>Note 1</small>
Input capacitance	$C_{iss}$	—	TBD	—	pF	$V_{DS} = 10\text{V}$
Output capacitance	$C_{oss}$	—	TBD	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	$C_{rss}$	—	TBD	—	pF	$f = 1\text{MHz}$
Total gate charge	$Q_g$	—	TBD	—	nc	$V_{DD} = 10\text{V}$
Gate to source charge	$Q_{gs}$	—	TBD	—	nc	$V_{GS} = 10\text{V}$
Gate to drain charge	$Q_{gd}$	—	TBD	—	nc	$I_D = 50\text{A}$
Turn-on delay time	$t_{d(on)}$	—	TBD	—	ns	$V_{GS} = 10\text{V}$ , $I_D = 25\text{A}$
Rise time	$t_r$	—	TBD	—	ns	$R_L = 0.4\Omega$
Turn-off delay time	$t_{d(off)}$	—	TBD	—	ns	
Fall time	$t_f$	—	TBD	—	ns	
Body-drain diode forward voltage	$V_{DF}$	—	TBD	—	V	$I_F = 50\text{A}$ , $V_{GS} = 0$
Body-drain diode reverse recovery time	$t_{rr}$	—	TBD	—	ns	$I_F = 50\text{A}$ , $V_{GS} = 0$ $di_F/dt = 50\text{A}/\mu\text{s}$

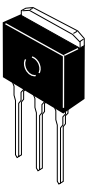
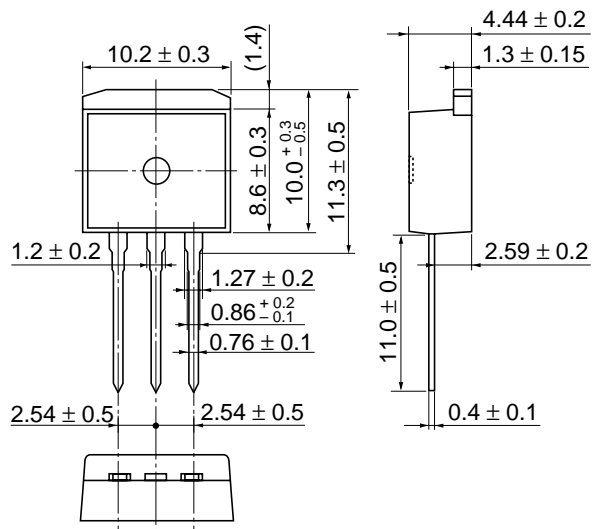
Note: 1. Pulse test

Main Characteristics



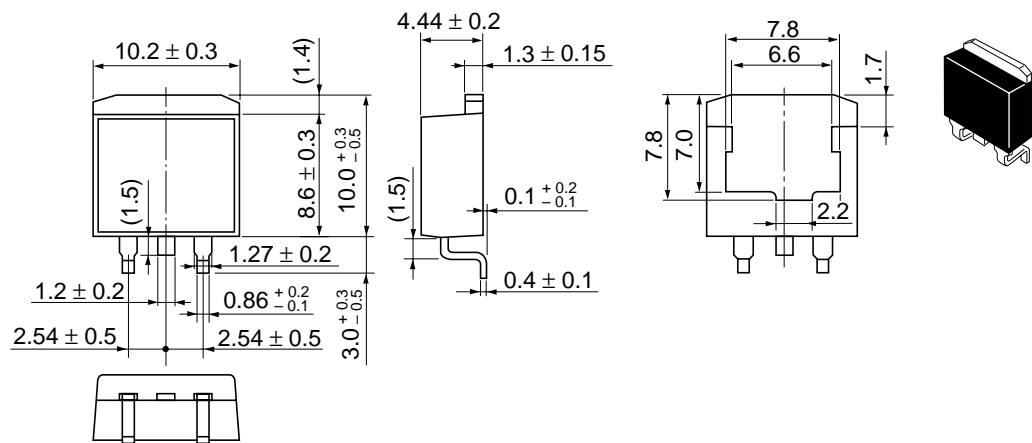
Package Dimensions

As of January, 2001  
Unit: mm



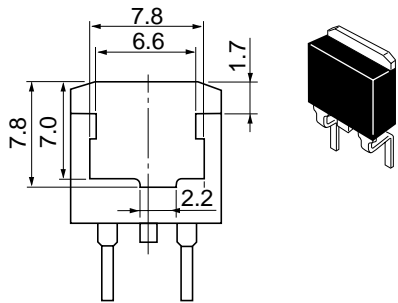
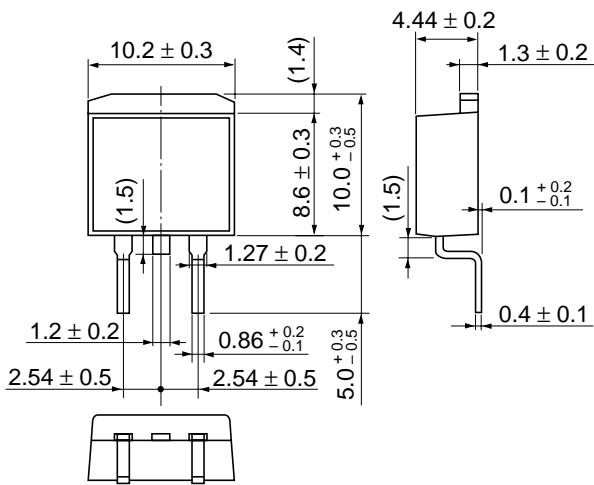
Hitachi Code	LDPAK (L)
JEDEC	—
EIAJ	—
Mass (reference value)	1.4 g

As of January, 2001  
Unit: mm



Hitachi Code	LDBAK (S)-(1)
JEDEC	—
EIAJ	—
Mass (reference value)	1.3 g

As of January, 2001  
Unit: mm



Hitachi Code	LDPAK (S)-(2)
JEDEC	—
EIAJ	—
Mass (reference value)	1.35 g

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