Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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PHOTOCOUPLER

PS9317L,PS9317L2

HIGH CMR, 10 Mbps, OPEN COLLECTOR OUTPUT TYPE, 8 mm CREEPAGE 6-PIN SDIP PHOTOCOUPLER -NEPOC Series-

DESCRIPTION

The PS9317L and PS9317L2 are optical coupled high-speed, active low type isolators containing a GaAlAs LED on the input side and a photodiode and a signal processing circuit on the output side on one chip.

The PS9317L and PS9317L2 are designed specifically for high common mode transient immunity (CMR) and low pulse width distortion.

The PS9317L is lead bending type (Gull-wing) for surface mounting.

The PS9317L2 is lead bending type for long creepage distance (Gull-wing) for surface mount.

FEATURES

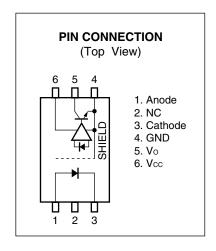
- Pulse width distortion (| tPHL tPLH | = 35 ns MAX.)
- High common mode transient immunity (CMH, CML = ± 15 kV/ μ s MIN.)
- · Half size of 8-pin DIP
- · Long creepage distance (8 mm MIN.: PS9317L2)
- High-speed (10 Mbps)
- High isolation voltage (BV = 5 000 Vr.m.s.)
- · Open collector output
- Pb-Free product
- Safety standards
 - UL approved: No. E72422
 - CSA approved: No. CA 101391 (CA5A, CAN/CSA-C22.2 60065, 60950)
 - DIN EN60747-5-2 (VDE0884 Part2) approved: No. 40024069 (Option)

APPLICATIONS

- Measurement equipment
- PDP

<R>

FA Network



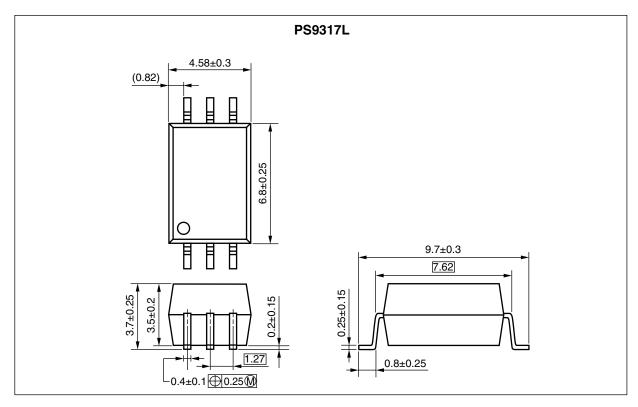
TRUTH TABLE

LED	Output			
ON	L			
OFF	Н			

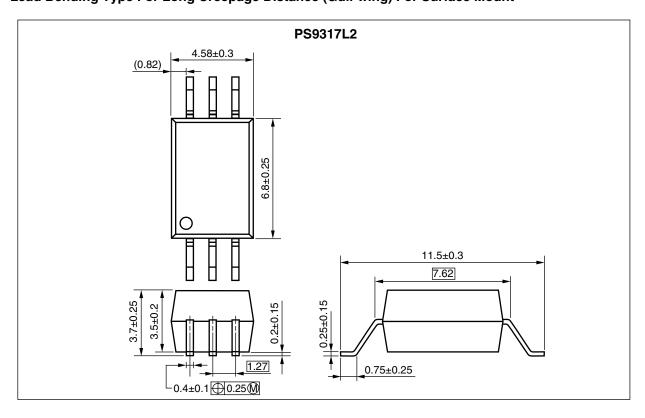
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PACKAGE DIMENSIONS (UNIT: mm)

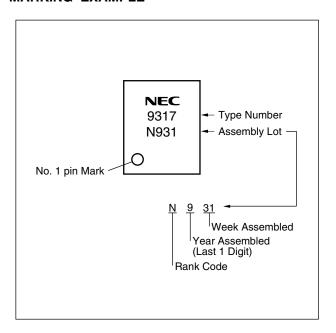
Lead Bending Type (Gull-wing) For Surface Mount



Lead Bending Type For Long Creepage Distance (Gull-wing) For Surface Mount



<R> MARKING EXAMPLE



PHOTOCOUPLER CONSTRUCTION

Parameter	PS9317L	PS9317L2
Air Distance (MIN.)	7 mm	8 mm
Outer Creepage Distance (MIN.)	7 mm	8 mm
Isolation Distance (MIN.)	0.4 mm	0.4 mm

ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number ^{¹¹}
PS9317L	PS9317L-AX	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard products	PS9317L
PS9317L-E3	PS9317L-E3-AX	(Ni/Pd/Au)	Embossed Tape 2 000 pcs/reel	(UL, CSA approved)	
PS9317L2	PS9317L2-AX		20 pcs (Tape 20 pcs cut)		PS9317L2
PS9317L2-E3	PS9317L2-E3-AX		Embossed Tape 2 000 pcs/reel		
PS9317L-V	PS9317L-V-AX		20 pcs (Tape 20 pcs cut)	DIN EN60747-5-2	PS9317L
PS9317L-V-E3	PS9317L-V-E3-AX		Embossed Tape 2 000 pcs/reel	(VDE0884 Part2)	
PS9317L2-V	PS9317L2-V-AX		20 pcs (Tape 20 pcs cut)	Approved (Option)	PS9317L2
PS9317L2-V-E3	PS9317L2-V-E3-AX		Embossed Tape 2 000 pcs/reel		

^{*1} For the application of the Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)

Parameter		Symbol	Ratings	Unit
Diode	Forward Current ¹	lF	20	mA
	Reverse Voltage	VR	5	٧
Detector	Supply Voltage	Vcc	7	٧
	Output Voltage	Vo	7	٧
	Output Current	lo	25	mA
	Power Dissipation ²	Pc	40	mW
Isolation	Voltage ^{*3}	BV	5 000	Vr.m.s.
Operating Ambient Temperature		TA	-40 to +85	°C
Storage Temperature		Tstg	-55 to +125	°C

^{*1} Reduced to 0.3 mA/ $^{\circ}$ C at T_A = 65 $^{\circ}$ C or more.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Low Level Input Voltage	V _{FL}	0		0.8	V
High Level Input Current	lғн	6		12	mA
Supply Voltage	Vcc	4.5	5.0	5.5	V
TTL (R∟ = 1 kΩ, loads)	N			5	
Pull-up Resistor	RL	330		4 k	Ω

^{*2} Applies to output pin Vo (collector pin). Reduced to 1.5 mW/ $^{\circ}$ C at T_A = 65 $^{\circ}$ C or more.

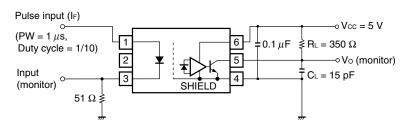
^{*3} AC voltage for 1 minute at $T_A = 25^{\circ}C$, RH = 60% between input and output. Pins 1-3 shorted together, 4-6 shorted together.

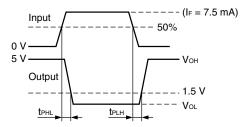


<R> ELECTRICAL CHARACTERISTICS (Ta = -40 to +85°C, unless otherwise specified)

	Parameter	Symbol	Conditions	MIN.	TYP. ^{*1}	MAX.	Unit
Diode	Forward Voltage	VF	IF = 10 mA, T _A = 25°C	1.2	1.56	1.9	٧
	Reverse Current	IR	VR = 3 V, TA = 25°C			10	μА
	Terminal Capacitance	Ct	V = 0 V, f = 1 MHz, T _A = 25°C		30		pF
Detector	High Level Output Current	Іон	Vcc = Vo = 5.5 V, V _F = 0.8 V		1	100	μΑ
	Low Level Output Voltage ²	Vol	$V_{CC} = 5.5 \text{ V}, I_F = 5 \text{ mA}, I_{OL} = 13 \text{ mA}$		0.16	0.6	٧
	High Level Supply Current	Іссн	Vcc = 5.5 V, I _F = 0 mA, Vo = open		3.5	7	mA
	Low Level Supply Current	Iccl	Vcc = 5.5 V, I _F = 10 mA, Vo = open		6.5	10	mA
Coupled	Threshold Input Current	IFHL	$Vcc = 5 \text{ V}, Vo = 0.8 \text{ V}, RL = 350 \Omega$		2	5	mA
	$(H \to L)$						
	Isolation Resistance	R _{I-O}	V _{I-O} = 1 kV _{DC} , RH = 40 to 60%, T _A = 25°C	1011			Ω
	Isolation Capacitance	CI-O	V = 0 V, f = 1 MHz, T _A = 25°C		0.7		pF
	Propagation Delay Time	tрнL	$Vcc = 5 \text{ V}, \text{ RL} = 350 \ \Omega, \text{ CL} = 15 \text{ pF},$		40	75	ns
	$(H \to L)^{*_3}$		IF = 7.5 mA, VTHHL = VTHLH = 1.5 V				
	Propagation Delay Time	t PLH			35	75	
	$(L \rightarrow H)^{*_3}$						
	Rise Time	tr			20		
	Fall Time	tr			5		
	Pulse Width Distortion (PWD)	тенц—тецн			5	35	
	Propagation Delay Skew	t psk				40	
	Common Mode Transient Immunity at High Level Output ^{*4}	СМн	$V_{\rm CC} = 5 \text{ V, } R_{\rm L} = 350 \ \Omega, T_{\rm A} = 25^{\circ} \text{C},$ $I_{\rm F} = 0 \text{ mA, } V_{\rm O} > 2 \text{ V, } V_{\rm CM} = 1.5 \text{ kV}$	15			kV/μs
	Common Mode Transient Immunity at Low Level Output ^{*4}	CM∟	$V_{\text{CC}} = 5 \text{ V}, \text{ RL} = 350 \ \Omega, \text{ TA} = 25^{\circ}\text{C},$ $I_{\text{F}} = 7.5 \text{ mA}, \text{ Vo} < 0.8 \text{ V}, \text{ V}_{\text{CM}} = 1.5 \text{ kV}$	15			kV/μs

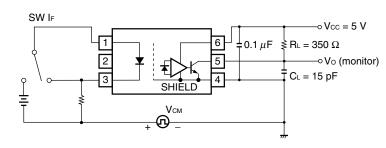
- *1 Typical values at $T_A = 25^{\circ}C$.
- *2 Because VoL of 2 V or more may be output when the LED current is input and when output supply of Vcc = 2.6 V or less, it is important to confirm the characteristics (operation with the power supply on and off) during design, before using this device.
- *3 Test circuit for propagation delay time

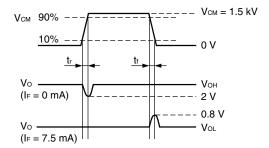




Remark CL includes probe and stray wiring capacitance.

*4 Test circuit for common mode transient immunity





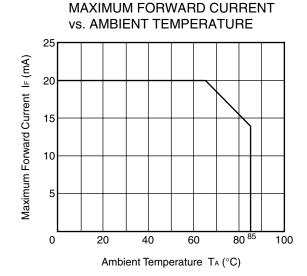
Remark CL includes probe and stray wiring capacitance.

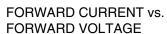
USAGE CAUTIONS

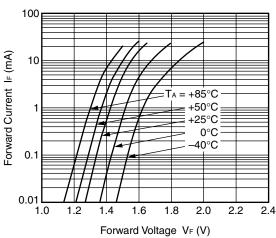
- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. By-pass capacitor of more than 0.1 μ F is used between Vcc and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
- 3. Pin 2 (which is an NC⁻¹ pin) can either be connected directly to the GND pin on the LED side or left open. Unconnected pins should not be used as a bypass for signals or for any other similar purpose because this may degrade the internal noise environment of the device.
 - *1 NC: Not connected (No connection)
- 4. Avoid storage at a high temperature and high humidity.

<R>

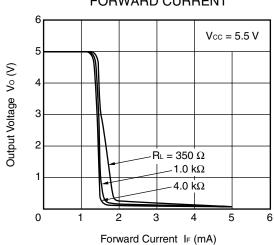
<R> TYPICAL CHARACTERISTICS (Ta = 25°C, unless otherwise specified)



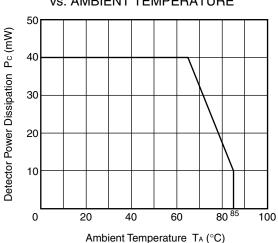




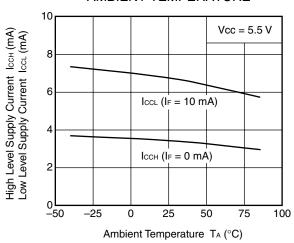
OUTPUT VOLTAGE vs. FORWARD CURRENT



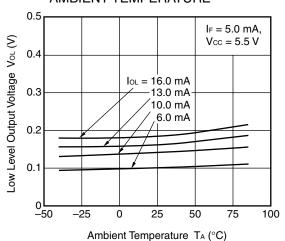
DETECTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE



SUPPLY CURRENT vs. AMBIENT TEMPERATURE

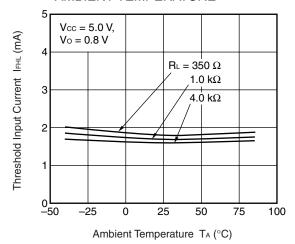


LOW LEVEL OUTPUT VOLTAGE vs. AMBIENT TEMPERATURE

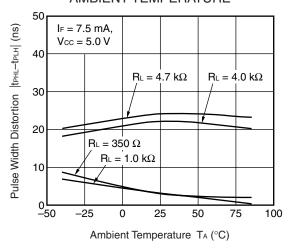


Remark The graphs indicate nominal characteristics.

THRESHOLD INPUT CURRENT vs. AMBIENT TEMPERATURE

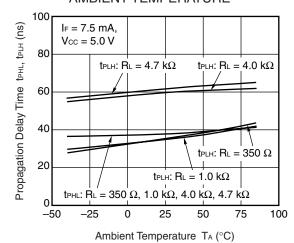


PULSE WIDTH DISTORTION vs. AMBIENT TEMPERATURE

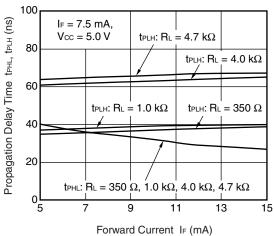


Remark The graphs indicate nominal characteristics.

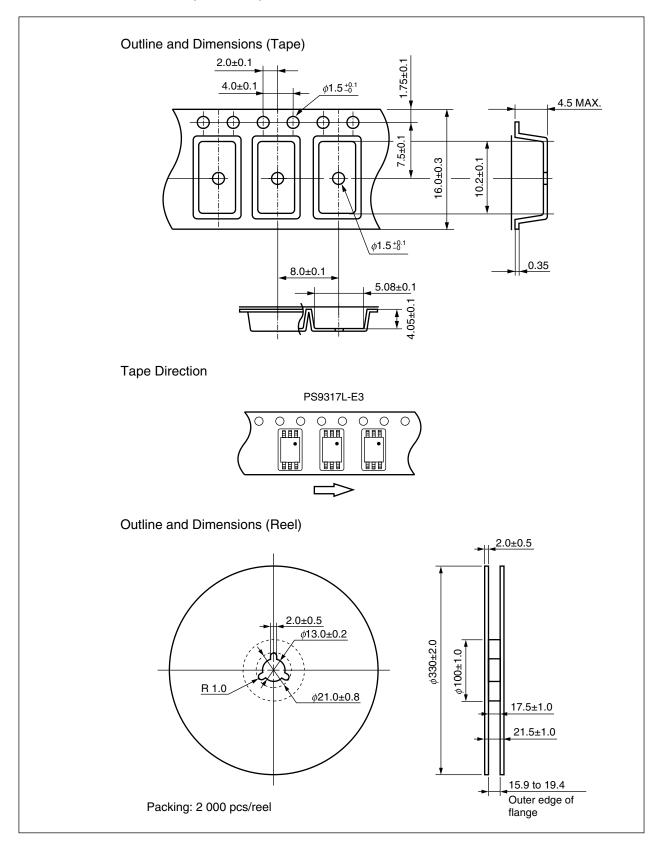
PROPAGATION DELAY TIME vs. AMBIENT TEMPERATURE

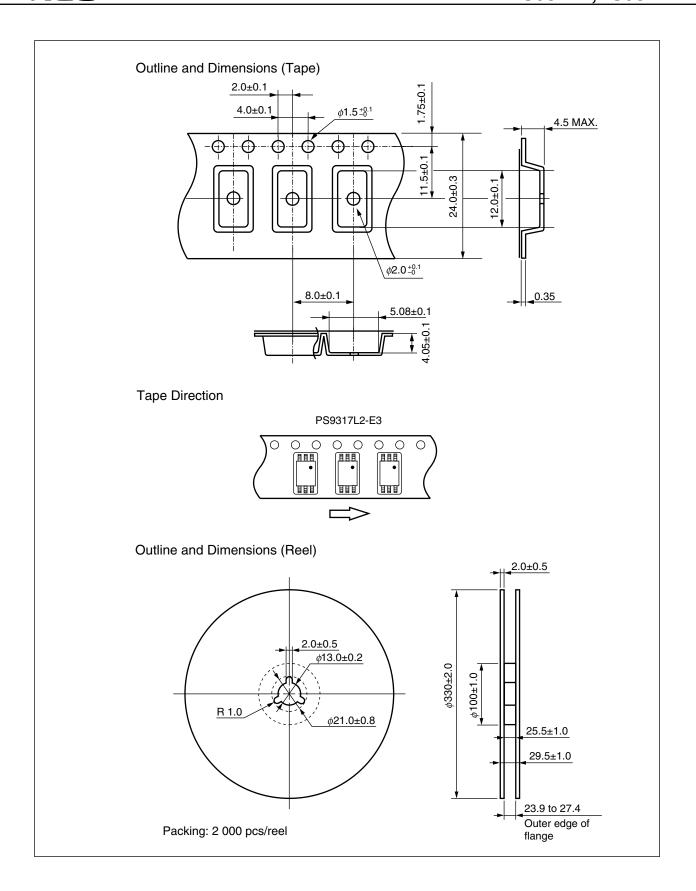


PROPAGATION DELAY TIME vs. **FORWARD CURRENT**



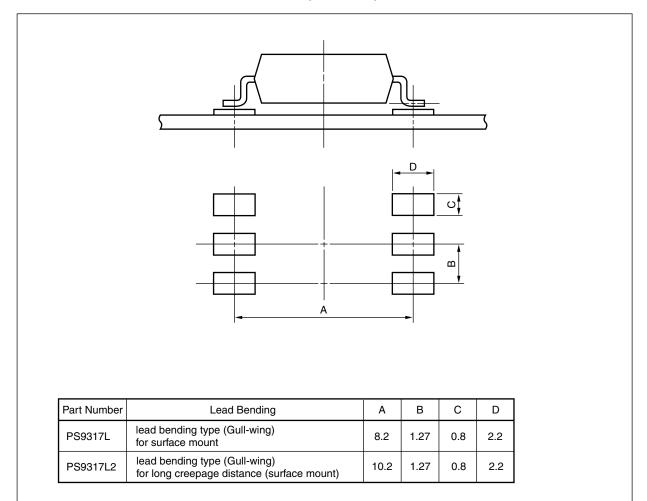
<R> TAPING SPECIFICATIONS (UNIT: mm)







<R> RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



<R> NOTES ON HANDLING

1. Recommended soldering conditions

(1) Infrared reflow soldering

Peak reflow temperature
 260°C or below (package surface temperature)

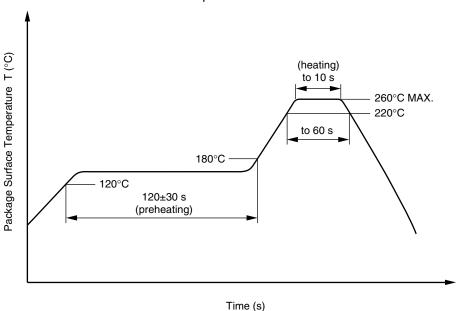
Time of peak reflow temperature
 Time of temperature higher than 220°C
 10 seconds or less
 60 seconds or less

Time to preheat temperature from 120 to 180°C 120±30 s
 Number of reflows Three

• Flux Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

Recommended Temperature Profile of Infrared Reflow



(2) Wave soldering

• Temperature 260°C or below (molten solder temperature)

• Time 10 seconds or less

• Preheating conditions 120°C or below (package surface temperature)

Number of times
 One (Allowed to be dipped in solder including plastic mold portion.)

• Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine

content of 0.2 Wt% is recommended.)

(3) Soldering by soldering iron

Peak temperature (lead part temperature) 350°C or below
 Time (each pins) 3 seconds or less

Flux
 Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead.

(b) Please be sure that the temperature of the package would not be heated over 100°C.



(4) Cautions

Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output side may enter the on state, even if the voltage is within the absolute maximum ratings.

13



<R> SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Spec.	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		40/85/21	
Dielectric strength			
maximum operating isolation voltage	UIORM	1 130	V_{peak}
Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.5 \times U_{IORM}$, $P_d < 5$ pC	U_pr	1 695	V_{peak}
Test voltage (partial discharge test, procedure b for all devices) $U_{pr} = 1.875 \times U_{\text{IORM}}, P_{d} < 5 \; pC$	Upr	2 119	V _{peak}
Highest permissible overvoltage	Utr	8 000	V _{peak}
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	CTI	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		III a	
Storage temperature range	T _{stg}	-55 to +125	°C
Operating temperature range	TA	-40 to +85	°C
Isolation resistance, minimum value			
$V_{10} = 500 \text{ V dc at } T_A = 25^{\circ}\text{C}$	Ris MIN.	10 ¹²	Ω
V _{IO} = 500 V dc at T _A MAX. at least 100°C	Ris MIN.	10 ¹¹	Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve)			
Package temperature	Tsi	175	°C
Current (input current I _F , Psi = 0)	Isi	400	mA
Power (output or total power dissipation)	Psi	700	mW
Isolation resistance			
V _{IO} = 500 V dc at T _A = Tsi	Ris MIN.	10°	Ω

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M8E0904E

Caution

GaAs Products

This product uses gallium arsenide (GaAs).

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- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
- Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or i any way allow it to enter the mouth.