TITLE: SPECIFICATION CONTROL DRAWING





X TEMPERATURE COEFFICIENT OF ATTENUATION. 1 X 10 -3 DB/DB/°C ATTENUATION SHIFT NEGATIVE

X DB VALUE

SHIFT	
(NEG)	DB VALUE
00 <u>3</u>	1, 2, 3, 4, 5, 6, 8
00 <u>4</u>	1, 2, 3, 4, 6
00 <u>5</u>	1, 2, 3, 4, 5, 6
00 <u>6</u>	2 ,3, 4
00 <u>7</u>	2, 3, 4, 5, 6
00 <u>9</u>	3, 4, 6

DESCRIPTION: CHIP ATTENUATOR NETWORK, ROHS COMPLIANT

ASSEMBLY DWG: N/A

1.0 SPECIFICATIONS:

- 1.1 ELECTRICAL:
 - 1.1.1 IMPEDANCE: 50 OHMS NOMINAL.
 - 1.1.2 OPERATING FREQUENCY RANGE: DC 6.0 GHz
 - 1.1.3 ATTENUATION VALUES AVAILABLE: SEE ABOVE DESCRIPTION.
 - 1.1.4 ATTENUATION ACCURACY AT 25°C: ±.75 dB AT 2 GHz.
 - 1.1.5 VSWR: 1.30:1 MAX @ 1 GHz
 - 1.1.6 INPUT POWER: 200 MILLIWATTS.

1.1.6.1 FULL RATED POWER TO 105°C DERATED TO 0 WATTS AT 125°C.

- 1.2 MECHANICAL:
 - 1.2.1 OUTLINE DWG: SEE SHEET 2.
- 1.3 ENVIRONMENTAL:
 - 1.3.1 OPERATING TEMPERATURE RANGE: -35°C TO + 125°C.
- **2.0 UNIT MARKING:** DB VALUE (X), DIRECTION OF SHIFT (N) AND TCA SHIFT (X).

LEGIBILITY AND PERMANENCY PER MIL-STD-130.

3.0 QUALITY ASSURANCE:

- 3.1 SAMPLE INSPECT PER ANSI/ASQC Z1.4 GENERAL INSPECTION, LEVEL II, AQL = 1.0.
 - 3.1.1 VISUAL AND MECHANICAL EXAMINATION FOR CONFORMANCE TO OUTLINE DRAWING REQUIREMENTS.
- 3.2 SAMPLE INSPECTION (DESTRUCTIVE TESTING).
 - 3.2.1 SELECT THREE (3) UNITS FROM LOT AND MEASURE DCA EVERY 20°C OVER THE TEMPERATURE RANGE -55°C TO +125°C.
 - 3.2.1.1 CALCULATE, USING LINEAR REGRESSION, THE SLOPE OF THE CURVE.
 - 3.2.1.2 CALCULATE TCA USING THE FOLLOWING FORMULA:

TCA = SLOPE

ATTENUATION @ 25°C

- 3.2.1.3 ACCEPTANCE LIMITS: PER 1.1.7.
- 3.3 INSPECTION IN ACCORDANCE WITH 824W170 AND 824F036 FOR COMMERCIAL GRADE PRODUCT.
- 3.4 TEST DATA REQUIREMENTS:
 - 3.4.1 NO TEST DATA REQUIRED FOR CUSTOMER.
 - 3.4.2 DATA RETENTION 24 MONTHS.
- **4.0 PACKAGING:** STANDARD TAPE AND REEL PACK PER 755W002.

EMC TECHNOLOGY	CA	DWG#	1011275000			
8851 SW OLD KANSAS AVE.	CHANGE NOTICE	EN 09-E0733	REV LVL	C		
STUART, FL 34997			SHEET	1	OF	2

