

# **DVTR2800S Series**

## HIGH RELIABILITY HYBRID DC-DC CONVERTERS

#### **DESCRIPTION**

is operable over the full military (-55 °C to +125 °C) temperature range with no power derating. Unique to the DVTR series are robust and effective input and output filters which provide dramatically reduced input and output noise performance when compared to other manufacturers competing devices. Operating at a nominal fixed frequency of 475 kHz, these regulated, isolated units utilize a high speed magnetic feedback design and well controlled undervoltage lockout circuitry to eliminate slow start-up problems.

The DVTR series of high reliability DC-DC converters

These converters are designed and manufactured in a facility qualified to ISO9001 and certified to MIL-PRF-38534 and MIL-STD-883.

This product may incorporate one or more of the following U.S. patents:

5,784,266 5,790,389 5,963,438 5,999,433 6,005,780 6,084,792 6,118,673

#### **FEATURES**

- High Reliability
- Very Low Output Noise
- Wide Input Voltage Range: 15 to 50 Volts per MIL-STD-704
- Up to 40 Watts Output Power
- Fault Tolerant Magnetic Feedback Circuit
- Output Voltage Trim Up +10%
- NO Use of Optoisolators
- Undervoltage Lockout
- Indefinite Short Circuit Protection
- Current Limit Protection
- Industry Standard Pinout
- High Input Transient Voltage: 80 Volts for 1 sec per MIL-STD-704A
- Precision Seam Seal or Solder Seal Hermetic Package
- High Power Density: > 40 W/in<sup>3</sup>
- Custom Versions Available
- Additional Environmental Screening Available
- Meets MIL-STD-461C and MIL-STD-461D EMC Requirements When Used With a DVMC28 EMI Filter
- Flanged and Non-flanged Versions Available.
- MIL-PRF-38534 Element Evaluated Components
- Space Applications should consider VPT's "S" Series of Radiation Tolerant Power Conversion Devices. Contact VPT for details.

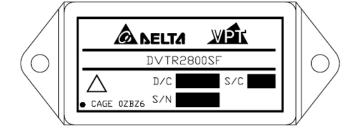


Figure 1 – DVTR2800S / DVTR2800SF DC-DC Converter (Exact marking may differ from that shown)





#### **ABSOLUTE MAXIMUM RATINGS**

Input Voltage (Continuous) 50 V<sub>DC</sub> Junction Temperature Rise to Case +15°C
Input Voltage (Transient, 1 second) 80 Volts Storage Temperature
Output Power<sup>1</sup> 40 Watts Lead Solder Temperature (10 seconds) 270°C

Power Dissipation (Full Load, T<sub>CASE</sub> = +125°C) 13 Watts Weight (Maximum) (Un-Flanged / Flanged) (52 / 55) Grams

Parameter	Conditions	D	VTR282R	5S	DVTR283R3S			Units
Parameter	Conditions	Min	Тур	Max	Min	Тур	Max	
STATIC								
INPUT _	Continuous	15	28	50	15	28	50	V
Voltage <sup>4</sup>	Transient, 1 sec	-	-	80	-	-	80	V
Current	Inhibited	-	4	6	-	4	6	mA
Current	No Load	-	50	90	-	50	90	mA
Ripple Current	Full Load, 20Hz to 10MHz	-	15	50	-	20	50	mA <sub>p-p</sub>
Inhibit Pin Input <sup>4</sup>		0	-	1.5	0	-	1.5	V
Inhibit Pin Open Circuit Voltage <sup>4</sup>		9.0	11.0	13.0	9.0	11.0	13.0	V
UVLO Turn On		12.0	-	14.8	12.0	-	14.8	V
UVLO Turn Off⁴		11.0	-	14.5	11.0	-	14.5	V
OUTPUT Vou	T <sub>CASE</sub> = 25°C	2.47	2.5	2.53	3.25	3.3	3.35	V
Voltage V <sub>ou</sub>	T <sub>CASE</sub> = -55°C to +125°C	2.46	2.5	2.54	3.2	3.3	3.4	V
Power <sup>3</sup>		0	-	15	0	-	20	W
Current <sup>3</sup> V <sub>OL</sub>	т	0	-	6.0	0	-	6.06	Α
Ripple Voltage Vol	Full Load, 20Hz to 10MHz	-	20	50	-	25	50	$mV_{p-p}$
Line Regulation Vol	<sub>T</sub> V <sub>IN</sub> = 15V to 50V	-	2	20	-	2	20	mV
Load Regulation V <sub>OL</sub>	No Load to Full Load	-	2	50	-	2	50	mV
EFFICIENCY		63	66	-	65	70	-	%
LOAD FAULT POWER DISSIPATION	4 Overload	-	-	18	-	-	16	W
LOAD FAULT POWER DISSIPATION	Short Circuit	-	-	18	-	-	16	W
CAPACITIVE LOAD <sup>4</sup>		-	-	1000	-	-	1000	μF
SWITCHING FREQUENCY		400	475	550	400	475	550	kHz
SYNC FREQUENCY RANGE	V <sub>H</sub> - V <sub>L</sub> = 5V, DC = 20-80%	500	-	600	500	-	600	kHz
ISOLATION	500 V <sub>DC</sub>	100	-	-	100	-	-	ΜΩ
MTBF (MIL-HDBK-217F)	AIF @ T <sub>C</sub> = 55°C	-	413	-	-	413	-	kHrs
DYNAMIC								
Load Step Output Transient Vol	Half Load to Full Load	-	90	400	-	180	500	$mV_{PK}$
Load Step Recovery <sup>2</sup>	I IAII LUAU IU FUII LUAU	-	280	500	-	260	500	μSec
Line Step Output Transient <sup>4</sup> V <sub>OU</sub>	T	-	300	450	-	300	600	$mV_{PK}$
Line Step Recovery <sup>2, 4</sup>	$V_{IN} = 16V \text{ to } 40V$	-	300	450	-	300	500	μSec
Turn On Delay V <sub>ou</sub>	T	-	10	20	-	10	20	mSec
Turn On Overshoot	$V_{IN} = 0V \text{ to } 28V$	-	0	15	-	0	15	$mV_{PK}$

Notes: 1. Dependant on output voltage.

- 2. Time for output voltage to settle within 1% of its nominal value.
- 3. Derate linearly to 0 at 135°C.
- 4. Verified by qualification testing.





#### **ABSOLUTE MAXIMUM RATINGS**

Input Voltage (Continuous) 50 V<sub>DC</sub> Junction Temperature Rise to Case +15°C
Input Voltage (Transient, 1 second) 80 Volts Storage Temperature
Output Power<sup>1</sup> 40 Watts Lead Solder Temperature (10 seconds) 270°C

Power Dissipation (Full Load,  $T_{CASE} = +125^{\circ}C$ ) 13 Watts Weight (Maximum) (Un-Flanged / Flanged) (52 / 55) Grams

Parameter		Conditions		DVTR2805S			DVTR285R2S		
Parameter		Conditions	Min	Тур	Max	Min	Тур	Max	Units
STATIC									
INPUT		Continuous	15	28	50	15	28	50	V
Voltage⁴		Transient, 1 sec	-	-	80	-	-	80	V
Current		Inhibited	-	4	6	-	4	6	mA
Current		No Load	-	55	90	-	55	90	mA
Ripple Current		Full Load, 20Hz to 10MHz	-	30	50	-	30	50	mA <sub>p-p</sub>
Inhibit Pin Input <sup>4</sup>			0	-	1.5	0	-	1.5	V
Inhibit Pin Open Circuit Voltag	je <sup>4</sup>		9.0	11.0	13.0	9.0	11.0	13.0	V
UVLO Turn On			12.0	-	14.8	12.0	-	14.8	V
UVLO Turn Off⁴			11.0	-	14.5	11.0	-	14.5	V
OUTPUT	$V_{\text{OUT}}$	T <sub>CASE</sub> = 25°C	4.95	5	5.05	5.14	5.2	5.26	V
Voltage	$V_{\text{OUT}}$	T <sub>CASE</sub> = -55°C to +125°C	4.875	5	5.125	5.07	5.2	5.33	V
Power <sup>3</sup>			0	-	30	0	-	30	W
Current <sup>3</sup>	$V_{\text{OUT}}$		0	-	6	0	-	6	Α
Ripple Voltage	V <sub>OUT</sub>	Full Load, 20Hz to 10MHz	-	20	50	-	20	50	$mV_{p-p}$
Line Regulation	$V_{\text{OUT}}$	V <sub>IN</sub> = 15V to 50V	-	2	20	-	2	20	mV
Load Regulation	$V_{\text{OUT}}$	No Load to Full Load	-	2	50	-	2	50	mV
EFFICIENCY			72	78	-	72	78	-	%
LOAD FAULT POWER DISSIPAT	ION <sup>4</sup>	Overload	-	-	16	-	-	16	W
LOAD FAULT FOWER DISSIPAT	ION	Short Circuit	-	-	16	-	-	16	W
CAPACITIVE LOAD⁴			-	-	1000	-	-	1000	μF
SWITCHING FREQUENCY			400	475	550	400	475	550	kHz
SYNC FREQUENCY RANGE		V <sub>H</sub> - V <sub>L</sub> = 5V, DC = 20-80%	500	-	600	500	-	600	kHz
ISOLATION		500 V <sub>DC</sub>	100	-	-	100	-	-	ΜΩ
MTBF (MIL-HDBK-217F)		AIF @ T <sub>C</sub> = 55°C	-	413	-	-	413	-	kHrs
DYNAMIC									
Load Step Output Transient	$V_{\text{OUT}}$	Half Load to Full Load	-	170	500	-	170	500	$mV_{PK}$
Load Step Recovery <sup>2</sup>		TIAII LUAU IU FUII LUAU	-	60	500	-	60	500	μSec
Line Step Output Transient4	$V_{\text{OUT}}$	\/ = 46\/ to 40\/	-	300	600	-	300	600	$mV_{PK}$
Line Step Recovery <sup>2, 4</sup>		$V_{IN}$ = 16V to 40V	-	300	500	-	300	500	μSec
Turn On Delay	V <sub>OUT</sub>	\/ - 0\/ t= 20\/	-	10	20	-	10	20	mSec
Turn On Overshoot		$V_{IN} = 0V \text{ to } 28V$	-	0	25	-	0	90 50 1.5 13.0 14.8 14.5 5.26 5.33 30 6 50 20 50 - 16 1000 550 600 - - - 500 500 500 500 500	$mV_{PK}$

Notes: 1. Dependant on output voltage.

- 2. Time for output voltage to settle within 1% of its nominal value.
- 3. Derate linearly to 0 at 135°C.
- 4. Verified by qualification testing.





#### **ABSOLUTE MAXIMUM RATINGS**

Input Voltage (Continuous)  $50 V_{DC}$ Junction Temperature Rise to Case +15°C Input Voltage (Transient, 1 second) 80 Volts Storage Temperature -65°C to +150°C Output Power<sup>1</sup> 40 Watts Lead Solder Temperature (10 seconds) 270°C

Power Dissipation (Full Load,  $T_{CASE} = +125^{\circ}C$ ) 13 Watts Weight (Maximum) (Un-Flanged / Flanged) (52 / 55) Grams

Dorometer		Conditions	С	DVTR2807S			DVTR2808S			
Parameter		Conditions	Min	Тур	Max	Min	Тур	Тур Мах		
STATIC										
INPUT		Continuous	15	28	50	15	28	50	V	
Voltage⁴	•	Transient, 1 sec	-	-	80	-	-	80	V	
Current		Inhibited	-	4	6	-	4	6	mA	
Current	•	No Load	-	65	90	-	65	90	mA	
Ripple Current		Full Load, 20Hz to 10MHz	-	30	50	-	30	50	mA <sub>p-p</sub>	
Inhibit Pin Input⁴			0	-	1.5	0	-	1.5	V	
Inhibit Pin Open Circuit Voltage	e <sup>4</sup>		9.0	11.0	13.0	9.0	11.0	13.0	V	
UVLO Turn On			12.0	-	14.8	12.0	-	14.8	V	
UVLO Turn Off⁴			11.0	-	14.5	11.0	-	14.5	V	
OUTPUT	V <sub>OUT</sub>	T <sub>CASE</sub> = 25°C	6.93	7	7.07	7.92	8	8.08	V	
Voltage	V <sub>OUT</sub>	$T_{CASE}$ = -55°C to +125°C	6.895	7	7.105	7.88	8	8.12	V	
Power <sup>3</sup>			0	-	35	0	-	35	W	
Current <sup>3</sup>	V <sub>OUT</sub>		0	-	5	0	-	4.4	Α	
Ripple Voltage	<b>√</b> out	Full Load, 20Hz to 10MHz	-	30	50	-	30	50	$mV_{p-p}$	
Line Regulation	V <sub>out</sub>	V <sub>IN</sub> = 15V to 50V	-	2	20	-	2	20	mV	
Load Regulation	V <sub>OUT</sub>	No Load to Full Load	-	2	50	-	2	50	mV	
EFFICIENCY			73	77	-	73	78	-	%	
LOAD FAULT POWER DISSIPATION	ON14	Overload	-	-	16	-	-	16	W	
LOAD FAULT POWER DISSIPATION	OIN	Short Circuit	-	-	16	-	-	16	W	
CAPACITIVE LOAD <sup>4</sup>			-	-	1000	-	-	1000	μF	
SWITCHING FREQUENCY			400	475	550	400	475	550	kHz	
SYNC FREQUENCY RANGE		V <sub>H</sub> - V <sub>L</sub> = 5V, DC = 20-80%	500	-	600	500	-	600	kHz	
ISOLATION		500 V <sub>DC</sub>	100	-	-	100	-	-	ΜΩ	
MTBF (MIL-HDBK-217F)		AIF @ T <sub>C</sub> = 55°C	-	413	-	-	413	-	kHrs	
DYNAMIC										
Load Step Output Transient	V <sub>out</sub>	Half Load to Full Load	-	300	600	-	300	600	$mV_{PK}$	
Load Step Recovery <sup>2</sup>		Hall Load to Full Load	-	250	500	-	250	500	μSec	
Line Step Output Transient4	V <sub>OUT</sub>	\/ = 40\/ to 40\/	-	400	700	-	400	700	$mV_{PK}$	
Line Step Recovery <sup>2, 4</sup>		$V_{IN}$ = 16V to 40V	-	300	500	-	300	500	μSec	
Turn On Delay	V <sub>OUT</sub>	\/ - 0\/45 20\/	-	10	20	-	10	20	mSec	
Turn On Overshoot		$V_{IN} = 0V \text{ to } 28V$	-	0	25	-	0	25	$mV_{PK}$	

Notes: 1. Dependant on output voltage. 2. Time for output voltage to settle within 1% of its nominal value.

3. Derate linearly to 0 at 135°C.

4. Verified by qualification testing.





SPECIFICATIONS (T<sub>CASE</sub> = -55°C to +125°C, V<sub>IN</sub> = +28V ± 5%, Full Load, Unless Otherwise Specified)

#### **ABSOLUTE MAXIMUM RATINGS**

Input Voltage (Continuous)  $50 V_{DC}$ Junction Temperature Rise to Case +15°C Input Voltage (Transient, 1 second) -65°C to +150°C 80 Volts Storage Temperature Output Power<sup>1</sup> 270°C 40 Watts Lead Solder Temperature (10 seconds) Power Dissipation (Full Load,  $T_{CASE} = +125^{\circ}C$ ) 13 Watts Weight (Maximum) (Un-Flanged / Flanged) (52 / 55) Grams

**DVTR2812S DVTR2815S Parameter Conditions** Units Min Typ Max Min Typ Max **STATIC** V **INPUT** Continuous 15 28 50 15 28 50 Voltage<sup>4</sup> Transient, 1 sec -80 -80 V 6 Inhibited 4 6 4 mA Current No Load \_ 60 90 -60 90 mΑ Ripple Current Full Load, 20Hz to 10MHz 30 50 30 50  $mA_{p-p}$ Inhibit Pin Input⁴ 0 \_ 1.5 0 1.5 ٧ Inhibit Pin Open Circuit Voltage4 13.0 ٧ 9.0 11.0 13.0 9.0 11.0 **UVLO Turn On** 12.0 -14.8 12.0 14.8 ٧ UVLO Turn Off⁴ ٧ 11.0 14.5 11.0 14.5 OUTPUT  $V_{OUT}$  $T_{CASE} = 25^{\circ}C$ 11.88 12 12.12 14.85 15 15.15 V Voltage  $T_{CASE} = -55^{\circ}C$  to  $+125^{\circ}C$ 14.625 15.375 ٧  $V_{\text{OUT}}$ 11.7 12 12.3 15 Power<sup>3</sup> 0 40 0 40 W Current<sup>3</sup>  $V_{OUT}$ 3.33 0 2.67 Α Ripple Voltage  $V_{OUT}$ Full Load, 20Hz to 10MHz -20 20  $mV_{p-p}$  $V_{IN} = 15V \text{ to } 50V$ Line Regulation  $V_{OUT}$ 20 mV Load Regulation No Load to Full Load 10 50 10 50  $V_{OUT}$ mV **EFFICIENCY** 76 84 77 84 % Overload \_ W 14 14 LOAD FAULT POWER DISSIPATION<sup>4</sup> **Short Circuit** 14 CAPACITIVE LOAD4 500 500 μF SWITCHING FREQUENCY 475 400 475 550 400 550 kHz SYNC FREQUENCY RANGE  $V_H - V_L = 5V$ , DC = 20-80% 500 500 600 kHz 500 V<sub>DC</sub> **ISOLATION** 100 100 MO MTBF (MIL-HDBK-217F) AIF @ T<sub>C</sub> = 55°C 413 413 kHrs **DYNAMIC** Load Step Output Transient  $V_{OUT}$ 450 700 500 700  $mV_{PK}$ Half Load to Full Load Load Step Recovery<sup>2</sup> 200 500 150 500 μSec Line Step Output Transient<sup>4</sup>  $V_{OUT}$ 500 900 500 900  $mV_{PK}$  $V_{IN} = 16V \text{ to } 40V$ Line Step Recovery<sup>2, 4</sup> 300 500 300 500 μSec Turn On Delay  $V_{OUT}$ 10 20 10 20 mSec \_  $V_{IN} = 0V \text{ to } 28V$ Turn On Overshoot 0 50 50  $mV_{PK}$ 

Notes: 1. Dependant on output voltage.

- 2. Time for output voltage to settle within 1% of its nominal value.
- 3. Derate linearly to 0 at 135°C.
- 4. Verified by qualification testing.





#### **ABSOLUTE MAXIMUM RATINGS**

Input Voltage (Continuous) 50 V<sub>DC</sub> Junction Temperature Rise to Case +15°C
Input Voltage (Transient, 1 second) 80 Volts Storage Temperature
Output Power<sup>1</sup> 40 Watts Lead Solder Temperature (10 seconds) 270°C

Power Dissipation (Full Load,  $T_{CASE} = +125$ °C) 13 Watts Weight (Maximum) (Un-Flanged / Flanged) (52 / 55) Grams

Parameter		Conditions	DVTR2818S			DVTR2828S			Units
Parameter		Conditions	Min	Тур	Max	Min	Тур	Тур Мах	
STATIC									
INPUT		Continuous	15	28	50	15	28	50	V
Voltage⁴		Transient, 1 sec	-	-	80	-	-	80	V
Current		Inhibited	-	4	6	-	4	6	mA
Current		No Load	-	60	90	-	60	90	mA
Ripple Current		Full Load, 20Hz to 10MHz	-	30	50	-	30	50	mA <sub>p-p</sub>
Inhibit Pin Input <sup>4</sup>			0	-	1.5	0	-	1.5	V
Inhibit Pin Open Circuit Voltag	ge⁴		9.0	11.0	13.0	9.0	11.0	13.0	V
UVLO Turn On			12.0	-	14.8	12.0	-	14.8	V
UVLO Turn Off⁴			11.0	-	14.5	11.0	-	14.5	V
OUTPUT	$V_{\text{OUT}}$	T <sub>CASE</sub> = 25°C	17.82	18	18.18	27.72	28	28.28	V
Voltage	$V_{\text{OUT}}$	T <sub>CASE</sub> = -55°C to +125°C	17.55	18	18.45	27.30	28	28.70	V
Power <sup>3</sup>			0	-	40	0	-	40	W
Current <sup>3</sup>	$V_{\text{OUT}}$		0	-	2.22	0	-	1.43	Α
Ripple Voltage	$V_{\text{OUT}}$	Full Load, 20Hz to 10MHz	-	20	50	-	30	150	$mV_{p-p}$
Line Regulation	$V_{\text{OUT}}$	V <sub>IN</sub> = 15V to 50V	-	2	20	-	2	150	mV
Load Regulation	$V_{\text{OUT}}$	No Load to Full Load	-	10	50	-	20	150	mV
EFFICIENCY			75	84	-	79	85	-	%
LOAD FAULT POWER DISSIPAT	-IONI <sup>4</sup>	Overload	-	-	14	-	-	14	W
LOAD FAULT POWER DISSIPAT	ION	Short Circuit	-	-	14	-	-	14	W
CAPACITIVE LOAD⁴			-	-	500	-	-	500	μF
SWITCHING FREQUENCY			400	475	550	400	450	550	kHz
SYNC FREQUENCY RANGE		V <sub>H</sub> - V <sub>L</sub> = 5V, DC = 20-80%	500	-	600	500	-	600	kHz
ISOLATION		500 V <sub>DC</sub>	100	-	-	100	-	-	МΩ
MTBF (MIL-HDBK-217F)		AIF @ T <sub>C</sub> = 55°C	-	413	-	-	413	-	kHrs
DYNAMIC									
Load Step Output Transient	$V_{\text{OUT}}$	- Half Load to Full Load	-	500	700	-	500	1000	$mV_{PK}$
Load Step Recovery <sup>2</sup>		rian Load to r un Load	-	200	500	-	250	500	μSec
Line Step Output Transient4	$V_{\text{OUT}}$	\/ = 16\/ to 40\/	-	500	900	-	500	1800	$mV_{PK}$
Line Step Recovery <sup>2, 4</sup>		$V_{IN}$ = 16V to 40V	-	300	500	-	300	500	μSec
Turn On Delay	V <sub>OUT</sub>	\/ = 0\/ t= 20\/	-	10	20	-	10	20	mSec
Turn On Overshoot		$V_{IN} = 0V \text{ to } 28V$	-	0	50	-	0	13.0 14.8 14.5 28.28 28.70 40 1.43 150 150 14 14 500 550 600 1000 500 1800 500	$mV_{PK}$

Notes: 1. Dependant on output voltage.

- 2. Time for output voltage to settle within 1% of its nominal value.
- 3. Derate linearly to 0 at 135°C.
- 4. Verified by qualification testing.



## **DVTR2800S Series**

#### **BLOCK DIAGRAM**

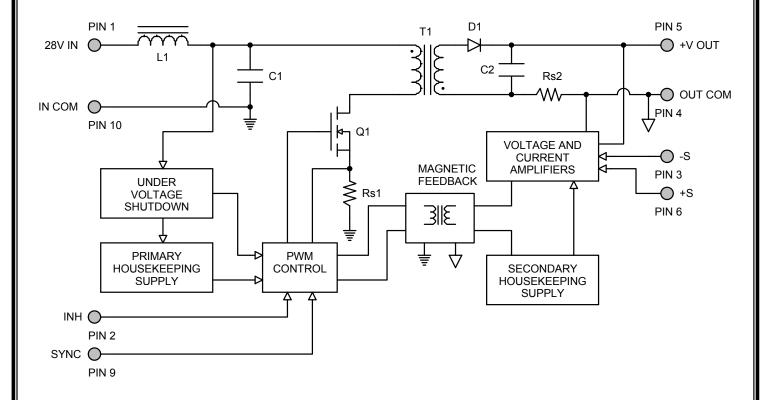


Figure 2

#### **CONNECTION DIAGRAM**

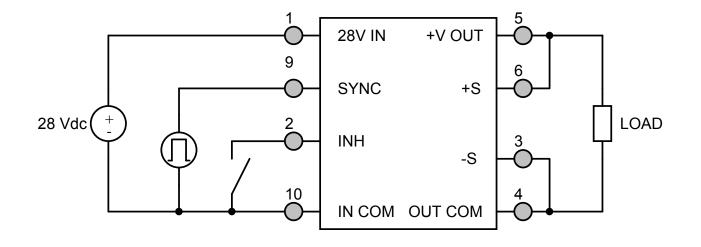
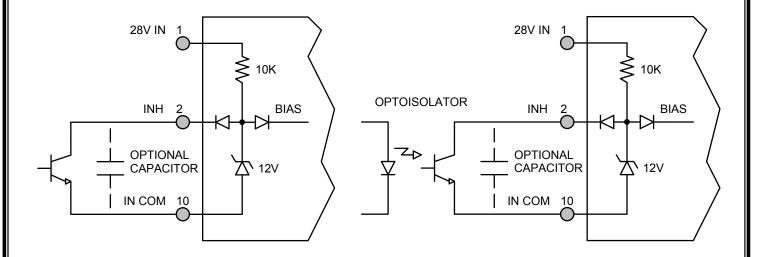


Figure 3



#### INHIBIT DRIVE CONNECTION DIAGRAMS



**Figure 4** – Internal Inhibit Circuit and Recommended Drive (Shown with optional capacitor for turn-on delay)

Figure 5 – Isolated Inhibit Drive (Shown with optional capacitor for turn-on delay)

#### EMI FILTER HOOKUP DIAGRAM

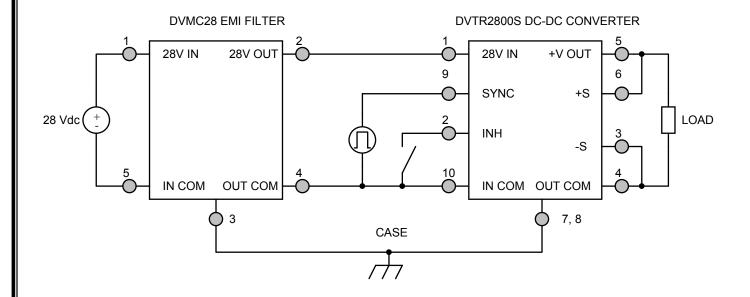
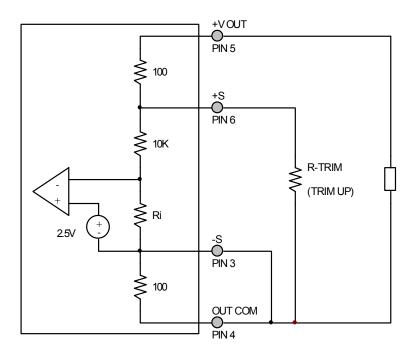


Figure 6 – Converter with EMI Filter



### **OUTPUT VOLTAGE TRIM**



The output voltage can be trimmed up by connecting a resistor between the +S pin (PIN 6) and the OUT COM pin (PIN 4). The maximum trim range is +10%. The appropriate resistor values versus the output voltage are given in the trim table below. The –S pin should be connected to the OUT COM pin.



Figure 7 – Output Voltage Trim

DVTR2	82R5S	DVTR2	283R3S	DVTR	2805S	DVTR2	85R2S
+V <sub>оит</sub> (V)	R <sub>TRIM</sub> (Ω)	+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)
2.75	1.05k	3.60	1.13k	5.50	1.05k	5.70	1.09k
2.70	1.33k	3.55	1.36k	5.45	1.18k	5.65	1.22k
2.65	1.82k	3.50	1.72k	5.40	1.33k	5.60	1.39k
2.60	2.86k	3.45	2.32k	5.35	1.54k	5.55	1.60k
2.55	6.67k	3.40	3.59k	5.30	1.82k	5.50	1.89k
2.50	-	3.35	7.87k	5.25	2.22k	5.45	2.31k
		3.30	-	5.20	2.86k	5.40	2.97k
				5.15	4k	5.35	4.16k
				5.10	6.67k	5.30	6.93k
				5.05	20k	5.25	20.8k
				5.00	-	5.20	-

DVTR	2812S	DVTR	2815S	DVTR2818S		
+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	
13.2	1.09k	16.50	1.09k	19.8	0.99k	
13.1	1.19k	16.40	1.18k	19.6	1.12k	
13.0	1.33k	16.30	1.28k	19.4	1.28k	
12.9	1.49k	16.20	1.4k	19.2	1.49k	
12.8	1.7k	16.10	1.54k	19	1.79k	
12.7	1.98k	16.00	1.71k	18.8	2.23k	
12.6	2.38k	15.90	1.94k	18.6	2.98k	
12.5	2.96k	15.80	2.22k	18.4	4.47k	
12.4	3.94k	15.70	2.61k	18.2	8.94k	
12.3	5.86k	15.60	3.16k	18	-	
12.2	11.4k	15.50	4k			
12.1	242k	15.40	5.46k		-	
12.0	-	15.30	8.57k			
		15.20	20k			
		15.10	Note 1			
		15.00	-			

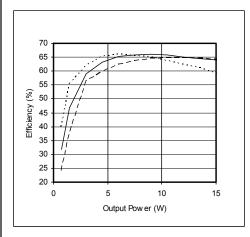
Notes: 1. Connect R-TRIM =  $400\Omega$  from +VOUT (PIN 5) to +S (PIN 6).

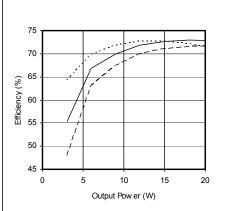


## **DVTR2800S Series**

EFFICIENCY PERFORMANCE CURVES (T<sub>CASE</sub> = 25°C, Full Load, Unless Otherwise Specified)

V <sub>IN</sub> = 16V	V <sub>IN</sub> = 28V	V <sub>IN</sub> = 40V
-----------------------	-----------------------	-----------------------





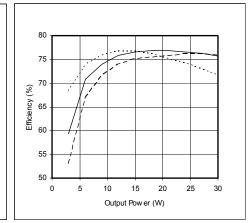


Figure 8 – DVTR282R5S
Efficiency (%) vs. Output Power (W)

Figure 9 – DVTR283R3S Efficiency (%) vs. Output Power (W)

Figure 10 – DVTR2805S / DVTR285R2S Efficiency (%) vs. Output Power (W)

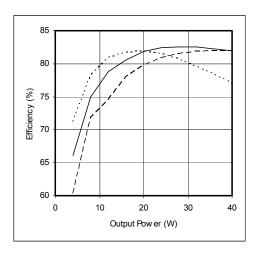


Figure 11 – DVTR2812S
Efficiency (%) vs. Output Power (W)

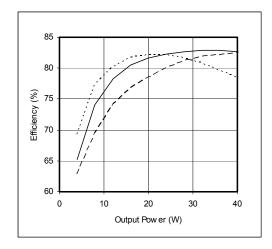
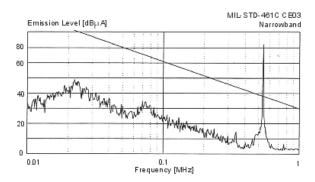


Figure 12 – DVTR2815S
Efficiency (%) vs. Output Power (W)



#### **EMI PERFORMANCE CURVES**

 $(T_{CASE} = 25^{\circ}C, V_{IN} = +28V \pm 5\%, Full Load, Unless Otherwise Specified)$ 



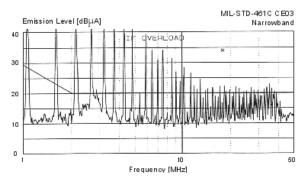
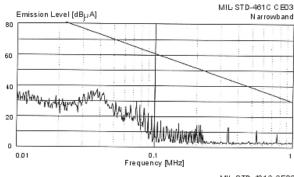


Figure 13 – DVTR2800S without EMI Filter



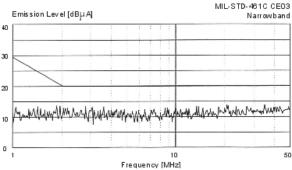
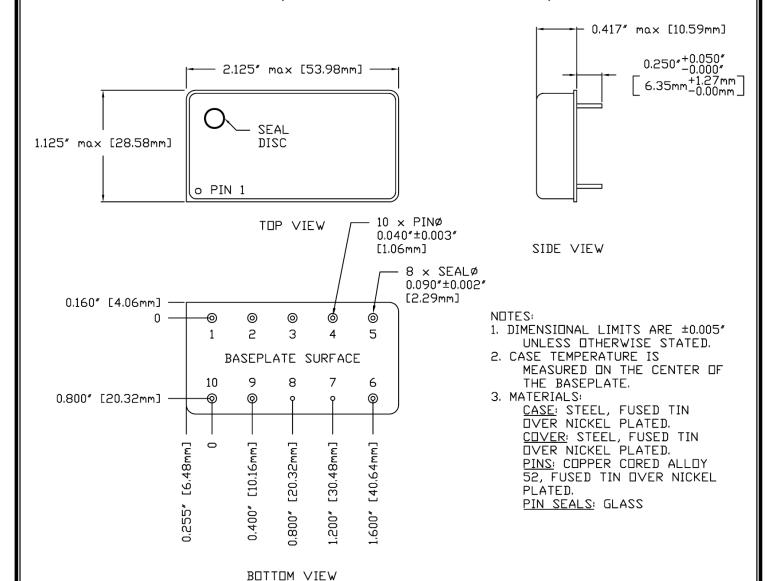


Figure 14 - DVTR2800S with EMI Filter



#### PACKAGE SPECIFICATIONS (NON-FLANGED, SOLDER SEAL)

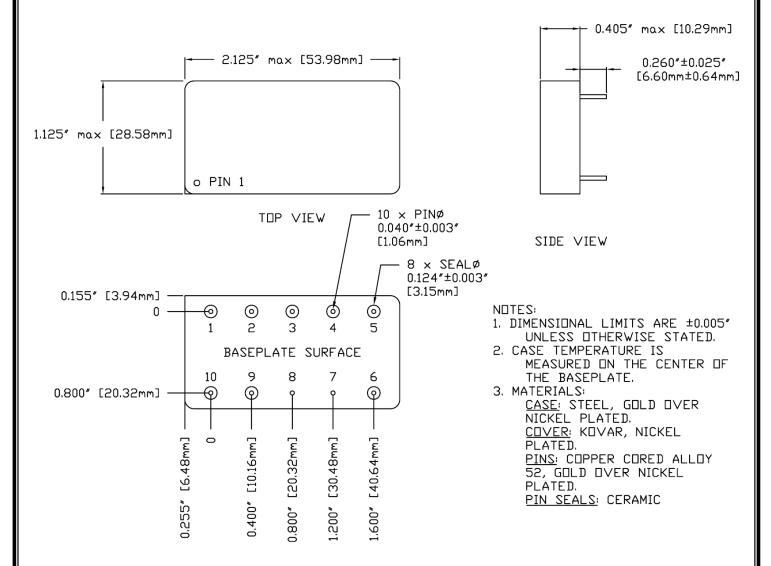


PIN	FUNCTION	PIN	FUNCTION
1	28V IN	6	+S
2	INHIBIT	7	CASE
3	-S	8	CASE
4	OUT COM	9	SYNC
5	+V OUT	10	IN COM

Figure 15 – Non-Flanged, Solder Seal Package and Pinout (Not Used for /HB or Higher Screened Products)



### PACKAGE SPECIFICATIONS (NON-FLANGED, SEAM SEAL)



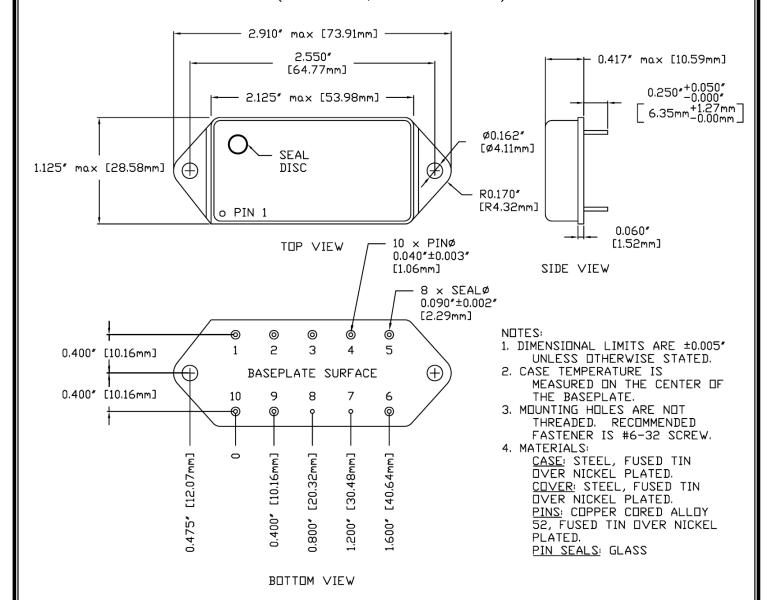
		1 '		W

PIN	FUNCTION	PIN	FUNCTION
1	28V IN	6	+S
2	INHIBIT	7	CASE
3	-S	8	CASE
4	OUT COM	9	SYNC
5	+V OUT	10	IN COM

Figure 16 – Non-Flanged, Seam Seal Package and Pinout



#### PACKAGE SPECIFICATIONS (FLANGED, SOLDER SEAL)

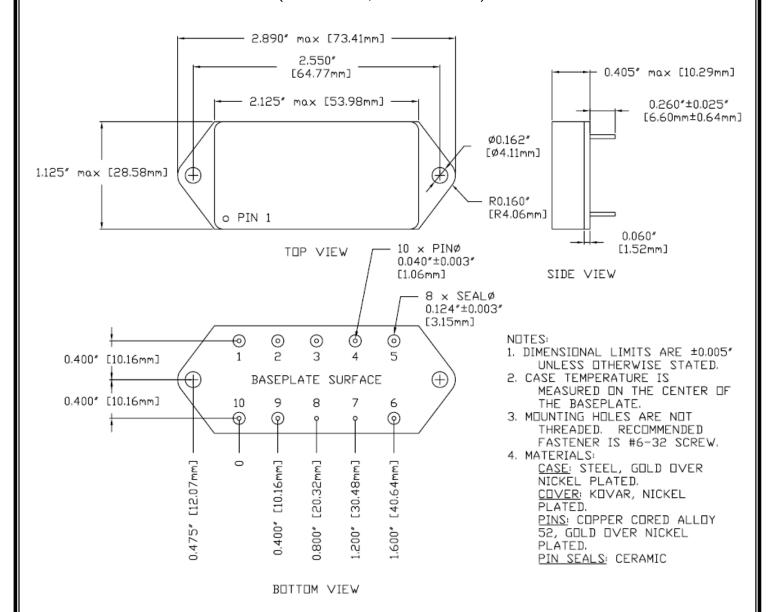


PIN	FUNCTION	PIN	FUNCTION
1	28V IN	6	+S
2	INHIBIT	7	CASE
3	-S	8	CASE
4	OUT COM	9	SYNC
5	+V OUT	10	IN COM

Figure 17 – Flanged, Solder Seal Package and Pinout (Not Used for /HB or Higher Screened Products)



#### PACKAGE SPECIFICATIONS (FLANGED, SEAM SEAL)



PIN	FUNCTION	PIN	FUNCTION
1	28V IN	6	+S
2	INHIBIT	7	CASE
3	-S	8	CASE
4	OUT COM	9	SYNC
5	+V OUT	10	IN COM

Figure 18 – Flanged, Seam Seal Package and Pinout



## PACKAGE PIN DESCRIPTION

Pin	Function	Description		
1	28V IN	Positive Input Voltage Connection		
2	INHIBIT	Logic Low = Disabled Output. Connecting the inhibit pin to input common causes converter shutdown.  Logic High = Enabled Output. Unconnected or open collector TTL.		
3	-S	Return Sense		
4	OUT COM	Output Common Connection		
5	+V OUT	Positive Output Voltage Connection		
6	+S	Positive Sense		
7	CASE	Case Connection		
8	CASE	Case Connection		
9	SYNC	Synchronization Signal		
10	IN COM	Input Common Connection		



ENVIRONMENTAL SCREENING (100% Tested Per MIL-STD-883 as referenced to MIL-PRF-38534)

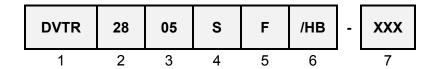
Screening	MIL-STD-883	Standard (No Suffix)	Extended /ES	HB /HB	Class H /H	Class K /K
Non- Destructive Bond Pull	Method 2023	•	•	•	•	•
Internal Visual	Method 2017, 2032 Internal Procedure	•	•	•	•	•
Temperature Cycling	Method 1010, Condition C Method 1010, -55°C to 125°C		•	•	•	•
Constant Acceleration	Method 2001, 3000g, Y1 Direction Method 2001, 500g, Y1 Direction		•	•	•	•
PIND	Method 2020, Condition A <sup>2</sup>					•
Pre Burn-In Electrical	100% at 25°C					•
Burn-In	Method 1015, 320 hours at +125°C Method 1015, 160 hours at +125°C 96 hours at +125°C 24 hours at +125°C	•	•	•	•	•
Final Electrical	MIL-PRF-38534, Group A <sup>1</sup> 100% at 25°C	•	•	•	•	•
Hermeticity	Method 1014, Fine Leak, Condition A Method 1014, Gross Leak, Condition C Dip (1 x 10 <sup>-3</sup> )	•	•	•	•	•
Radiography	Method 2012 <sup>3</sup>					•
External Visual	Method 2009	•	•	•	•	•

Notes:

- 100% R&R testing at  $-55^{\circ}$ C,  $+25^{\circ}$ C, and  $+125^{\circ}$ C with all test data included in product shipment. PIND test Certificate of Compliance included in product shipment. 1.
- 2.
- Radiographic test Certificate of Compliance and film(s) included in product shipment. 3.



#### ORDERING INFORMATION



(1) (2) (3)

Product Series	Nominal Input Voltage		Output Voltage		Number of Outputs	
DVTR	28	28 Volts	2R5 3R3 05 5R2 07 08 12 15 18 28	2.5 Volts 3.3 Volts 5 Volts 5.2 Volts 7 Volts 8 Volts 12 Volts 15 Volts 18 Volts 28 Volts	S	Single

(5) (6) (7)

Packa	ge Option	Screenin	g Code <sup>1,2</sup>	Additional Screening Code
None F	Non-Flanged Flanged	None /ES /HB /H /K	Standard Extended HB Class H Class K	Contact Sales

Notes:

- 1. Contact the VPT Inc. Sales Department for availability of Class H (/H) or Class K (/K) qualified products.
- 2. VPT Inc. reserves the right to ship higher screened or SMD products to meet lower screened orders at our sole discretion unless specifically forbidden by customer contract.

Please contact your sales representative or the VPT Inc. Sales Department for more information concerning additional environmental screening and testing, different input voltage, output voltage, power requirement, source inspection, and/or special element evaluation for space or other higher quality applications.



#### SMD (STANDARD MICROCIRCUIT DRAWING) NUMBERS

Standard Microcircuit Drawing (SMD)	DVTR2800S Series Similar Part Number		
*T.B.D.	DVTR282R5S/H DVTR282R5SF/H		
*T.B.D.	DVTR283R3S/H DVTR283R3SF/H		
*T.B.D.	DVTR2805S/H DVTR2805SF/H		
*T.B.D.	DVTR285R2S/H DVTR285R2SF/H		
*T.B.D.	DVTR2807S/H DVTR2807SF/H		
*T.B.D.	DVTR2808S/H DVTR2808SF/H		
*T.B.D.	DVTR2812S/H DVTR2812SF/H		
*T.B.D.	DVTR2815S/H DVTR2815SF/H		
*T.B.D.	DVTR2818S/H DVTR2818SF/H		
*T.B.D.	DVTR2828S/H DVTR2828SF/H		

Do not use the DVTR2800S Series similar part number for SMD product acquisition. It is listed for reference only. For exact specifications for the SMD product, refer to the SMD drawing. SMD's can be downloaded from the DSCC website at <a href="http://www.dscc.dla.mil/programs/smcr/">http://www.dscc.dla.mil/programs/smcr/</a>. The SMD number listed above is for MIL-PRF-38534 Class H screening, standard gold plated lead finish, and no RHA (Radiation Hardness Assurance) level. Please reference the SMD for other screening levels, lead finishes, and radiation levels. All SMD products are marked with a "Q" on the cover as specified by the QML certification mark requirement of MIL-PRF-38534.



### **CONTACT INFORMATION**

To request a quotation or place orders please contact your sales representative or the VPT Inc. Sales Department at:

**Phone**: (425) 353-3010 **Fax**: (425) 353-4030

**E-mail**: vptsales@vpt-inc.com

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