

Optocoupler with Phototransistor Output

Description

The TCET110./ TCET2100/ TCET4100 consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4-lead up to 16-lead plastic dual inline package.

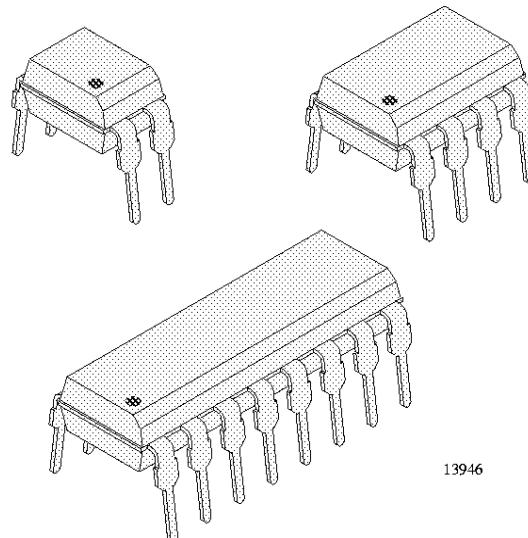
The elements are mounted on one leadframe using a **coplanar technique**, providing a fixed distance between input and output for highest safety requirements.

Applications

Circuits for safe protective separation against electrical shock according to safety class II (reinforced isolation):

- For application class I – IV at mains voltage ≤ 300 V
- For application class I – III at mains voltage ≤ 600 V according to VDE 0884, table 2, suitable for:

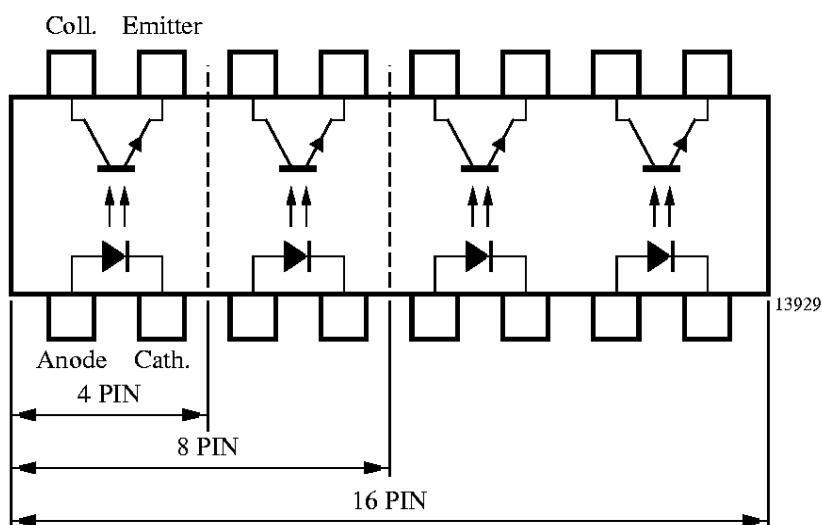
Switch-mode power supplies, computer peripheral interface, microprocessor system interface, line receiver.



These couplers perform safety functions according to the following equipment standards:

- **VDE 0884**
Optocoupler providing protective separation
- **VDE 0805/IEC 950/EN 60950**
Office machines (applied for reinforced isolation for mains voltage ≤ 400 V_{RMS})
- **VDE 0804**
Telecommunication apparatus and data processing
- **VDE 0860/IEC 65**
Safety for mains-operated electronic and related household apparatus

Pin Connection



Features

- Approvals:
 - BSI: BS EN 41003, BS EN 60065 (BS 415)
 - BS EN 60950 (BS 7002)
 - Certificate No. 7081 and 7402
 - FIMKO** (SETI): EN 60950
 - Certificate No. 41400
 - Underwriters Laboratory (UL) 1577
recognized-file No. E-76222
 - VDE** 0884 Certificate number 101348
- VDE 0884 related features:
 - Rated impulse voltage (transient overvoltage)
 $V_{1OTM} = 6 \text{ kV}$ peak
 - Isolation test voltage (partial discharge test voltage)
 $V_{pd} = 1.6 \text{ kV}$
 - Rated isolation voltage (RMS includes DC)
 $V_{1OWM} = 600 \text{ V}_{\text{RMS}}$ (848 V peak)
 - Rated recurring peak voltage (repetitive)
 $V_{1ORM} = 600 \text{ V}_{\text{RMS}}$
 - Creepage current resistance according to
VDE 0303/IEC 112
Comparative Tracking Index: **CTI** ≥ 200
 - Thickness through insulation $\geq 0.75 \text{ mm}$
- General features:
 - CTR offered in 7 groups
 - Isolation materials according to UL94-VO
 - Pollution degree 2 (DIN/VDE 0110 / resp. IEC 664)
 - Climatic classification 55/100/21 (IEC 68 part 1)
 - Special construction:
Therefore extra low coupling capacity of typical
0.2 pF, high **C**ommon **M**ode **R**ejection
 - Low temperature coefficient of CTR
 - G = Leadform 10.16 mm;
provides creepage distance $> 8 \text{ mm}$,
for TCET2100/ TCET4100 optional
suffix letter "G" is not marked on the optocoupler
 - Coupling System U

Order Instruction

Part Numbers	CTR Ranking
TCET1100(G)/ TCET2100/ TCET4100	50 to 600%
TCET1101(G)	40 to 80%
TCET1102(G)	63 to 125%
TCET1103(G)	100 to 200%
TCET1105(G)	50 to 150%
TCET1106(G)	100 to 300%
TCET1107(G)	80 to 160%

Absolute Maximum Ratings

Input (Emitter)

Parameters	Test Conditions	Symbol	Value	Unit
Reverse voltage		V _R	6	V
Forward current		I _F	60	mA
Forward surge current	t _p ≤ 10 μs	I _{FSM}	1.5	A
Power dissipation	T _{amb} ≤ 25°C	P _V	100	mW
Junction temperature		T _j	125	°C

Output (Detector)

Parameters	Test Conditions	Symbol	Value	Unit
Collector emitter voltage		V _{CEO}	70	V
Emitter collector voltage		V _{ECO}	7	V
Collector current		I _C	50	mA
Collector peak current	t _p /T = 0.5, t _p ≤ 10 ms	I _{CM}	100	mA
Power dissipation	T _{amb} ≤ 25°C	P _v	150	mW
Junction temperature		T _j	125	°C

Coupler

Parameters	Test Conditions	Symbol	Value	Unit
Isolation test voltage (RMS)	t = 1 min	V _{IO}	3.75	kV
Total power dissipation	T _{amb} ≤ 25°C	P _{tot}	250	mW
Operating ambient temperature range		T _{amb}	-40 to +100	°C
Storage temperature range		T _{stg}	-55 to +125	°C
Soldering temperature	2 mm from case t ≤ 10 s	T _{sd}	260	°C

Maximum Safety Ratings¹⁾ (according to VDE 0884)

Input (Emitter)

Parameters	Test Conditions	Symbol	Value	Unit
Forward current		I _{si}	130	mA

Output (Detector)

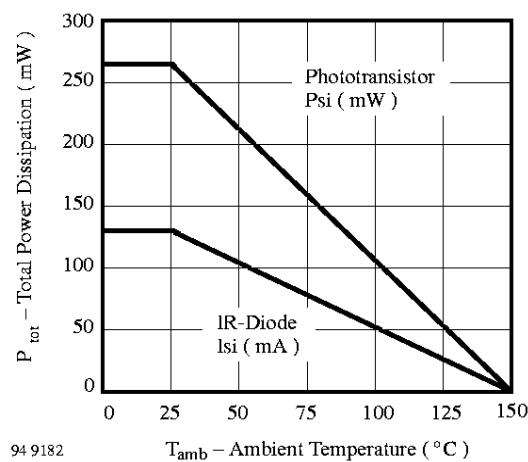
Parameters	Test Conditions	Symbol	Value	Unit
Power dissipation	T _{amb} ≤ 25°C	P _{si}	265	mW

Coupler

Parameters	Test Conditions	Symbol	Value	Unit
Rated impulse voltage		V _{1OTM}	6	kV
Safety temperature		T _{si}	150	°C

- 1) This device is used for protective separation against electrical shock only within the maximum safety ratings.
This must be ensured by using protective circuits in the applications.

Derating Diagram



Electrical Characteristics $T_{amb} = 25^\circ C$

Input (Emitter)

Parameters	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Forward voltage	$I_F = 50 \text{ mA}$	V_F		1.25	1.6	V
Junction capacitance	$V_R = 0, f = 1 \text{ MHz}$	C_J		50		pF

Output (Detector)

Parameters	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Collector emitter voltage	$I_C = 1 \text{ mA}$	V_{CE0}	70			V
Emitter collector voltage	$I_E = 100 \mu\text{A}$	V_{EC0}	7			V
Collector emitter cut off current	$V_{CE} = 20 \text{ V}, I_f = 0, E = 0$	I_{CE0}		10	100	nA

Coupler

Parameters	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
AC isolation test voltage (RMS)	$f = 50 \text{ Hz}, t = 1 \text{ s}$	$V_{IO}^2)$	3.75			V
Collector/emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 1 \text{ mA}$	V_{CESat}			0.3	V
Cut off frequency	$V_{CE} = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 100 \Omega$	f_c		110		kHz
Coupling capacitance	$f = 1 \text{ MHz}$	C_k		0.3		pF

2) Related to standard climate 23/50 DIN 50014

Current Transfer Ratio (CTR)

Parameters	Test Conditions	Type	Symbol	Min.	Typ.	Max.
I_C/I_F	$V_{CE} = 10 \text{ V}, I_F = 10 \text{ mA}$	TCET1101	CTR	0.40		0.8
I_C/I_F	$V_{CE} = 10 \text{ V}, I_F = 10 \text{ mA}$	TCET1102	CTR	0.63		1.25
I_C/I_F	$V_{CE} = 5 \text{ V}, I_F = 5 \text{ mA}$	TCET1100/TCET2100/TCET4100	CTR	0.50		6.0
I_C/I_F	$V_{CE} = 5 \text{ V}, I_F = 5 \text{ mA}$	TCET1103	CTR	1.0		2.0
I_C/I_F	$V_{CE} = 5 \text{ V}, I_F = 5 \text{ mA}$	TCET1105	CTR	0.5		1.5
I_C/I_F	$V_{CE} = 5 \text{ V}, I_F = 5 \text{ mA}$	TCET1106	CTR	1.0		3.0
I_C/I_F	$V_{CE} = 5 \text{ V}, I_F = 5 \text{ mA}$	TCET1107	CTR	0.8		1.6

Switching Characteristics (Typical Values)

$V_S = 5 \text{ V}$

Type	$R_L = 100 \Omega$ (see figure 1)						$R_L = 1 \text{ k}\Omega$ (see figure 2)			
	$t_d[\mu\text{s}]$	$t_f[\mu\text{s}]$	$t_{on}[\mu\text{s}]$	$t_s[\mu\text{s}]$	$t_f[\mu\text{s}]$	$t_{off}[\mu\text{s}]$	$I_C[\text{mA}]$	$t_{on}[\mu\text{s}]$	$t_{off}[\mu\text{s}]$	$I_F[\text{mA}]$
TCET110./TCET2100/ TCET4100	3.0	3.0	6.0	0.3	4.7	5.0	2.0	9.0	18.0	10.0

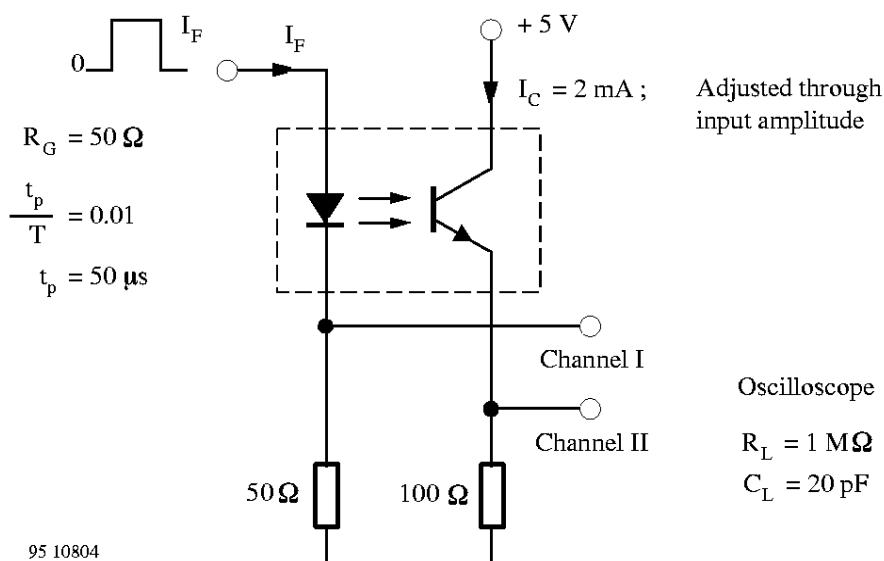


Figure 1. Test circuit

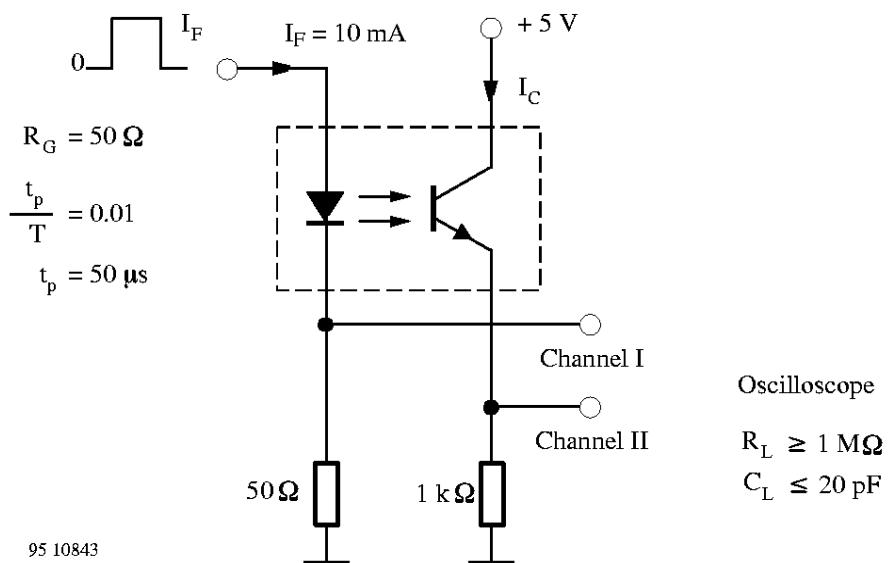


Figure 2. Test circuit, saturated operation

Insulation Rated Parameters (according to VDE 0884)

Parameters		Test Conditions	Symbol	Min.	Typ.	Max.	Unit	
Partial discharge test voltage	Routine test	100%, $t_{test} = 1$ s	V_{pd}	1.6			kV	
	Lot test (sample test)	$t_{Tr} = 60$ s, $t_{test} = 10$ s (see figure 3)	V_{IOTM}	6			kV	
			V_{pd}	1.3			kV	
Isolation resistance		$V_{IO} = 500$ V	R_{IO}	10^{12}			Ω	
		$V_{IO} = 500$ V, $T_{amb} = 100^\circ\text{C}$	R_{IO}	10^{11}			Ω	
		$V_{IO} = 500$ V, $T_{amb} = 150^\circ\text{C}$ (construction test only)	R_{IO}	10^9			Ω	

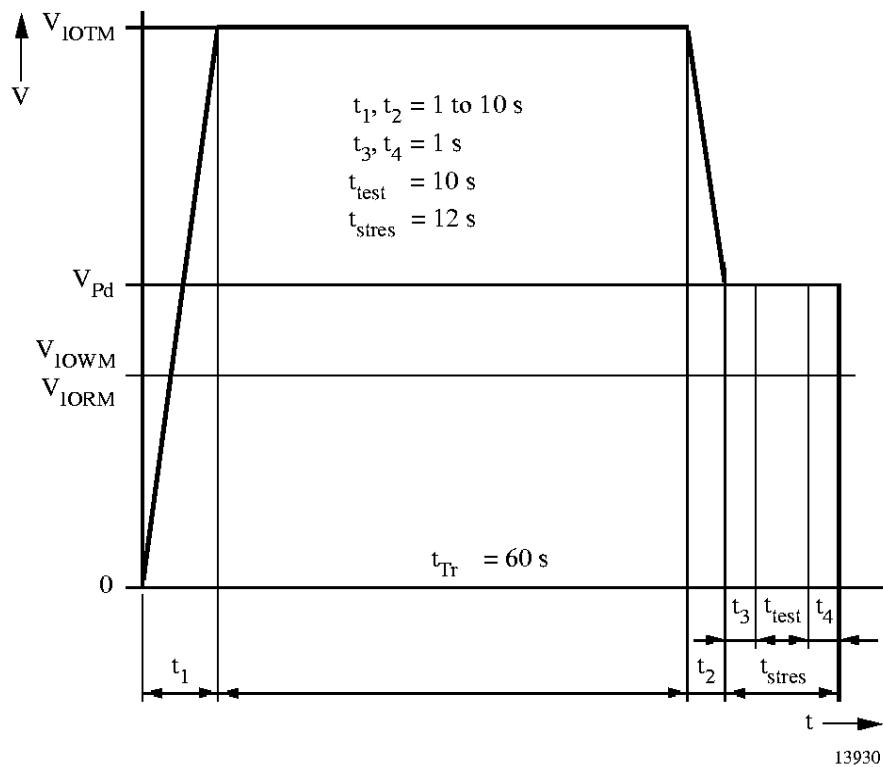


Figure 3. Test pulse diagram for sample test according to DIN VDE 0884

TCET110.(G) up to TCET4100

TEMIC
Semiconductors

Typical Characteristics ($T_{amb} = 25^{\circ}\text{C}$, unless otherwise specified)

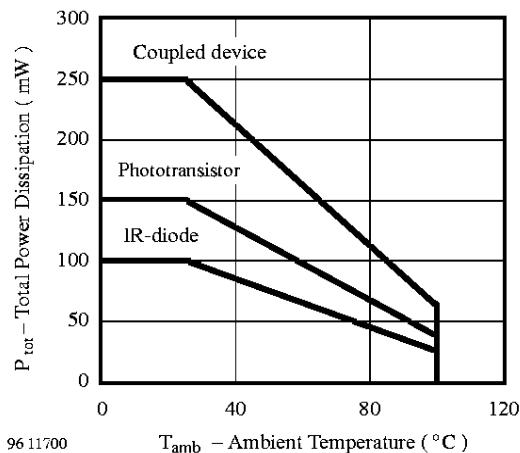


Figure 4. Total Power Dissipation vs. Ambient Temperature

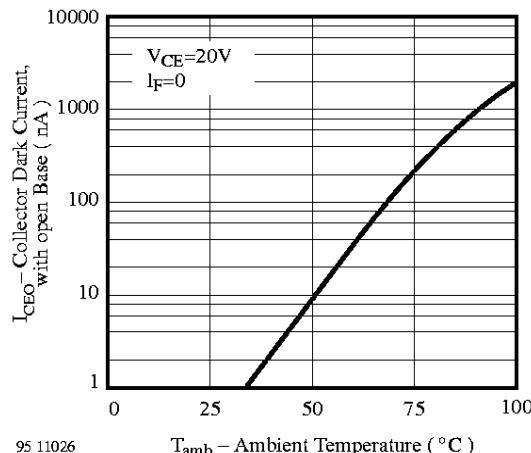


Figure 7. Collector Dark Current vs. Ambient Temperature

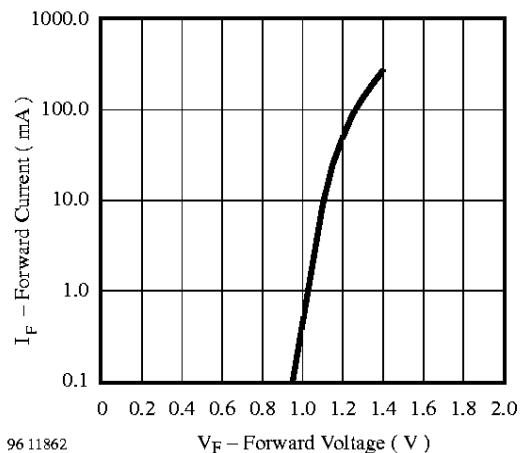


Figure 5. Forward Current vs. Forward Voltage

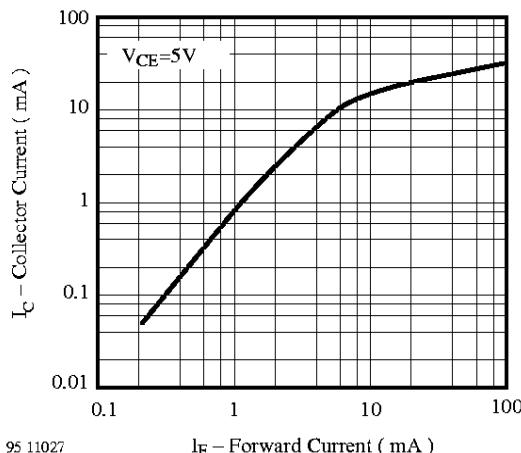


Figure 8. Collector Current vs. Forward Current

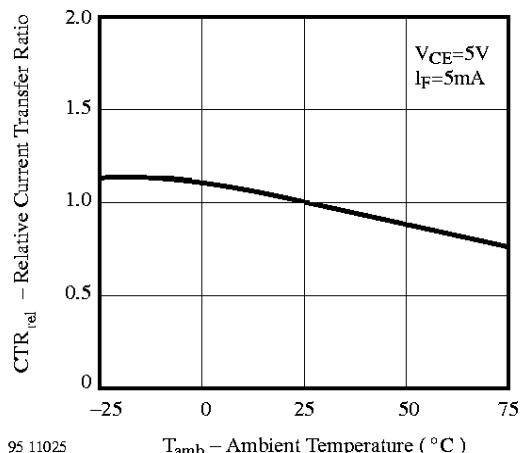


Figure 6. Rel. Current Transfer Ratio vs. Ambient Temperature

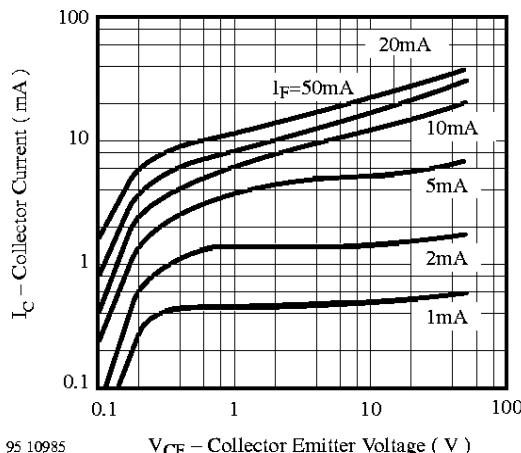


Figure 9. Collector Current vs. Collector Emitter Voltage

Typical Characteristics ($T_{amb} = 25^\circ C$, unless otherwise specified)

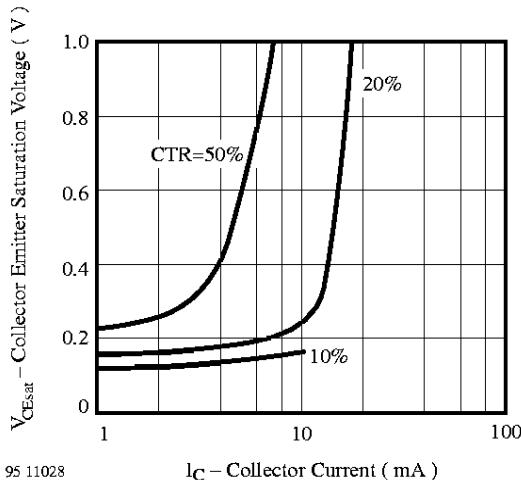


Figure 10. Collector Emitter Sat. Voltage vs. Collector Current

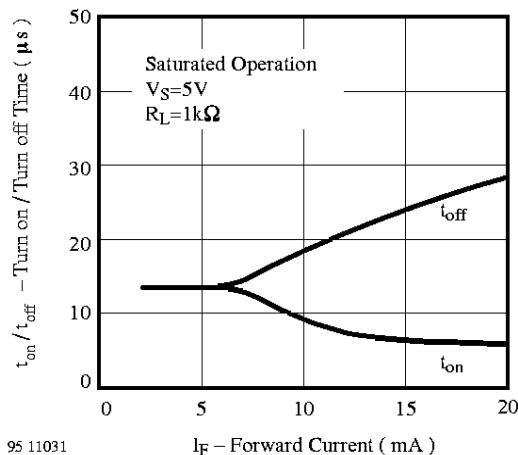


Figure 12. Turn on / off Time vs. Forward Current

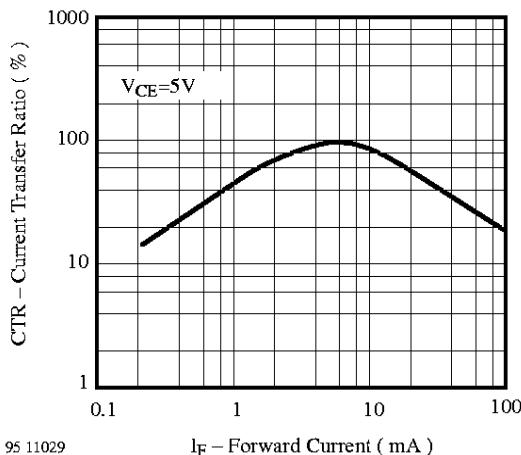


Figure 11. Current Transfer Ratio vs. Forward Current

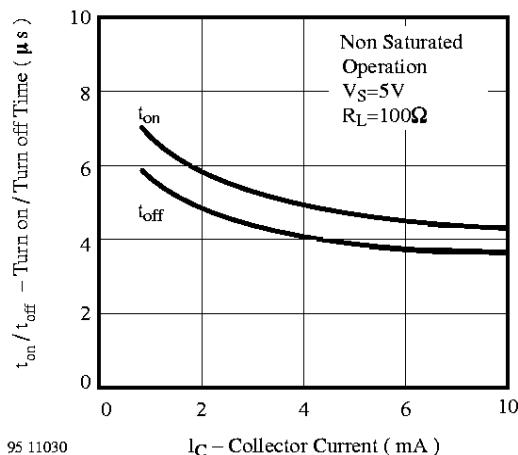
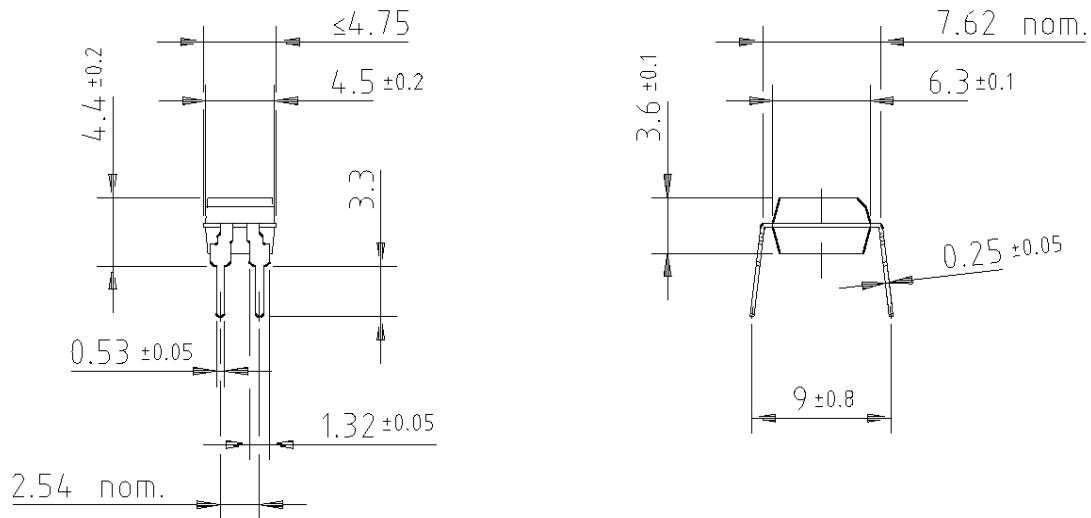


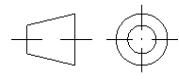
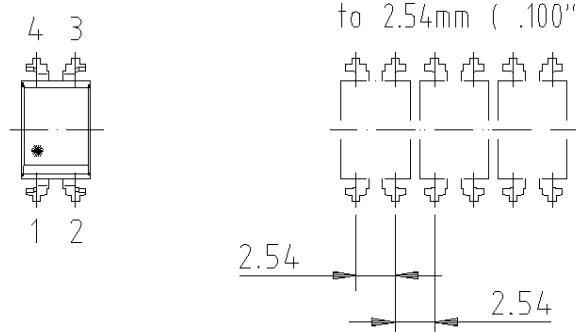
Figure 13. Turn on / off Time vs. Collector Current

Dimensions of TCET110. in mm



E.g.:

special Features: endstackable
to 2.54mm (.100") spacing



technical drawings
according to DIN
specifications

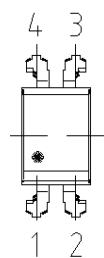
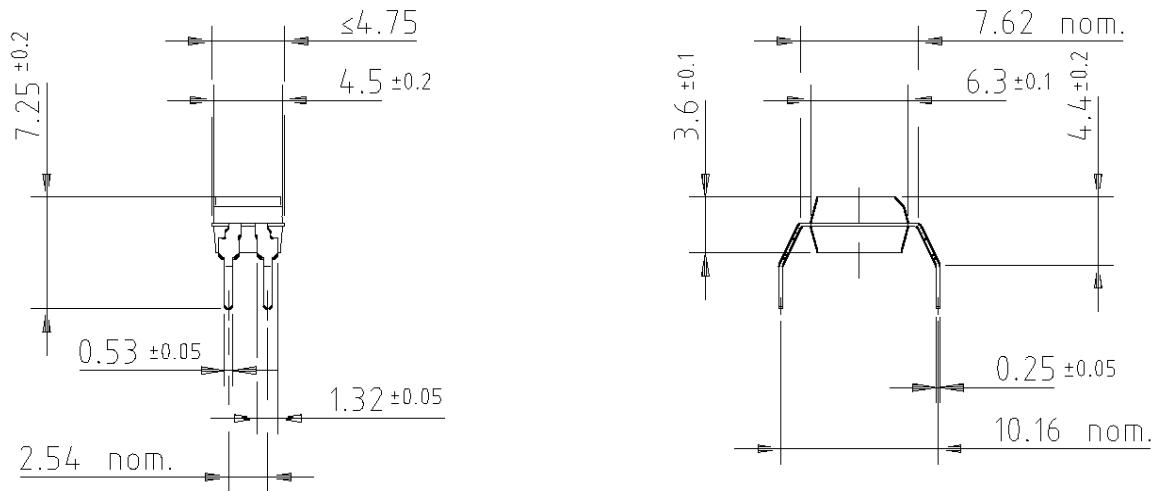
14789

weight: 0.25 g
creeping distance: ≥ 6 mm
air path: ≥ 6 mm

after mounting on PC board

Dimensions of TCET110.G in mm

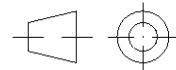
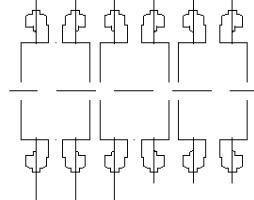
Leadform 10.16. mm (G-type)



2.54 2.54

E.g.:

special Features: endstackable
to 2.54mm (.100") spacing



technical drawings
according to DIN
specifications

weight : ca 0.25g

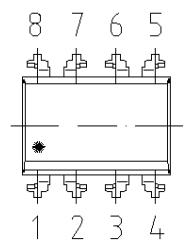
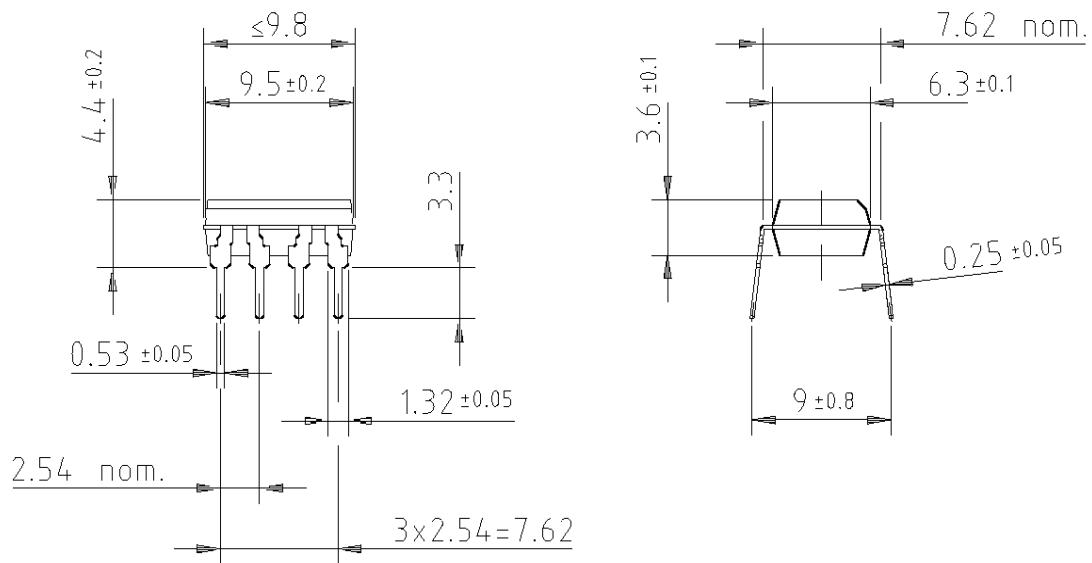
creeping distance : ≥ 8mm

air path : ≥ 8mm

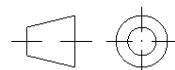
after mounting on PC board

14792

Dimensions of TCET2100 in mm



14784

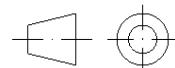
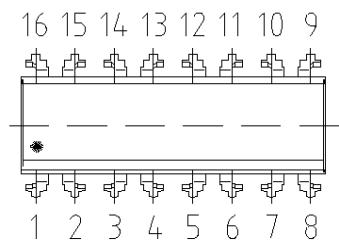
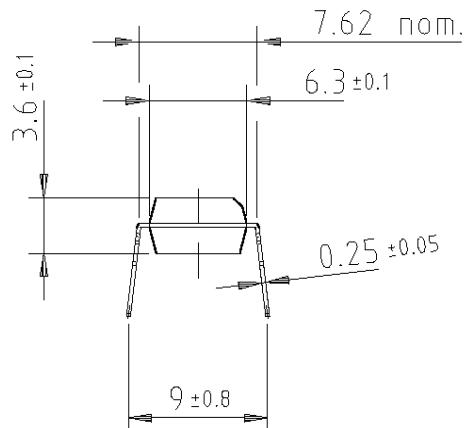
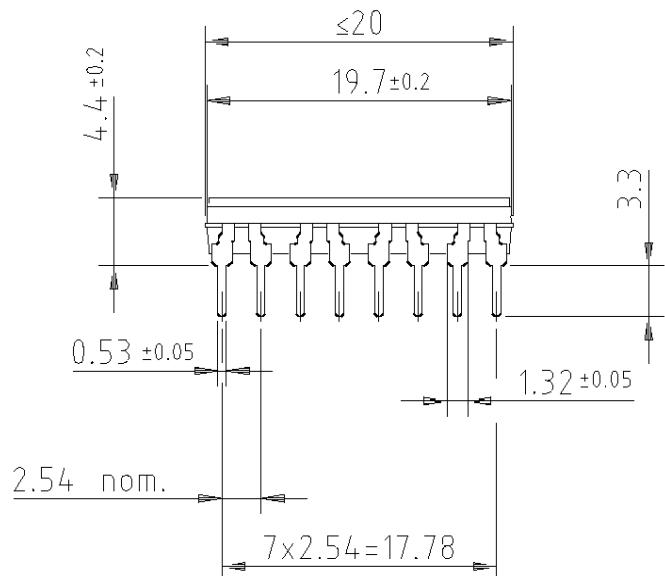


technical drawings
according to DIN
specifications

weight: 0.5 g
creeping distance: ≥ 6 mm
air path: ≥ 6 mm

after mounting on PC board

Dimensions of TCET4100 in mm



technical drawings
according to DIN
specifications

14783

weight: 1.0 g
creeping distance: ≥ 6 mm
air path: ≥ 6 mm

after mounting on PC board

Ozone Depleting Substances Policy Statement

It is the policy of **TEMIC TELEFUNKEN microelectronic GmbH** to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

TEMIC TELEFUNKEN microelectronic GmbH semiconductor division has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

TEMIC can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use TEMIC products for any unintended or unauthorized application, the buyer shall indemnify TEMIC against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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