

**Vishay Semiconductors** 

### 1 Form A Solid State Relay

#### Features

- Current Limit Protection
- Isolation Test Voltage 5300 V<sub>RMS</sub>
- Typical R<sub>ON</sub> 28 Ω
- Load Voltage 350 V
- Load Current 120 mA
- High Surge Capability
- Clean Bounce Free Switching
- Low Power Consumption
- High Reliability Monolithic Receptor
- SMD lead available on tape and reel
- Equivalent to CP Clare LCA110
- Lead-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

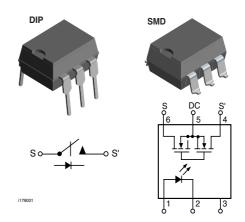
#### **Agency Approvals**

- UL1577, File No. E52744 System Code H or J, Double Protection
- CSA Certification 093751
- BSI/BABT Cert. No. 7980
- FIMKO Approval

#### Applications

- General Telecom Switching
- On/off Hook Control
- Ring Delay
- Dial Pulse
- Ground Start
- Ground Fault Protection
- Instrumentation
- Industrial Controls

See "Solid State Relays" (Application Note 56)



#### Description

The LH1546 is robust, ideal for telecom and ground fault applications. It is a SPST normally open switch (1 Form A) that replaces electromechanical relays in many applications. It is constructed using a GaAIAs LED for actuation control and an integrated monolithic die for the switch output. The die, fabricated in a highvoltage dielectrically isolated technology, is comprised of a photodiode array, switch control circuitry and MOSFET switches. In addition, it employs current-limiting circuitry which meets FCC 68.302 and other regulatory voltage surge requirements when overvoltage protection is provided.

#### **Customer Specific Requirement**

- Order information has been changed to identify that the product is lead free.

#### **Order Information**

Part	Remarks
LH1546AAB-E3	Tubes, SMD-6
LH1546AABTR-E3	Tape and Reel, SMD-6
LH1546AT-E3	Tubes, DIP-6



#### **Vishay Semiconductors**

Absolute Maximum Ratings,  $T_{amb} = 25$  °C Stresses in excess of the absolute Maximum Ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute Maximum Ratings for extended periods of time can adversely affect reliability.

#### SSR

Parameter	Test condition	Symbol	Value	Unit
SSR output power dissipation (continuous)		P <sub>diss</sub>	550	mW
LED continuous forward current		١ <sub>F</sub>	50	mA
LED reverse voltage	I <sub>R</sub> ≤ 10 μA	V <sub>R</sub>	8.0	V
DC or peak AC load voltage	$I_L \le 50 \ \mu A$	VL	350	V
Continuous DC load current at 25 °C, Bidirectional			120	mA
Continuous DC load current at 25 °C, Unidirectional			200	mA
Ambient temperature range		T <sub>amb</sub>	- 40 to + 85	۵°C
Storage temperature range		T <sub>stg</sub>	- 40 to + 150	٥C
Soldering temperature	t = 10 s max	T <sub>sld</sub>	260	۵°C
Isolation test voltage	for 1.0 s	V <sub>IO</sub>	5300	V <sub>RMS</sub>
Isolation resistance	$V_{IO}$ = 500 V, $T_{amb}$ = 25 °C	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω
	$V_{IO}$ = 500 V, $T_{amb}$ = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω

### Electrical Characteristics, T<sub>amb</sub> = 25 °C

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

#### Input

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
LED forward current, switch turn-on	I <sub>L</sub> = 100 mA, t = 10 ms	I <sub>Fon</sub>		1.1	2.0	mA
LED forward current, switch turn-off	V <sub>L</sub> = ± 350 V	I <sub>Foff</sub>	0.2	1.0		mA
LED forward voltage	I <sub>F</sub> = 10 mA	V <sub>F</sub>	1.15	1.26	1.45	V

#### Output

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
ON-resistance, ac/dc: Pin 4 (±) to 6 (±)	$I_{\rm F} = 5.0 \text{ mA}, I_{\rm L} = 50 \text{ mA}$	R <sub>ON</sub>		28	35	Ω
ON-resistance, dc: Pin 4, 6 (+) to 5 (-)	$I_{\rm F} = 5.0 \text{ mA}, I_{\rm L} = 50 \text{ mA}$	R <sub>ON</sub>		7.0	10.0	Ω
OFF-resistance	$I_{F} = 0 \text{ mA}, V_{L} = \pm 100 \text{ V}$	R <sub>OFF</sub>	0.5	300		GΩ
Current limit ac/dc	$I_F = 5.0 \text{ mA}, t = 5.0 \text{ ms},$ $V_L = 6.0 \text{ V}$	I <sub>LMT</sub>	170	210	250	mA
Off-state leakage current	$I_{F} = 0 \text{ mA}, V_{L} = \pm 100 \text{ V}$	Ι <sub>Ο</sub>		0.35	200	nA
	$I_{F} = 0 \text{ mA}, V_{L} = \pm 350 \text{ V}$	Ι <sub>Ο</sub>		0.096	1.0	nA
Output capacitance Pin 4 to 6	I <sub>F</sub> = 0 mA, V <sub>L</sub> = 1.0 V	C <sub>O</sub>		18		pF
	I <sub>F</sub> = 0 mA, V <sub>L</sub> = 50 V	C <sub>O</sub>		6.7		pF
Switch offset	I <sub>F</sub> = 5.0 mA	V <sub>OS</sub>		0.3		μA



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#### Transfer

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Capacitance (input-output)	V <sub>ISO</sub> = 1.0 V	CIO		0.67		pF
Turn-on time	I <sub>F</sub> = 5.0 mA, I <sub>L</sub> = 50 mA	t <sub>on</sub>		1.14	3.0	ms
Turn-off time	I <sub>F</sub> = 5.0 mA, I <sub>L</sub> = 50 mA	t <sub>off</sub>		0.71	3.0	ms

### Typical Characteristics (Tamb = 25 °C unless otherwise specified)

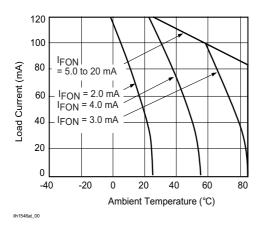


Figure 1. Recommended Operating Conditions

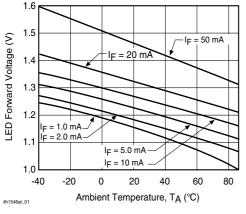


Figure 2. LED Voltage vs. Temperature

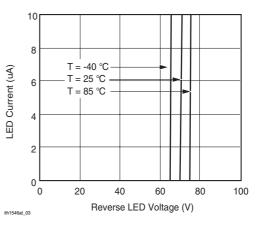


Figure 3. LED Reverse Current vs. LED Reverse Voltage

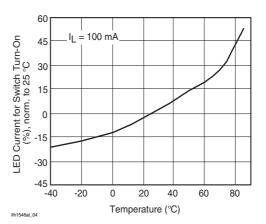
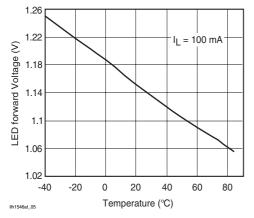
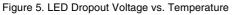


Figure 4. LED Current for Switch Turn-on vs. Temperature

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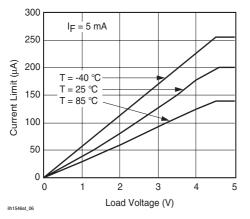


Figure 6. Load Current vs. Load Voltage

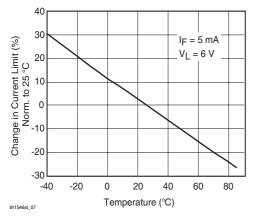


Figure 7. Current Limit vs. Temperature

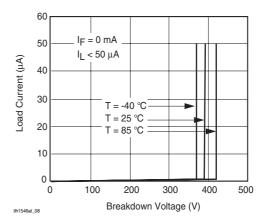
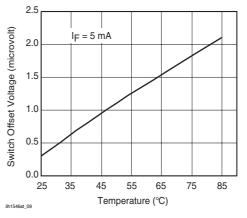
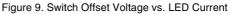


Figure 8. Switch Breakdown Voltage vs. Load Current





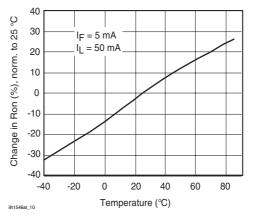


Figure 10. ON-Resistance vs. Temperature



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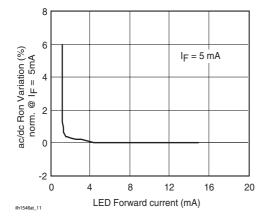


Figure 11. Variation in ON-Resistance vs. LED Current

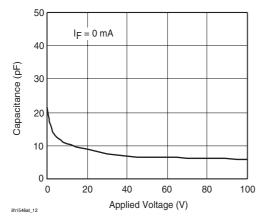


Figure 12. Switch Capacitance vs. Applied Voltage

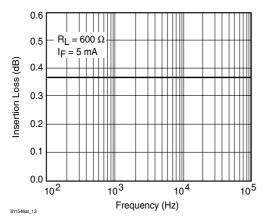


Figure 13. Insertion Loss vs. Frequency

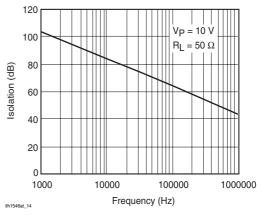
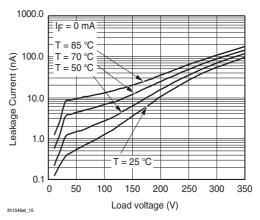
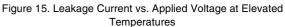
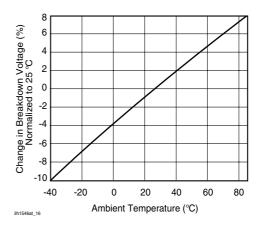


Figure 14. Output Isolation









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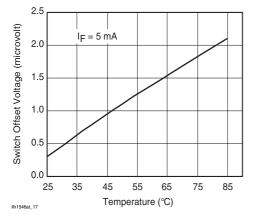


Figure 17. Switch Offset Voltage vs. Temperature

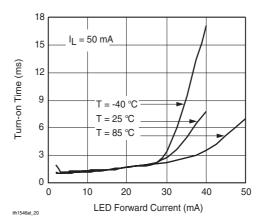


Figure 20. Turn-on Time vs. LED Current

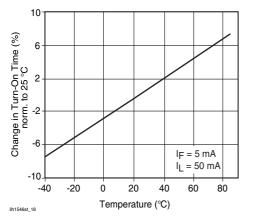


Figure 18. Turn-on Time vs. Temperature

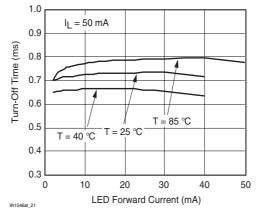


Figure 21. Turn-off Time vs. LED Current

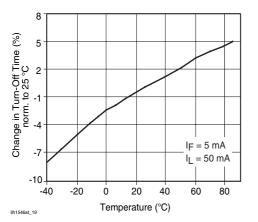
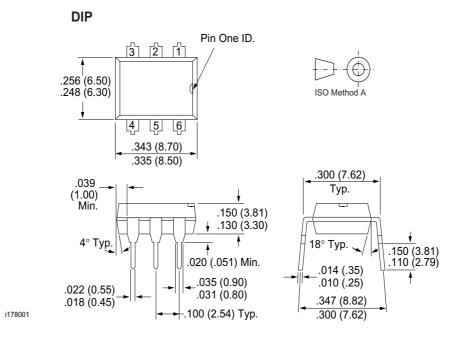


Figure 19. Turn-off Time vs. Temperature

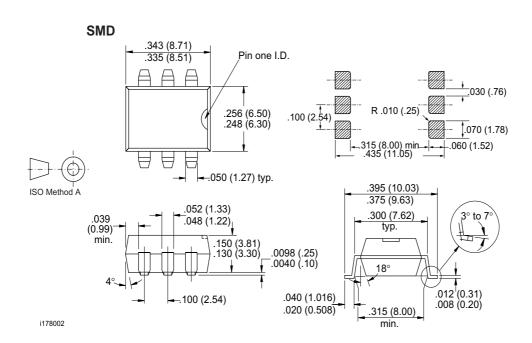


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#### Package Dimensions in Inches (mm)



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#### **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operatingsystems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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