EB71F51 Series

- Oven Controlled Crystal Oscillators (OCXO)
- HCMOS Output
- +5.0V Supply Voltage
- AT-Cut Crystal Used
- External Voltage Control Function
- 14 pin DIP Metal Package





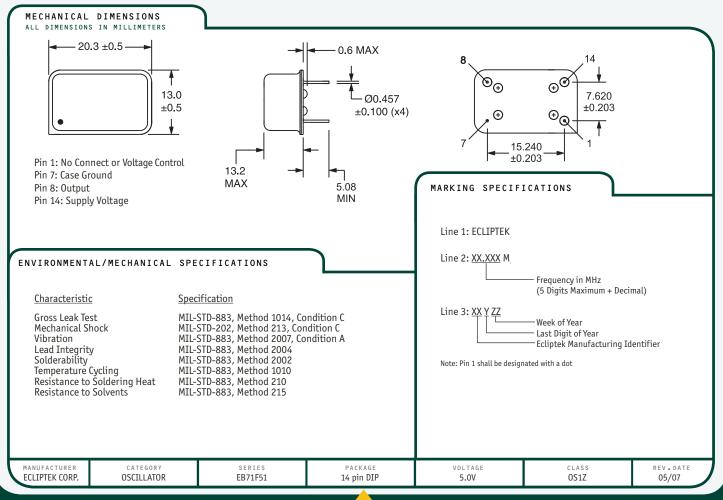
ELECTRICAL SPECIFICATIONS

	Frequency Range 10.000MHz, 12.288MHz, 12.800MHz, 16.000MHz, 1						
Operating Tem	perature Range (OTR)			0°C to 50°C, (0°C to 50°C, 0°C to 70°C, or -20°C to 70°C		
Storage Temp	perature Range			-55°C to 125	-55°C to 125°C		
Supply Voltage	• \/			5.0V _{DC} ±5%	5.0V _{DC} ±5%		
	lerance / Stability						
vs. Initial Tole	erance	at Nominal V_{DD} and V_{C} , at 25°C		±1.0ppm or ±5	±1.0ppm or ±500ppb Maximum		
vs. Temperatu	ure Stability	at Nominal V_{DD} and V_{DD}	ominal V_{DD} and V_{C} ± 100 ppb, ± 280 ppb, or ± 500 pp		ppb Maximum		
vs. Vdd		$V_{DD} \pm 5\%$		±50ppb Maxi	±50ppb Maximum		
vs. Load		Vload ±5%		±50ppb Maxi	±50ppb Maximum		
vs. Aging (1 [Day)	after 72 Hours of Ope	eration	±30ppb Maxi	±30ppb Maximum		
vs. Aging (1 Year)		after 72 Hours of Operation		±500ppb Max	±500ppb Maximum		
vs. Aging (10	Years)	after 72 Hours of Ope	eration	±3.0ppm Max	±3.0ppm Maximum		
Crystal Cut				AT-Cut	AT-Cut		
Warm Up Time		to ±500ppb of Final Frequency at 1 Hour at 25°C		5°C 3 Minutes Ma	3 Minutes Maximum		
Power Consumption		at Steady State, at 25°C		1.6 Watts Ma	1.6 Watts Maximum		
		During Warm Up, at 2	25°C	2.5 Watts Ma	ximum		
Output Voltage Logic High (V _{OH})		$I_{OH} = -8mA$		V _{DD} -0.5V _{DC} Mir	V _{DD} -0.5V _{DC} Minimum		
Output Voltage Logic Low (V _{oL})		$I_{0L} = +8mA$		0.5V _{DC} Maxim	0.5V _{DC} Maximum		
Rise Time / Fall Time		Measured at 20% to 80% of Waveform		6nSec Maxim	6nSec Maximum		
Duty Cycle		Measured at 50% of Waveform		50 ±5(%)	50 ±5(%)		
Load Drive Capability				15pF Maximu	15pF Maximum		
Frequency Deviation		Referenced to F_0 at $V_C = 2.5V_{DC}$; $V_{DD} = 5.0V_{DC}$ over OTR		r OTR ±5ppm Minin	±5ppm Minimum		
Control Volta	ige Range			$0.0V_{DC}$ to V_{DD}	0.0V _{DC} to V _{DD}		
Control Volta	ige (V _c)			2.5V _{DC} ±2.5V _C	2.5V _{DC} ±2.5V _{DC}		
Transfer Function				Positive Trans	Positive Transfer Characteristic		
Linearity				±10% Maxim	±10% Maximum		
Input Impedance				10k0hms Typ	10k0hms Typical		
Typical Phase Noise (at 12.800MHz)		at 10Hz Offset		-95dBc/Hz	-95dBc/Hz		
		at 100Hz Offset		-120dBc/Hz	-120dBc/Hz		
		at 1kHz Offset		-135dBc/Hz	-135dBc/Hz		
		at 10kHz Offset		-140dBc/Hz	-140dBc/Hz		
MANUFACTURER ECLIPTEK CORP.	CATEGORY OSCILLATOR	SERIES EB71F51	PACKAGE 14 pin DIP	VOLTAGE 5.0V	CLASS OS1Z	REV = DATE 05/07	

PART NUMBERING GUIDE

EB71F51 C 10 B V 2 - 20.000M - CL125 **AVAILABLE OPTIONS** INITIAL TOLERANCE Blank=None $C=\pm 1.0$ ppm CLXXX=Custom Lead Length D= ±500ppb G=Full Size Gull Wing FREQUENCY STABILITY **FREQUENCY** 2 Digit Code Per Table 1 **DUTY CYCLE OPERATING TEMPERATURE RANGE (OTR)** 2=50% ±5% 1 Letter Code Per Table 1 **VOLTAGE CONTROL OPTION** N=None (No Connect on Pin 1)

	TABLE 1: PART NUMBERING CODES										
Operating Temperature Range		Frequency Stability X Denotes Availability									
ature			±100ppb	±200ppb	±280ppb	±500ppb					
mper		Code	10	20	28	50					
g Tel	0°C to +50°C	А	Х	Х	Х	Х					
ratir	0°C to +70°C	В		Х	Х	Х					
Ope	-20°C to +70°C	С			Х	Х					



V=Voltage Control on Pin 1