



INNOVATIVE SENSOR TECHNOLOGY



## TSic™ 506F

**Rapid Response, Low-Cost Temperature Sensor IC with Digital Output**

### *Data Sheet*

#### **TSic™ 506F Features**

- Low cost, precision temperature sensor
- Single-wire 11-bit digital serial signal output
- Communication range > 10 meters
- Resolution: 0.1°C
- Accuracy:  $\pm 0.1^\circ\text{C}$  over span of  $40^\circ\text{C}$
- Focussed measurement range:  $-10$  to  $+60^\circ\text{C}$
- Signal read-out every 0.1s (other rates available on request)
- Supply Voltage 3.0V to 5.5V
- Package: 8-pin SOIC, 3-pin e-line, Chip on Flex
- Low quiescent current to minimize self-heating and power consumption (45 $\mu\text{A}$  typ.)
- System-on-chip based on advanced mixed-signal CMOS technology integrating precision temperature sensing bandgap reference with proportional-to-absolute-temperature (PTAT) output, digital signal processor (DSP) core, and electrically erasable memory (EEPROM)

#### **TSic™ Family**

The TSic™ temperature sensor IC family are fully tested and calibrated sensors with absolute measurement accuracy on delivery – no further calibration needed. The TSic™ combines outstanding accuracy with long term stability, yet it is very simple to use.

The TSic™ series is specifically designed for high performance, cost-effective solutions for sensing temperature in building automation, automotive, industrial, office automation, white goods and low-power/mobile applications.

TSic™ employs a high precision bandgap reference with PTAT output; a low-power, precision ADC; and an on-chip DSP core with EEPROM to precisely calibrate the output

temperature signal. The TSic™ series includes ICs with two linear analog signal output options, such as standard 0~1Vout (Supply voltage (V+) = 3.0V to 5.5V) or ratiometric (10~90% of supply voltage); or the digital serial output signal for interfacing with microcontrollers.

#### **Benefits**

- **Several accuracy classes available with 100% upward compatibility**
- **No calibration by customer needed; absolute calibration specified**
- **Simple to integrate, reducing cost and time for application-development**
- **Fast data measurement – optimal for temperature control**
- **Packages for standard SMD, THT or application specific assembly**
- **Miniaturized solutions with Bare-Chip (COB, COF, CSP\*) or e-line packages – very fast response time for COF**
- **Very low power consumption – ideal for mobile and standard applications**
- **Field reconfiguration/recalibration option available (high volume customers only)**
- **Outstanding long term stability**

\* COB: Chip-On-Board; COF: Chip-On-Flex; CSP: Chip Scale Packaging

#### **Application Support**

For TSic™ evaluation ZMD provides a special Evaluation Tool. (Ordering Code: TSic Lab Kit )

Further application support is available through the hotline: email: [tsic@zmd.de](mailto:tsic@zmd.de)

Phone: +49 351 8822-916



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### Absolute Maximum Ratings

PARAMETER	MIN	MAX	UNITS
Supply Voltage (V+)	-0.3	6.0	V
Voltages at analog I/O – Pins (V <sub>INA</sub> , V <sub>OUTA</sub> )	-0.3	V <sub>DDA</sub> +0.3	V
Storage Temperature Range (T <sub>stor</sub> )	-50	150	°C

### Operating Conditions

PARAMETER	MIN	TYP	MAX	UNITS
Supply <sup>1</sup> Voltage to Gnd (V+)	3.0	5.0	5.5	V
Supply Current (I <sub>V+</sub> ) <sup>2</sup> @ V+ = 3.3V, RT	30	45	80	µA
Ambient Temperature <sup>3</sup> Range (T <sub>amb</sub> )	-10		60	°C
External Capacitance between V+ and Gnd <sup>4</sup> (C <sub>V+</sub> )	80	100	470	nF
Output Load Capacitance (C <sub>L</sub> )			15	nF
Output Load Resistance between signal and Gnd (or V+)	1			MΩ

<sup>1</sup> With supply voltage 2.7V – 3.0V accuracy reduced.

<sup>2</sup> Without load

<sup>3</sup> Output signal is limited to this ambient temperature ±3°C (with regard to calibration, offset and gain)

<sup>4</sup> Recommended as close to TSic V+ and Gnd-Pins as possible

### Temperature Accuracy<sup>5</sup>

PARAMETER	MIN	TYP	MAX	UNITS
<i>Focussed Range Device for -10° to 60°C</i>				
T1 +5°C to +45°C	-0.1	±0.05	0.1	°C
T2 -5°C to +5°C	-0.1	+0.1	0.2	°C
T3 +45°C to +55°C	-0.1	+0.1	0.2	°C

<sup>5</sup> Accuracy = specification plus quantization error of 1 bit (0.034°C). This device gets calibrated at 5V. For applications where best accuracy at 3V is requested: ask for a customer specific 3V calibrated device. Accuracy for supply voltage within V+ = 4.5V to 5.5V, 2σ value.

Other TSic products with customer specific calibration available on request: i.e. with special calibration where the 40°C span (bandgap) with the high precision temperature range of ±0.1 °C is shifted to another (lower or higher) temperature range.  
Temperature range limits T1, T2, T3: ±0.1°C;  
Measurement range limits -10°C to +60°C: ±3°C



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### Output Examples for TSic™ Devices

		Temperature Measurement Range -10°C to 60°C or 14°F to 140°F (Wide Range Device)
		TSic-506
Temp (°C)	Temp (°F)	Digital
-10	14	0x000
0	32	0x124
25	77	0x3FF
60	140	0x7FF

Temperature = (Digital Signal / 2047 \* 70 - 10)°C

### Package Information

TSic™ 506 SOP8: 150mil, Standard SMT Package, SOIC, Based on IEC 191-2Q, Type 076E35 B.  
Other packages available on demand: TSic™ 506 e-line; 3 pin THT package; Chip on Flex.

For further information see also Technical Note:

*“TSic™ Die and Package Specifications for TSic™ Temperature Sensor IC”*



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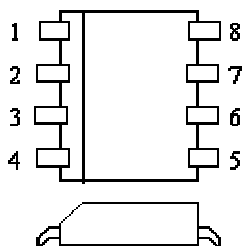


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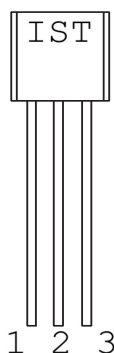
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#### SOP8 Package



Pin	Name	Description
1	V+	Supply voltage (3.0-5.5V)
2	Signal	Temperature output signal
4	GND	Ground
3, 5-8	TP/NC	Test pin / NC Do not connect

#### E-Line Package



Pin	Name	Description
1	GND	Ground
2	Signal	Temperature output signal
4	V+	Supply Voltage (3.0-5.5V)

#### Related products and ordering information

For related products and ordering information see [www.zmd.biz](http://www.zmd.biz) and ZMD "TSic™ Ordering Guide".



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#### ZMD Sales Contacts

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#### ZMD Distribution Partners

ZMD partners with WBC for device distribution in Europe and Future Electronics / All American for North- and South America.

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For the current revision of this document and for additional product information please look at [www.zmd.biz](http://www.zmd.biz).

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***Notes :***