Features

- **Fast Read Access Time - 45 ns**
- **Low Power CMOS Operation**
  - **100 µA max. Standby**
  - **25 mA max. Active at 5 MHz (AT27C010L)**
  - **40 mA max. Active at 5 MHz (AT27C010)**
- **Wide Selection of JEDEC Standard Packages**
  - **32-Lead 600-mil PDIP and Cerdip**
  - **32-Pad PLCC and LCC**
  - **32-Lead TSOP**
- **5 V ± 10% Supply**
- **High Reliability CMOS Technology**
  - **2000 V ESD Protection**
  - **200 mA Latchup Immunity**
- **Rapid Programming - 100 µs/byte (typical)**
- **Two-line Control**
- **CMOS and TTL Compatible Inputs and Outputs**
- **Integrated Product Identification Code**
- **Full Military, Commercial and Industrial Temperature Ranges**
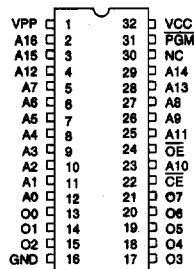
Description

The AT27C010/L chip family is a low-power, high performance 1,048,576 bit Ultraviolet Erasable and Electrically Programmable Read Only Memory (EPROM) organized as 128K x 8 bits. They require only one 5 V power supply in normal read mode operation. Any byte can be accessed in less than 45 ns, eliminating the need for speed reducing WAIT states on high performance microprocessor systems.

Two power versions are offered. In read mode, the AT27C010 typically consumes 25 mA while the AT27C010L takes only 8 mA. Standby mode supply current for both parts is typically less than 10 µA. *(continued)*
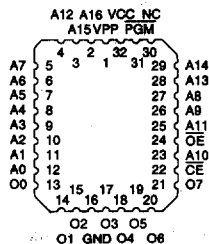
1 Megabit (128K x 8) UV Erasable CMOS EPROM

Pin Configurations

Pin Name	Function
A0-A16	Addresses
O0-O7	Outputs
$\overline{CE}$	Chip Enable
$\overline{OE}$	Output Enable
$\overline{PGM}$	Program Strobe
NC	No Connect

CDIP, PDIP Top View

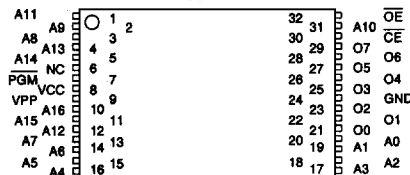|					
VPP	1		32	VCC	
A16	2		31	$\overline{PGM}$	
A15	3		30	NC	
A12	4		29	A14	
A7	5		28	A13	
A6	6		27	A8	
A5	7		26	A9	
A4	8		25	A11	
A3	9		24	$\overline{OE}$	
A2	10		23	A10	
A1	11		22	$\overline{CE}$	
A0	12		21	O7	
O0	13		20	O6	
O1	14		19	O5	
O2	15		18	O4	
GND	16		17	O3	

LCC, JLCC, PLCC Top

TSOP Top View
Type 1

Description (Continued)

The AT27C010/L comes in a choice of industry standard JEDEC-approved packages including: one-time programmable (OTP) plastic PDIP, PLCC, and TSOP, as well as windowed ceramic Cerdip and LCC. All devices feature two line control ($\overline{CE}$, $\overline{OE}$) to give designers the flexibility to prevent bus contention.

With high density 128K byte storage capability, the AT27C010/L allow firmware to be stored reliably and to be accessed by the system without the delays of mass storage media.

Atmel's 27C010/L have additional features to ensure high quality and efficient production use. The Rapid Programming Algorithm reduces the time required to program the part and guarantees reliable programming. Programming time is typically only 100 μs/byte. The Integrated Product Identification Code electronically identifies the device and manufacturer. This feature is used by industry standard programming equipment to select the proper programming algorithms and voltages.

Erasure Characteristics

The entire memory array of the AT27C010/L is erased (all outputs read as V_{OH}) after exposure to ultraviolet light at a wavelength of 2537Å. Complete erasure is assured after a minimum of 20 minutes exposure using 12,000 μW/cm² intensity lamps spaced one inch away from the chip. Minimum erase time for lamps at other intensity ratings can be calculated from the minimum integrated erasure dose of 15 W•sec/cm². To prevent unintentional erasure, an opaque label is recommended to cover the clear window on any UV erasable EPROM which will be subjected to continuous fluorescent indoor lighting or sunlight.

Block Diagram
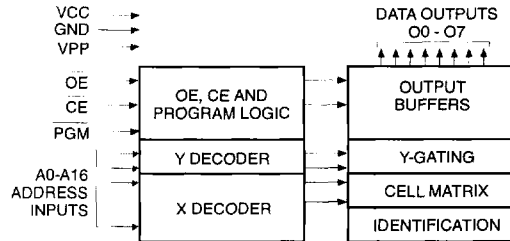
Absolute Maximum Ratings*

Temperature Under Bias	-55°C to +125°C
Storage Temperature	-65°C to +150°C
Voltage on Any Pin with Respect to Ground	-2.0 V to +7.0 V[1]
Voltage on A9 with Respect to Ground	-2.0 V to +14.0 V[1]
V_{PP} Supply Voltage with Respect to Ground	-2.0 V to +14.0 V[1]
Integrated UV Erase Dose	7258 W•sec/cm²

*NOTICE: Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Notes:
1. Minimum voltage is -0.6 V dc which may undershoot to -2.0 V for pulses of less than 20 ns. Maximum output pin voltage is V_{CC} + 0.75 V dc which may overshoot to +7.0 V for pulses of less than 20 ns.

Operating Modes

Mode \ Pin	$\overline{CE}$	$\overline{OE}$	$\overline{PGM}$	Ai	V_{PP}	V_{CC}	Outputs
Read	V_{IL}	V_{IL}	X[1]	Ai	X	V_{CC}	D_{OUT}
Output Disable	X	V_{IH}	X	X	X	V_{CC}	High Z
Standby	V_{IH}	X	X	X	X	V_{CC}	High Z
Rapid Program[2]	V_{IL}	V_{IH}	V_{IL}	Ai	V_{PP}	V_{CC}	D_{IN}
PGM Verify	V_{IL}	V_{IL}	V_{IH}	Ai	V_{PP}	V_{CC}	D_{OUT}
PGM Inhibit	V_{IH}	X	X	X	V_{PP}	V_{CC}	High Z
Product Identification[4]	V_{IL}	V_{IL}	X	A9=V_H [3] A0=V_{IH} or V_{IL} A1-A16=V_{IL}	X	V_{CC}	Identification Code

Notes:
1. X can be V_{IL} or V_{IH}.
2. Refer to Programming characteristics.
3. V_H = 12.0 ± 0.5 V.

4. Two identifier bytes may be selected. All Ai inputs are held low (V_{IL}), except A9 which is set to V_H and A0 which is toggled low (V_{IL}) to select the Manufacturer's Identification byte and high (V_{IH}) to select the Device Code byte.

AT27C010/L

D.C. and A.C. Operating Conditions for Read Operation

		AT27C010 / AT27C010L						
		-45	-55	-70	-90	-12	-15	-20
Operating Temp. (Case)	Com.	0°C - 70°C	0°C - 70°C	0°C - 70°C	0°C - 70°C	0°C - 70°C	0°C - 70°C	0°C - 70°C
	Ind.	-40°C - 85°C	-40°C - 85°C	-40°C - 85°C	-40°C - 85°C	-40°C - 85°C	-40°C - 85°C	-40°C - 85°C
	Mil.			-55°C-125°C	-55°C-125°C	-55°C-125°C	-55°C-125°C	-55°C-125°C
V_{CC} Supply		5 V ± 10%	5 V ± 10%	5 V ± 10%	5 V ± 10%	5 V ± 10%	5 V ± 10%	5 V ± 10%

▨ = Advance Information

D.C. and Operating Characteristics for Read Operation

Symbol	Parameter	Condition		Min	Max	Units
I_{LI}	Input Load Current	V_{IN} = 0 V to V_{CC}	Com.,Ind.		±1	μA
			Mil.		±5	μA
I_{LO}	Output Leakage Current	V_{OUT} = 0 V to V_{CC}	Com.,Ind.		±5	μA
			Mil.		±10	μA
I_{PP1} [2]	V_{PP} [1] Read/Standby Current	V_{PP} = V_{CC}			10	μA
I_{SB}	V_{CC} [1] Standby Current	I_{SB1} (CMOS), $\overline{CE}$ = V_{CC} ± 0.3 V			100	μA
		I_{SB2} (TTL), $\overline{CE}$ = 2.0 to V_{CC}+0.5 V			1	mA
I_{CC}	V_{CC} Active Current	f = 5 MHz, I_{OUT} = 0 mA, $\overline{CE}$ = V_{IL}	AT27C010L Com.		25	mA
			AT27C010L Ind.,Mil.		30	mA
			AT27C010 Com.		40	mA
			AT27C010 Ind.,Mil.		50	mA
V_{IL}	Input Low Voltage			-0.6	0.8	V
V_{IH}	Input High Voltage			2.0	V_{CC}+0.5	V
V_{OL}	Output Low Voltage	I_{OL} = 2.1 mA			.45	V
V_{OH}	Output High Voltage	I_{OH} = -100 μA		V_{CC}-0.3		V
		I_{OH} = -2.5 mA		3.5		V
		I_{OH} = -400 μA		2.4		V

Notes: 1. V_{CC} must be applied simultaneously or before V_{PP}, and removed simultaneously or after V_{PP}.
2. V_{PP} may be connected directly to V_{CC}, except during programming. The supply current would then be the sum of I_{CC} and I_{PP}.
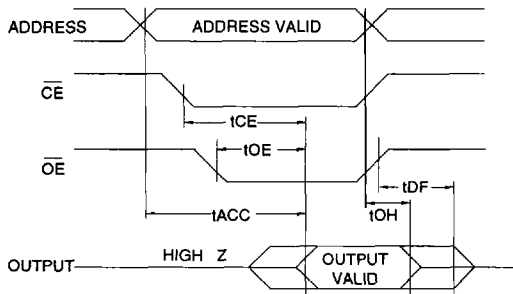
A.C. Characteristics for Read Operation

			AT27C010 / AT27C010L												
			-45		-55		-70		-90		-12		-15		-20
Symbol	Parameter	Condition	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min Max	Units
t_{ACC} [3]	Address to Output Delay	$\overline{CE}$ = $\overline{OE}$ = V_{IL} — Com.,Ind.		45		55		70		90		120		150	200	ns
		Mil.						70		90		120		150	200	ns
t_{CE} [2]	$\overline{CE}$ to Output Delay	$\overline{OE}$ = V_{IL}		45		55		70		90		120		150	200	ns
t_{OE} [2,3]	$\overline{OE}$ to Output Delay	$\overline{CE}$ = V_{IL}		20		25		30		35		35		40	70	ns
t_{DF} [4,5]	$\overline{OE}$ or $\overline{CE}$ High to Output Float			20		20		25		25		30		35	40	ns
t_{OH}	Output Hold from Address, $\overline{CE}$ or $\overline{OE}$, whichever occurred first		7		7		7		0		0		0		0		ns

Notes: 2, 3, 4, 5. - see AC Waveforms for Read Operation.

▨ = Advance Information

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A.C. Waveforms for Read Operation [1]
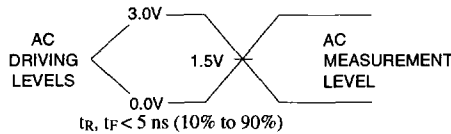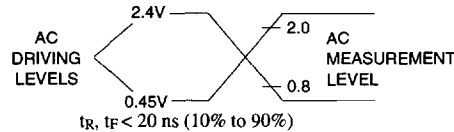
Input Test Waveforms and Measurement Levels

For -45 and -55 devices only:

t_R, $t_F < 5$ ns (10% to 90%)

For -70, -90, -12, -15, and -20 devices:

t_R, $t_F < 20$ ns (10% to 90%)

Output Test Load
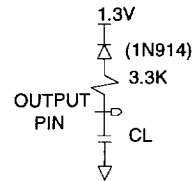
Note: C_L= 100 pF including jig capacitance, except for the -45 and -55 devices, where C_L= 30 pF.

Pin Capacitance (f = 1 MHz, T = 25°C) [1]

	Typ	Max	Units	Conditions
C_{IN}	4	8	pF	$V_{IN} = 0$ V
C_{OUT}	8	12	pF	$V_{OUT} = 0$ V

Notes: 1. Typical values for nominal supply voltage. This parameter is only sampled and is not 100% tested.

Programming Waveforms [1]
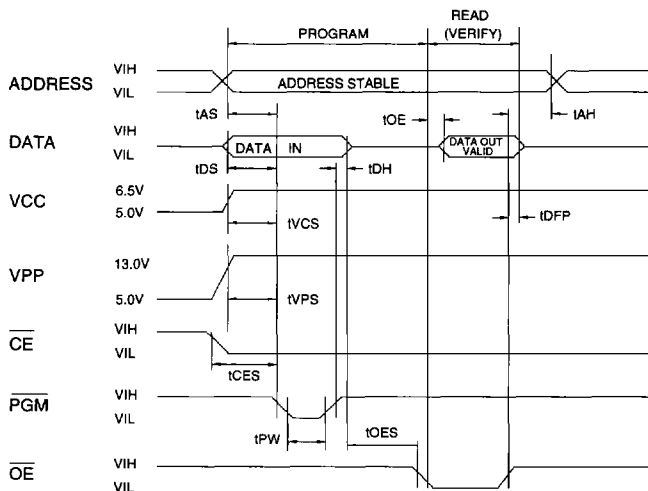
AT27C010/L

D.C. Programming Characteristics

$T_A = 25 \pm 5^\circ C$, $V_{CC} = 6.5 \pm 0.25$ V, $V_{PP} = 13.0 \pm 0.25$ V

Sym-bol	Parameter	Test Conditions	Limits Min	Max	Units
I_{LI}	Input Load Current	$V_{IN}=V_{IL},V_{IH}$		10	μA
V_{IL}	Input Low Level	(All Inputs)	-0.6	0.8	V
V_{IH}	Input High Level		2.0	$V_{CC}+1$	V
V_{OL}	Output Low Volt.	$I_{OL}=2.1$ mA		.45	V
V_{OH}	Output High Volt.	$I_{OH}=-400$ μA	2.4		V
I_{CC2}	V_{CC} Supply Current (Program and Verify)			40	mA
I_{PP2}	V_{PP} Supply Current	$\overline{CE}=\overline{PGM}=V_{IL}$		20	mA
V_{ID}	A9 Product Identification Voltage		11.5	12.5	V

A.C. Programming Characteristics

$T_A = 25 \pm 5^\circ C$, $V_{CC} = 6.5 \pm 0.25$ V, $V_{PP} = 13.0 \pm 0.25$ V

Sym-bol	Parameter	Test Conditions* (see Note 1)	Limits Min	Max	Units
t_{AS}	Address Setup Time		2		μs
t_{CES}	$\overline{CE}$ Setup Time		2		μs
t_{OES}	$\overline{OE}$ Setup Time		2		μs
t_{DS}	Data Setup Time		2		μs
t_{AH}	Address Hold Time		0		μs
t_{DH}	Data Hold Time		2		μs
t_{DFP}	$\overline{OE}$ High to Output Float Delay	(Note 2)	0	130	ns
t_{VPS}	V_{PP} Setup Time		2		μs
t_{VCS}	V_{CC} Setup Time		2		μs
t_{PW}	$\overline{PGM}$ Program Pulse Width	(Note 3)	95	105	μs
t_{OE}	Data Valid from $\overline{OE}$			150	ns

*A.C. Conditions of Test:
    Input Rise and Fall Times (10% to 90%) .... 20 ns
    Input Pulse Levels .... 0.45 V to 2.4 V
    Input Timing Reference Level .... 0.8 V to 2.0 V
    Output Timing Reference Level .... 0.8 V to 2.0 V

Notes:
1. V_{CC} must be applied simultaneously or before V_{PP} and removed simultaneously or after V_{PP}.
2. This parameter is only sampled and is not 100% tested. Output Float is defined as the point where data is no longer driven — see timing diagram.
3. Program Pulse width tolerance is 100 μsec ± 5%.

Atmel's 27C010/L Integrated Product Identification Code

Codes	Pins									Hex Data
	A0	O7	O6	O5	O4	O3	O2	O1	O0	
Manufacturer	0	0	0	0	1	1	1	1	0	1E
Device Type	1	0	0	0	0	0	1	0	1	05

Rapid Programming Algorithm

A 100 μs $\overline{PGM}$ pulse width is used to program. The address is set to the first location. V_{CC} is raised to 6.5 V and V_{PP} is raised to 13.0 V. Each address is first programmed with one 100 μs $\overline{PGM}$ pulse without verification. Then a verification / reprogramming loop is executed for each address. In the event a byte fails to pass verification, up to 10 successive 100 μs pulses are applied with a verification after each pulse. If the byte fails to verify after 10 pulses have been applied, the part is considered failed. After the byte verifies properly, the next address is selected until all have been checked. V_{PP} is then lowered to 5.0 V and V_{CC} to 5.0 V. All bytes are read again and compared with the original data to determine if the device passes or fails.

3
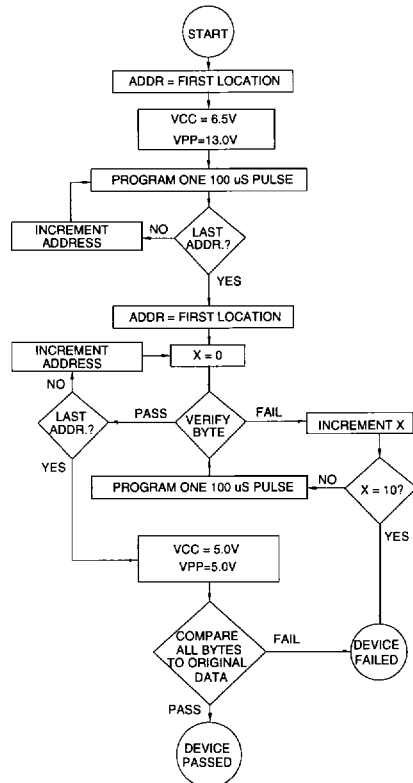
Ordering Information

▨ = Advance Information

tACC (ns)	ICC (mA)		Ordering Code	Package	Operation Range
	Active	Standby			
45	40	0.1	AT27C010-45DC AT27C010-45JC AT27C010-45LC AT27C010-45PC AT27C010-45TC	32DW6 32J 32LW 32P6 32T	Commercial (0°C to 70°C)
45	50	0.1	AT27C010-45DI AT27C010-45JI AT27C010-45LI AT27C010-45PI AT27C010-45TI	32DW6 32J 32LW 32P6 32T	Industrial (-40°C to 85°C)
55	40	0.1	AT27C010-55DC AT27C010-55JC AT27C010-55LC AT27C010-55PC AT27C010-55TC	32DW6 32J 32LW 32P6 32T	Commercial (0°C to 70°C)
55	50	0.1	AT27C010-55DI AT27C010-55JI AT27C010-55LI AT27C010-55PI AT27C010-55TI	32DW6 32J 32LW 32P6 32T	Industrial (-40°C to 85°C)
70	40	0.1	AT27C010-70DC AT27C010-70JC AT27C010-70LC AT27C010-70PC AT27C010-70TC	32DW6 32J 32LW 32P6 32T	Commercial (0°C to 70°C)
70	50	0.1	AT27C010-70DI AT27C010-70JI AT27C010-70LI AT27C010-70PI AT27C010-70TI	32DW6 32J 32LW 32P6 32T	Industrial (-40°C to 85°C)
			AT27C010-70DM AT27C010-70LM	32DW6 32LW	Military (-55°C to 125°C)
90	40	0.1	AT27C010-90DC AT27C010-90JC AT27C010-90KC AT27C010-90LC AT27C010-90PC AT27C010-90TC	32DW6 32J 32KW 32LW 32P6 32T	Commercial (0°C to 70°C)
90	50	0.1	AT27C010-90DI AT27C010-90JI AT27C010-90KI AT27C010-90LI AT27C010-90PI AT27C010-90TI	32DW6 32J 32KW 32LW 32P6 32T	Industrial (-40°C to 85°C)
			AT27C010-90DM AT27C010-90KM AT27C010-90LM	32DW6 32KW 32LW	Military (-55°C to 125°C)

Ordering Information

t$_{ACC}$ (ns)	I$_{CC}$ (mA)		Ordering Code	Package	Operation Range
	Active	Standby			
90	50	0.1	AT27C010-90DM/883 AT27C010-90KM/883 AT27C010-90LM/883	32DW6 32KW 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
120	40	0.1	AT27C010-12DC AT27C010-12JC AT27C010-12KC AT27C010-12LC AT27C010-12PC AT27C010-12TC	32DW6 32J 32KW 32LW 32P6 32T	Commercial (0°C to 70°C)
120	50	0.1	AT27C010-12DI AT27C010-12JI AT27C010-12KI AT27C010-12LI AT27C010-12PI AT27C010-12TI	32DW6 32J 32KW 32LW 32P6 32T	Industrial (-40°C to 85°C)
			AT27C010-12DM AT27C010-12KM AT27C010-12LM	32DW6 32KW 32LW	Military (-55°C to 125°C)
			AT27C010-12DM/883 AT27C010-12KM/883 AT27C010-12LM/883	32DW6 32KW 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
150	40	0.1	AT27C010-15DC AT27C010-15JC AT27C010-15KC AT27C010-15LC AT27C010-15PC AT27C010-15TC	32DW6 32J 32KW 32LW 32P6 32T	Commercial (0°C to 70°C)
150	50	0.1	AT27C010-15DI AT27C010-15JI AT27C010-15KI AT27C010-15LI AT27C010-15PI AT27C010-15TI	32DW6 32J 32KW 32LW 32P6 32T	Industrial (-40°C to 85°C)
			AT27C010-15DM AT27C010-15KM AT27C010-15LM	32DW6 32KW 32LW	Military (-55°C to 125°C)
			AT27C010-15DM/883 AT27C010-15KM/883 AT27C010-15LM/883	32DW6 32KW 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
200	40	0.1	AT27C010-20DC AT27C010-20JC AT27C010-20KC AT27C010-20LC AT27C010-20PC	32DW6 32J 32KW 32LW 32P6	Commercial (0°C to 70°C)

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Ordering Information

t_{ACC} (ns)	I_{CC} (mA) Active	I_{CC} (mA) Standby	Ordering Code	Package	Operation Range
200	50	0.1	AT27C010-20DI AT27C010-20JI AT27C010-20KI AT27C010-20LI AT27C010-20PI	32DW6 32J 32KW 32LW 32P6	Industrial (-40°C to 85°C)
			AT27C010-20DM AT27C010-20KM AT27C010-20LM	32DW6 32KW 32LW	Military (-55°C to 125°C)
			AT27C010-20DM/883 AT27C010-20KM/883 AT27C010-20LM/883	32DW6 32KW 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
90	50	0.1	5962-89614 07 M XX 5962-89614 07 M YX	32DW6 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
120	50	0.1	5962-89614 06 M XX 5962-89614 06 M YX	32DW6 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
150	50	0.1	5962-89614 05 M XX 5962-89614 05 M YX	32DW6 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
170	50	0.1	5962-89614 04 M XX 5962-89614 04 M YX	32DW6 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
200	50	0.1	5962-89614 03 M XX 5962-89614 03 M YX	32DW6 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
250	50	0.1	5962-89614 02 M XX 5962-89614 02 M YX	32DW6 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
300	50	0.1	5962-89614 01 M XX 5962-89614 01 M YX	32DW6 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)

AT27C010/L

Ordering Information

3

t$_{ACC}$ (ns)	Icc (mA)		Ordering Code	Package	Operation Range
	Active	Standby			
45	25	0.1	AT27C010L-45DC AT27C010L-45JC AT27C010L-45LC AT27C010L-45PC AT27C010L-45TC	32DW6 32J 32LW 32P6 32T	Commercial (0°C to 70°C)
45	30	0.1	AT27C010L-45DI AT27C010L-45JI AT27C010L-45LI AT27C010L-45PI AT27C010L-45TI	32DW6 32J 32LW 32P6 32T	Industrial (-40°C to 85°C)
55	25	0.1	AT27C010L-55DC AT27C010L-55JC AT27C010L-55LC AT27C010L-55PC AT27C010L-55TC	32DW6 32J 32LW 32P6 32T	Commercial (0°C to 70°C)
55	30	0.1	AT27C010L-55DI AT27C010L-55JI AT27C010L-55LI AT27C010L-55PI AT27C010L-55TI	32DW6 32J 32LW 32P6 32T	Industrial (-40°C to 85°C)
70	25	0.1	AT27C010L-70DC AT27C010L-70JC AT27C010L-70KC AT27C010L-70LC AT27C010L-70PC AT27C010L-70TC	32DW6 32J 32KW 32LW 32P6 32T	Commercial (0°C to 70°C)
			AT27C010L-70DM AT27C010L-70LM	32DW6 32LW	Military (-55°C to 125°C)
70	30	0.1	AT27C010L-70DI AT27C010L-70JI AT27C010L-70KI AT27C010L-70LI AT27C010L-70PI AT27C010L-70TI	32DW6 32J 32KW 32LW 32P6 32T	Industrial (-40°C to 85°C)
90	25	0.1	AT27C010L-90DC AT27C010L-90JC AT27C010L-90KC AT27C010L-90LC AT27C010L-90PC AT27C010L-90TC	32DW6 32J 32KW 32LW 32P6 32T	Commercial (0°C to 70°C)

Ordering Information

tACC (ns)	ICC (mA) Active	ICC (mA) Standby	Ordering Code	Package	Operation Range
90	30	0.1	AT27C010L-90DI AT27C010L-90JI AT27C010L-90KI AT27C010L-90LI AT27C010L-90PI AT27C010L-90TI	32DW6 32J 32KW 32LW 32P6 32T	Industrial (-40°C to 85°C)
			AT27C010L-90DM AT27C010L-90KM AT27C010L-90LM	32DW6 32KW 32LW	Military (-55°C to 125°C)
			AT27C010L-90DM/883 AT27C010L-90KM/883 AT27C010L-90LM/883	32DW6 32KW 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
120	25	0.1	AT27C010L-12DC AT27C010L-12JC AT27C010L-12KC AT27C010L-12LC AT27C010L-12PC AT27C010L-12TC	32DW6 32J 32KW 32LW 32P6 32T	Commercial (0°C to 70°C)
120	30	0.1	AT27C010L-12DI AT27C010L-12JI AT27C010L-12KI AT27C010L-12LI AT27C010L-12PI AT27C010L-12TI	32DW6 32J 32KW 32LW 32P6 32T	Industrial (-40°C to 85°C)
			AT27C010L-12DM AT27C010L-12KM AT27C010L-12LM	32DW6 32KW 32LW	Military (-55°C to 125°C)
			AT27C010L-12DM/883 AT27C010L-12KM/883 AT27C010L-12LM/883	32DW6 32KW 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
150	25	0.1	AT27C010L-15DC AT27C010L-15JC AT27C010L-15LC AT27C010L-15KC AT27C010L-15PC AT27C010L-15TC	32DW6 32J 32LW 32KW 32P6 32T	Commercial (0°C to 70°C)
150	30	0.1	AT27C010L-15DI AT27C010L-15JI AT27C010L-15KI AT27C010L-15LI AT27C010L-15PI AT27C010L-15TI	32DW6 32J 32KW 32LW 32P6 32T	Industrial (-40°C to 85°C)
			AT27C010L-15DM AT27C010L-15KM AT27C010L-15LM	32DW6 32KW 32LW	Military (-55°C to 125°C)
			AT27C010L-15DM/883 AT27C010L-15KM/883 AT27C010L-15LM/883	32DW6 32KW 32LW	Military/883C Class B, Fully Compliant (-55°C to 125)

AT27C010/L

Ordering Information

tACC (ns)	ICC (mA)		Ordering Code	Package	Operation Range
	Active	Standby			
200	25	0.1	AT27C010L-20DC AT27C010L-20JC AT27C010L-20KC AT27C010L-20LC AT27C010L-20PC	32DW6 32J 32KW 32LW 32P6	Commercial (0°C to 70°C)
200	30	0.1	AT27C010L-20DI AT27C010L-20JI AT27C010L-20KI AT27C010L-20LI AT27C010L-20PI	32DW6 32J 32KW 32LW 32P6	Industrial (-40°C to 85°C)
			AT27C010L-20DM AT27C010L-20KM AT27C010L-20LM	32DW6 32KW 32LW	Military (-55°C to 125°C)
			AT27C010L-20DM/883 AT27C010L-20KM/883 AT27C010L-20LM/883	32DW6 32KW 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)

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Package Type	
32DW6	32 Lead, 0.600" Wide, Windowed, Ceramic Dual Inline Package (Cerdip)
32J	32 Lead, Plastic J-Leaded Chip Carrier OTP (PLCC)
32KW	32 Lead, Windowed, Ceramic J-Leaded Chip Carrier (JLCC)
32LW	32 Pad, Windowed, Ceramic Leadless Chip Carrier (LCC)
32P6	32 Lead, 0.600" Wide, Plastic Dual Inline Package OTP (PDIP)
32T	32 Lead, Plastic Thin Small Outline Package OTP (TSOP)

ATMEL