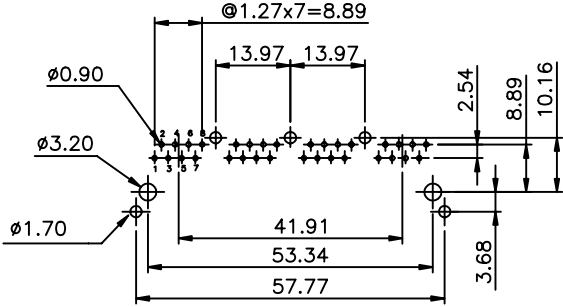
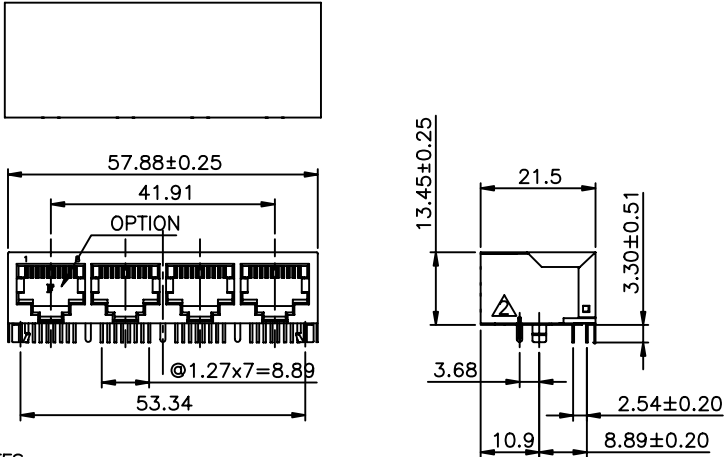


REVISION RECORD				
REV	ECO	DESCRIPTION	DRFT	CHKD
1		Drawing Modification	Jimmy	
1		Drawing Modification	Jimmy	



PCB Layout T=1.60mm
Component Side Shown

NOTES:

Characteristics Description:

1. RJ45 10/100 Base-Tx Transformer Jack With Transformer/ Impedance Resistor/High Voltage Capacitor.
2. Size Same As RJ-45 Modular Jack To Save PCB Board Space.
3. Reduce EMI Radiation, Improve EMI Performance.
4. Designed For Network Interface Card Application.
5. Designed To Meet IEEE 802.3u Requirement.
6. Designed For 100 Base Transmission over UTP-5 Cable.
7. Operation Temperature Range: -40°C To +85°C

Electrical Specification:

1. Inductance At 100 KHZ, 100 mV DC Bias: 350 uH MIN
2. C W/W (PF MAX): 28 PF
3. Leakage Inductance At 100 KHZ DC 100 mV: 0.3 uH MAX
4. Insertion Loss: 1.0 dB MAX @ 1-80 MHZ
1.2 dB MAX @ 100 MHZ
5. Return Loss: 14 dB MIN @ 1-60 MHZ
10 dB MIN @ 60-100 MHZ
6. Cross Talk: 35 dB MIN @ 1-80 MHZ
7. ISOLATION VOLTAGE @60HZ:1500Vrms(INPUT TO OUTPUT) 60SEC Δ

Mechanical:

1. Housing Material : Glass Filled Polyester UL94V-0.
2. Contact Material : Phosphor Bronze t=0.35mm.
3. Plating : 1: Contact Area Selective Gold Plating.
2: TailS Tin Over Nickel. Δ
4. Operating Life : 750 Cycles Min.
5. Pcb Retention Pre-Solder : 1 Lb Min.
6. Pcb Retention Post-Solder : 10 Lbs Min.

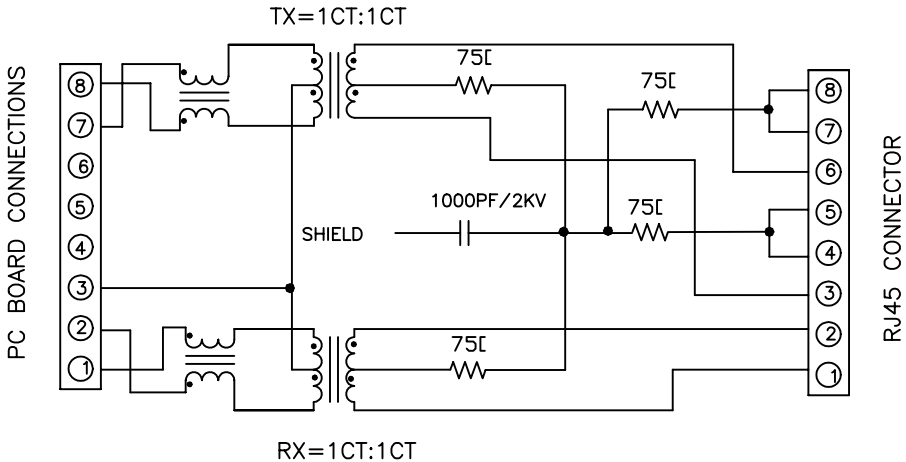
Environmental:

1. Storage : -40°C To+85°C.
- Mates With Modular Plug Conforming To
Fcc Part 68, Subpart F.

Part Number: ESTAB-YVC61X

1- 3u" 2- 6u" 3-15u"
4-30u" 5-50u"

Schematic:



MM (INCH)		DFTO SHJ	DATE 11/5/03'	FULL RISE ELECTRONIC CO., LTD			
TOLERANCES EXCEPT AS NOTED		CHKD Tirry	DATE 06/20/06				
MM		MFO Arthur	DATE 06/21/06	TITLE TRANSFORMER PCB GANG JACK (4 PORTS)			
.0 ±0.20	±	APPVL	DATE				
.00 ±0.15	±			DRAWING NO. GE5T3095			
.000 ±0.075	±	MATERIAL :					
ANGLES ±		QT'Y :		/PART NO.		SIZE A3	REV 2
		FINISH :		DO NOT SCALE DRAWING		SHEET X	OF Y
THIRD ANGLE PROJECTION		SCALE : 1:1					