



- 側面発光として実装可能な超薄型小型チップLEDです。
- Very-small-sized and thin chip LED which makes right-angle mounting available.

### ● 絶対最大定格/Absolute Maximum Rating<sup>※1</sup>

(Ta 25°C)

Series	Pd (mW)	I <sub>F</sub> (mA)	I <sub>FP</sub> <sup>※2</sup> (mA)	V <sub>R</sub> (V)	Top (°C)	Tst (°C)
CL-482T-WS	76	20	50	5	-25~+80	-30~+85

※1 1素子当たりの値です。/The values are based on 1-die performance.

※2 I<sub>FP</sub>の条件はduty 1/10、パルス幅0.1msecです。/Condition for I<sub>FP</sub> is pulse of 1/10 duty and 0.1msec width.

### ● 電気的光学的特性/Electro-optical Characteristics

(Ta 25°C)

Parameter	Symbol	Conditions	MIN	TYP	MAX	Unit
順電圧/Forward Voltage <sup>※1</sup>	V <sub>F</sub>	I <sub>F</sub> =5mA	—	2.9	3.15	V
逆電流/Reverse Current <sup>※1</sup>	I <sub>R</sub>	V <sub>R</sub> =5V	—	—	2	μA
光度/Luminous Intensity <sup>※2</sup>	I <sub>v</sub>	I <sub>F</sub> =5mA	60	115	—	mcd
色度座標/Chromaticity coordinates <sup>※3</sup>	x, y	I <sub>F</sub> =5mA	x(±0.02) a b c d	y(±0.02) 0.236 0.301 0.386 0.321		

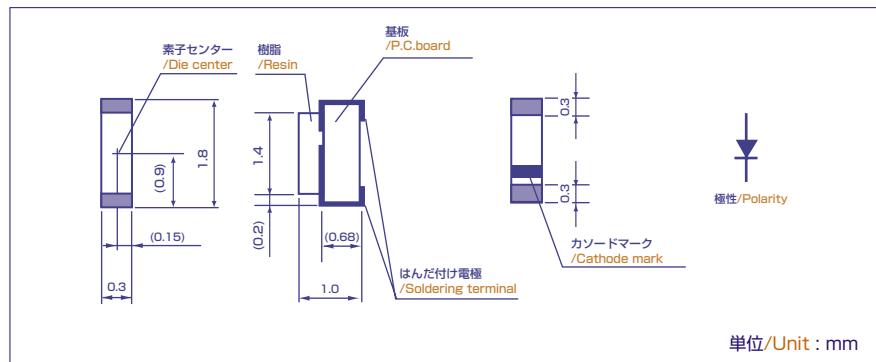
※1 1素子当たりの値です。/The values are based on 1-die performance.

※2 NIST規格に準拠、各素子同時点灯の値です。

Compliant with NIST. The values are based on the condition that each die lights simultaneously.

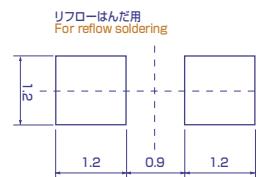
※3 色度座標は、a、b、c、dに囲まれた範囲です。/Chromaticity coordinates is surrounded with a,b,c,d

### ● 外形寸法図/Outline drawing



#### 推奨はんだ付けパターン

The following soldering patterns are recommended for reflow-soldering:



### ■ 諸特性/Characteristics

