T1625T-8I

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Standard 16 A Triac

Datasheet – production data

Features

- Four quadrants
- Trigger current of 25 mA
- Package is RoHS (2002/95/EC) compliant
- Tab insulated, voltage = 2500 V rms
- UL certified (ref. file E81734)

Applications

- General purpose AC line load switching
- Home appliances:
 - Fan
 - Pump
 - Solenoid
- Lighting
- Heaters
- Inrush current limiting circuits
- Overvoltage crowbar protection circuits

Description

Available in TO220AB-Ins. (ceramic insulated), the T1625T-8I Triac can be used as on/off or phase angle function controllers in general purpose AC switching.

Provides insulation rated at 2500 V rms (TO-220AB insulated package).

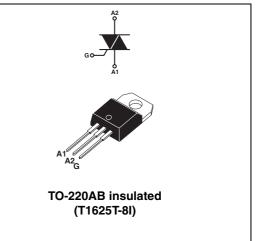


Table 1. Device summary

Order code	Quadrants	Value I _{GT} (mA)
T1625T-8I	- -	25
	IV	50

This is information on a product in full production.

1 Characteristics

Symbol	Parameter			Value	Unit	
I	I _{T(RMS)} On-state rms current (full sine wave)		T _c = 108 °C	16	٨	
IT(RMS)			T _c = 119 °C	12	A	
	Non repetitive surge peak on-state current (full	F = 50 Hz	t _p = 20 ms	120	А	
I _{TSM}	cycle, T _j initial = 25 °C)	F = 60 Hz	t _p = 16.7 ms	126	A	
l ² t	I ² t Value for fusing	•	t _p = 10 ms	95	A ² s	
V _{DRM} ,	Repetitive peak off-state voltage, gate open		T _j = 150 °C	600	V	
V_{RRM}			T _j = 125 °C	800		
V _{DSM} , V _{RSM}	Non repetitive surge peak off-state voltage	t _p = 10 ms	T _j = 25 °C	900	V	
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$		F = 100 Hz	100	A/µs	
I _{GM}	Peak gate current	t _p = 20 μs	T _j = 150 °C	4	А	
P _{G(AV)}	Average gate power dissipation		T _j = 150 °C	1	W	
T _{stg}	Storage junction temperature range				О°	
Тj	Operating junction temperature range		-40 to +150	Ũ		
ΤL	Lead temperature for soldering during 10 s (at 4 mm from case for TO220AB-ins.)			260	°C	
V _{ins} (rms)	Insulation rms voltage, 1 minute, TO220AB ceramic insulated			2500	V	

Table 2. Absolute maximum rating ($T_i = 25$ °C, unless otherwise specified)



Symbol	Test conditions				Value	Unit
			- -	MAX.	25	
I _{GT} ⁽¹⁾				MAX.	50	
'GT`′	V_D = 12 V, R_L = 30 Ω		- -	MIN.	1.25	mA
			IV	MIN.	2.50	
V _{GT}	V_D = 12 V, RL = 30 Ω		All	MAX.	1.3	V
V _{GD}	$V_{D} = 800 \text{ V}, \text{ R}_{L} = 3.3 \text{ k}\Omega, \text{ T}_{j} = 125 \text{ °C}$		All	MIN.	0.2	V
I _H ⁽¹⁾	I _T = 500 mA			MAX.	35	mA
	1 101		I - III-IV	MAX.	40	mA
ΙL	$I_{\rm G} = 1.2 I_{\rm GT}$	II	50			
dV/dt ⁽¹⁾	V _D = 67% x 800 V gate open	T _j = 125 °C		MIN.	500	V/µs
uv/ut V/	$V_D = 67\% \text{ x } 600 \text{ V gate open}$	T _j = 150 °C		IVIIIN.	300	
(dl/dt)c ⁽¹⁾	$(dV/dt)c = @ 10 V/\mu s$ $T_j = 125 °C T_j = 150 °C T_j = 150 °C$			MAINI	4	A/ms
				MIN.	2	
(dl/dt)c ⁽¹⁾	$T_j =$			MIN.	12	A /
	(dV/dt)c = @ 0.1 V/µs		IVIIIN.	6	A/ms	
t _{GT}	gate controlled turn on time I_{TM} = 13 A, V_D = 400 V, I_G = 100 mA, dI_G/dt = 100 mA/µs, R_L = 30 Ω		All	TYP.	2	μs

Table 3.Electrical characteristics ($T_j = 25$ °C, unless otherwise specified)

1. For both polarities of A2 referenced to A1

Table 4.Static characteristics

Symbol	Test conditions				Unit
V _{TM} ⁽¹⁾	I _{TM} = 22.6 A, t _p = 380 μs	T _j = 25 °C	MAX.	1.55	V
V _{to} ⁽¹⁾	Threshold voltage	T _j = 150 °C	MAX.	0.85	V
R _d ⁽¹⁾	Dynamic resistance	T _j = 150 °C	MAX.	30	mΩ
I _{DRM} I _{RRM}	V _ V _ 800 V	T _j = 25 °C	MAX.	5	μA
	$V_{\text{DRM}} = V_{\text{RRM}} = 800 \text{ V}$	T _j = 125 °C		1	mA
	$V_{DRM} = V_{RRM} = 600 V$	T _j = 150 °C	1	3.6	ШA

1. for both polarities of A2 referenced to A1

Table 5.Thermal resistance

Symbol	Parameter	Value	Unit
R _{th(j-c)}	Junction to case (AC)	2.1	°C/W
R _{th(j-a)}	Junction to ambient	60	°C/W



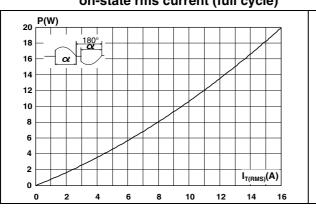
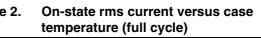
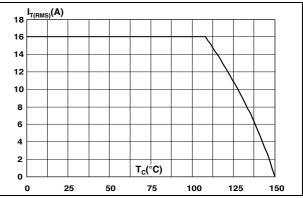


Figure 1. Maximum power dissipation versus Figure 2. on-state rms current (full cycle)





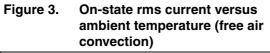


Figure 4. Relative variation of thermal impedance versus pulse duration

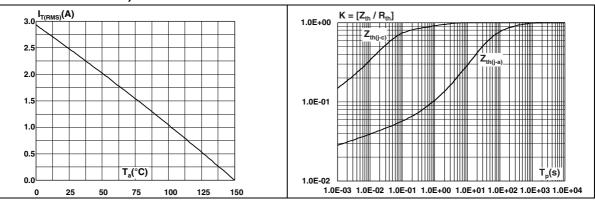
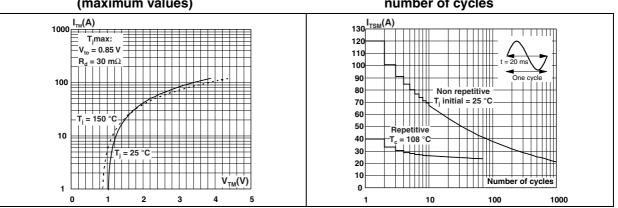


Figure 5. On-state characteristics (maximum values)

Figure 6. Surge peak on-state current versus number of cycles





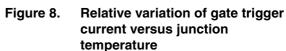
typical values

T_j(°C)

150

110 130

Non repetitive surge peak on-state Figure 7. current and corresponding values of I²t



 $I_{GT}[T_j]/I_{GT}[T_j = 25 \ ^{\circ}C]$

Q4 I_c

-10

10

30 50

Q1-Q2

4.0

3.5

3.0

2.5

2.0

1.5

1.0

0.5

0.0

-50 -30

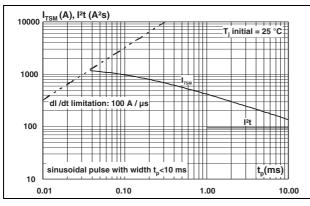


Figure 9. Relative variation of gate trigger voltage versus junction temperature

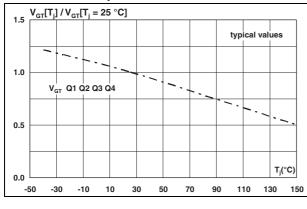
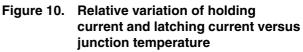
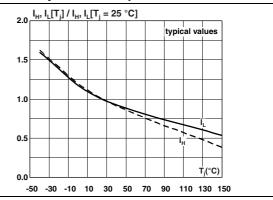


Figure 11. decrease of main current (di/dt)c versus reapplied (dV/dt)c

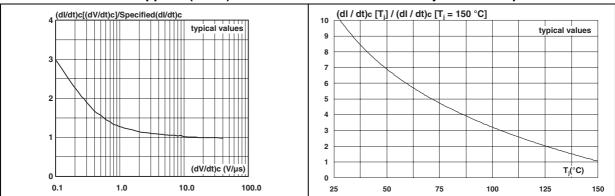


70

90



Relative variation of critical rate of Figure 12. Relative variation of critical rate of decrease of main current (di/dt)c versus junction temperature



0E+00
.0E-01
.0E-02
.0E-03
$0E-04 = V_{RRM} = 400 V = T_{j}(^{\circ}C) = 100$ 25 50 75 100 125 150

Figure 13. Relative variation of leakage current versus junction temperature for different values of blocking voltage



2 Package information

- Epoxy meets UL94, V0
- Recommended torque value: 0.4 to 0.6 N·m

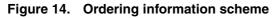
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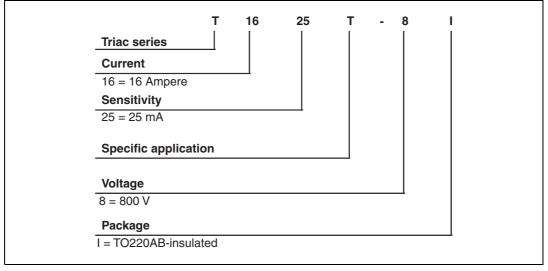
Table 6.TO-220AB (Nins. and ins. 20-up) dimensions

			Dimensions					
		Ref.	Mi	illimete	rs		Inches	
			Min.	Тур.	Max.	Min.	Тур.	Max.
		А	15.20		15.90	0.598		0.625
		a1		3.75			0.147	
ØI	ba C	a2	13.00		14.00	0.511		0.551
		В	10.00		10.40	0.393		0.409
	F	b1	0.61		0.88	0.024		0.034
A		b2	1.23		1.32	0.048		0.051
I4 I3 -⊕-		С	4.40		4.60	0.173		0.181
	c2	c1	0.49		0.70	0.019		0.027
		c2	2.40		2.72	0.094		0.107
a2		e	2.40		2.70	0.094		0.106
	M	F	6.20		6.60	0.244		0.259
e b1	← c1	ØI	3.75		3.85	0.147		0.151
		14	15.80	16.40	16.80	0.622	0.646	0.661
		L	2.65		2.95	0.104		0.116
		12	1.14		1.70	0.044		0.066
		13	1.14		1.70	0.044		0.066
		М		2.60			0.102	



3 Ordering information scheme







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4 Ordering information

Table 7. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
T1625T-8I	T1625T-8I	TO-220AB insulated	2.3	50	Tube

5 Revision history

Table 8.Document revision history

Date	Revision	Changes	
20-Jan-2012	1	First issue.	
25-Apr-2012	2	Updated UL certification.	



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