

## SWITCHING REGULATOR APPLICATIONS

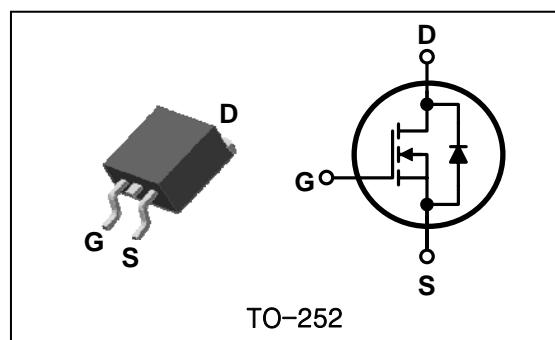
### Features

- High Voltage :  $BV_{DSS}=600V$ (Min.)
- Low  $C_{rss}$  :  $C_{rss}=3.4\text{pF}$ (Typ.)
- Low gate charge :  $Q_g=3.9\text{nC}$ (Typ.)
- Low  $R_{DS(on)}$  :  $R_{DS(on)}=11.5\Omega$ (Max.)

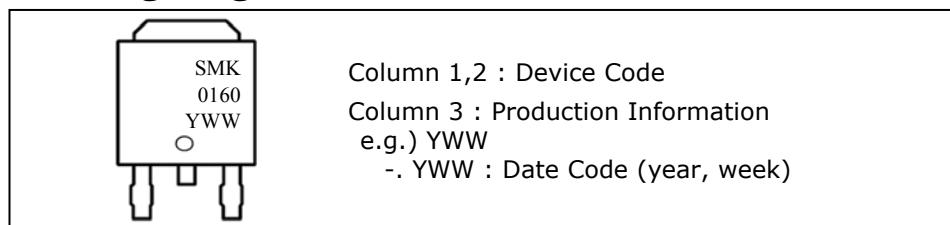
### Ordering Information

| Type No. | Marking | Package Code |
|----------|---------|--------------|
| SMK0160D | SMK0160 | TO-252       |

### PIN Connection



### Marking Diagram



### Absolute maximum ratings ( $T_c=25^\circ\text{C}$ unless otherwise noted)

| Characteristic                   | Symbol    | Rating                    | Unit             |
|----------------------------------|-----------|---------------------------|------------------|
| Drain-source voltage             | $V_{DSS}$ | 600                       | V                |
| Gate-source voltage              | $V_{GSS}$ | $\pm 30$                  | V                |
| Drain current (DC) *             | $I_D$     | $(T_c=25^\circ\text{C})$  | A                |
|                                  |           | $(T_c=100^\circ\text{C})$ | A                |
| Drain current (Pulsed) *         | $I_{DM}$  | 4.0                       | A                |
| Power dissipation                | $P_D$     | 28                        | W                |
| Avalanche current (Single) ②     | $I_{AS}$  | 1.0                       | A                |
| Single pulsed avalanche energy ② | $E_{AS}$  | 22                        | mJ               |
| Avalanche current (Repetitive) ① | $I_{AR}$  | 1.0                       | A                |
| Repetitive avalanche energy ①    | $E_{AR}$  | 2.5                       | mJ               |
| Junction temperature             | $T_J$     | 150                       | $^\circ\text{C}$ |
| Storage temperature range        | $T_{stg}$ | -55~150                   |                  |

\* Limited by maximum junction temperature

| Characteristic     | Symbol        | Typ. | Max. | Unit                      |
|--------------------|---------------|------|------|---------------------------|
| Thermal resistance | $R_{th(J-C)}$ | -    | 4.46 | $^\circ\text{C}/\text{W}$ |
|                    | $R_{th(J-A)}$ | -    | 62.5 |                           |

## Electrical Characteristics ( $T_C=25^\circ\text{C}$ unless otherwise noted)

| Characteristic                 | Symbol                   | Test Condition   | Min.   | Typ. | Max.      | Unit          |
|--------------------------------|--------------------------|--|--------|------|-----------|---------------|
| Drain-source breakdown voltage | $\text{BV}_{\text{DSS}}$ | $I_D=250\mu\text{A}, V_{GS}=0$                               | 600    | -    | -         | V             |
| Gate threshold voltage         | $V_{GS(\text{th})}$      | $I_D=250\mu\text{A}, V_{DS}=V_{GS}$                          | 2.0    | -    | 4.0       | V             |
| Drain-source cut-off current   | $I_{\text{DSS}}$         | $V_{DS}=600\text{V}, V_{GS}=0\text{V}$                       | -      | -    | 1         | $\mu\text{A}$ |
| Gate leakage current           | $I_{\text{GSS}}$         | $V_{DS}=0\text{V}, V_{GS}=\pm 30\text{V}$                    | -      | -    | $\pm 100$ | nA            |
| Drain-source on-resistance ④   | $R_{DS(\text{ON})}$      | $V_{GS}=10\text{V}, I_D=0.5\text{A}$                         | -      | 9.3  | 11.5      | $\Omega$      |
| Forward transfer conductance ④ | $g_{fs}$                 | $V_{DS}=10\text{V}, I_D=0.5\text{A}$                         | -      | 0.32 | -         | S             |
| Input capacitance              | $C_{iss}$                | $V_{GS}=0\text{V}, V_{DS}=25\text{V}, f=1\text{MHz}$         | -      | 131  | 164       | pF            |
| Output capacitance             | $C_{oss}$                |  | -      | 19.4 | 24.3      |               |
| Reverse transfer capacitance   | $C_{rss}$                |  | -      | 3.4  | 4.3       |               |
| Turn-on delay time             | $t_{d(on)}$              | $V_{DD}=300\text{V}, I_D=1.0\text{A}$<br>$R_G=25\Omega$      | -      | 5.5  | -         | ns            |
| Rise time                      | $t_r$                    |  | -      | 5    | -         |               |
| Turn-off delay time            | $t_{d(off)}$             |  | -      | 13   | -         |               |
| Fall time                      | $t_f$                    |  | -      | 28   | -         |               |
| Total gate charge              | $Q_g$                    | $V_{DS}=480\text{V}, V_{GS}=10\text{V}$<br>$I_D=1.0\text{A}$ | -      | 3.9  | 4.9       | nC            |
| Gate-source charge             | $Q_{gs}$                 |  | -      | 1.7  | -         |               |
| Gate-drain charge              | $Q_{gd}$                 |  | (3)(4) | -    | 0.85      | -             |

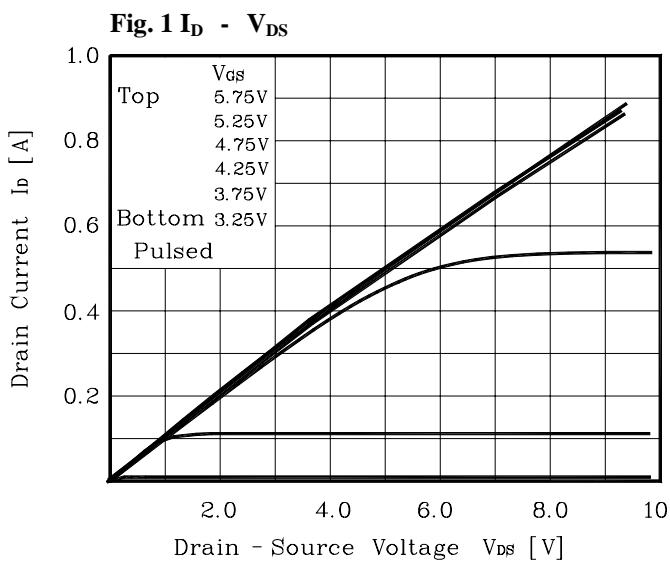
## Source-Drain Diode Ratings and Characteristics ( $T_C=25^\circ\text{C}$ unless otherwise noted)

| Characteristic            | Symbol   | Test Condition   | Min. | Typ. | Max. | Unit          |
|---------------------------|----------|--|------|------|------|---------------|
| Source current (DC)       | $I_S$    | Integral reverse diode in the MOSFET                                     | -    | -    | 1.0  | A             |
| Source current (Pulsed) ① | $I_{SM}$ |  | -    | -    | 4.0  |               |
| Forward voltage ④         | $V_{SD}$ | $V_{GS}=0\text{V}, I_S=1.0\text{A}$                                      | -    | -    | 1.4  | V             |
| Reverse recovery time     | $t_{rr}$ | $I_S=1.0\text{A}, V_{GS}=0\text{V}$<br>$dI_F/dt=100\text{A}/\mu\text{s}$ | -    | 190  | -    | ns            |
| Reverse recovery charge   | $Q_{rr}$ |  | -    | 0.53 | -    | $\mu\text{C}$ |

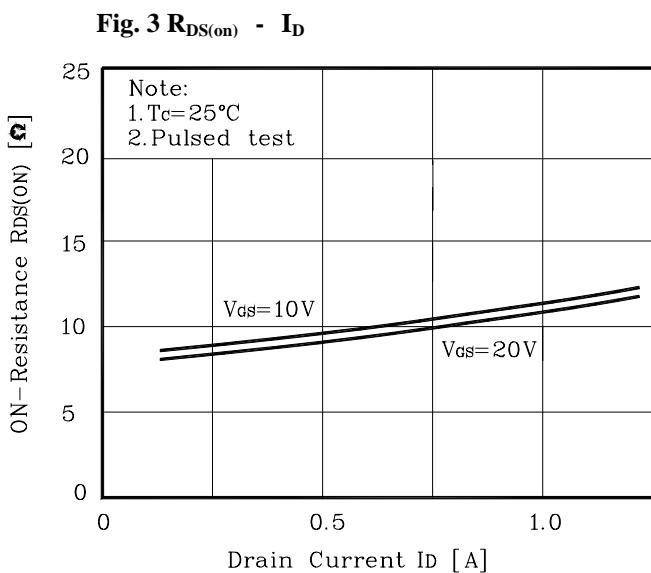
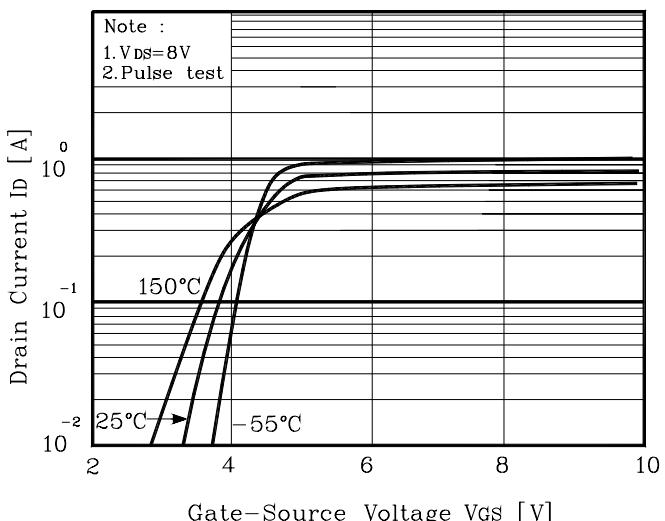
Note :

- ① Repetitive rating : Pulse width limited by maximum junction temperature
- ②  $L=1080\text{mH}, I_{AS}=0.3\text{A}, V_{DD}=50\text{V}, R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$
- ③ Pulse Test : Pulse width  $\leq 300\text{us}$ , Duty cycle  $\leq 2\%$
- ④ Essentially independent of operating temperature

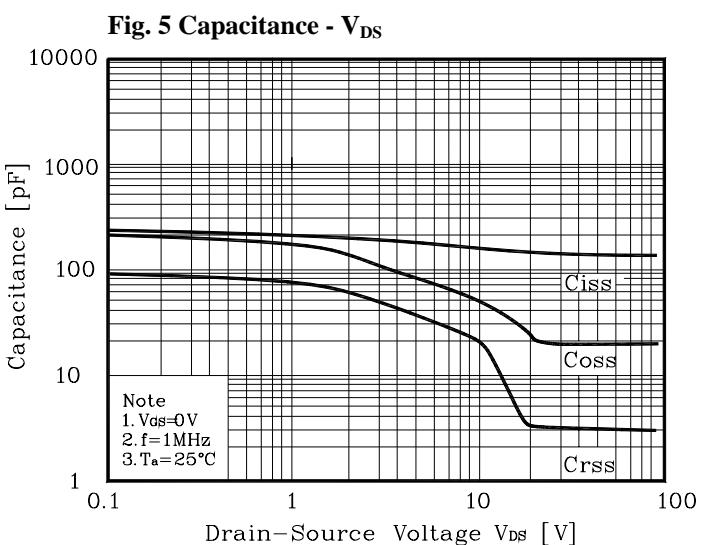
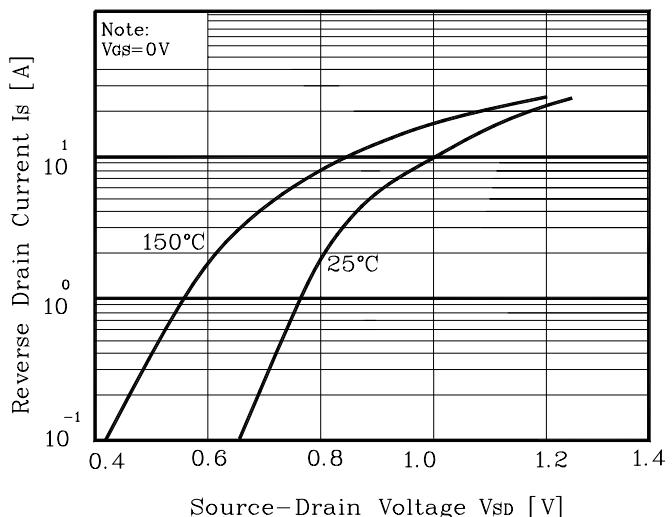
## Electrical Characteristic Curves



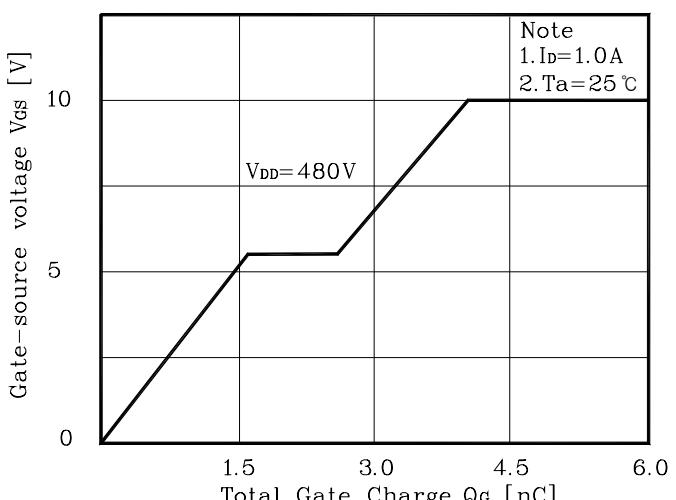
**Fig. 2  $I_D$  -  $V_{GS}$**



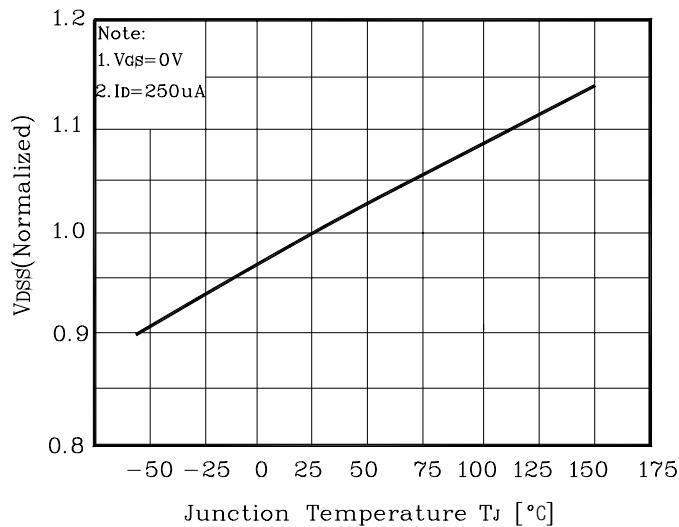
**Fig. 4  $I_S$  -  $V_{SD}$**



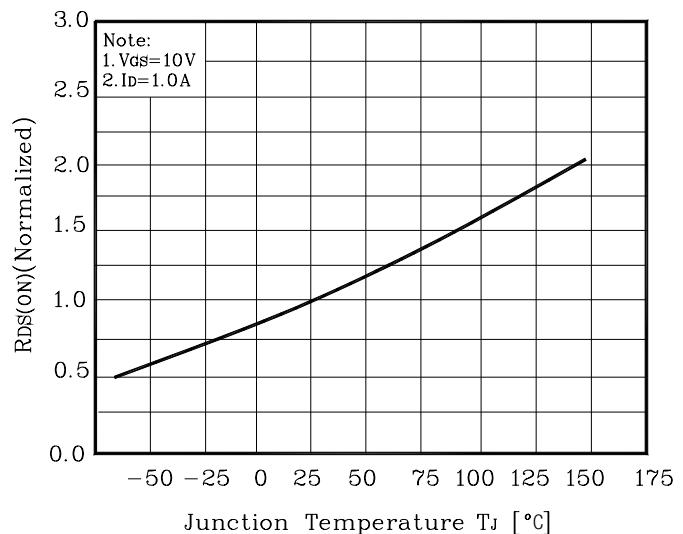
**Fig. 6  $V_{GS}$  -  $Q_G$**



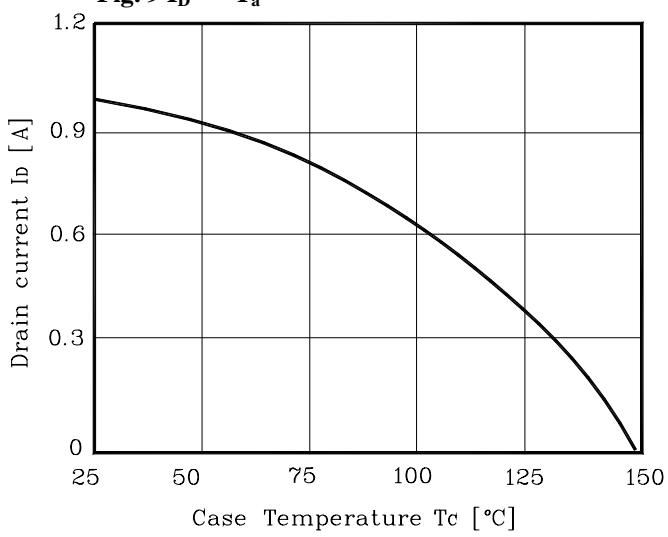
**Fig. 7  $V_{DSS}$  -  $T_J$**



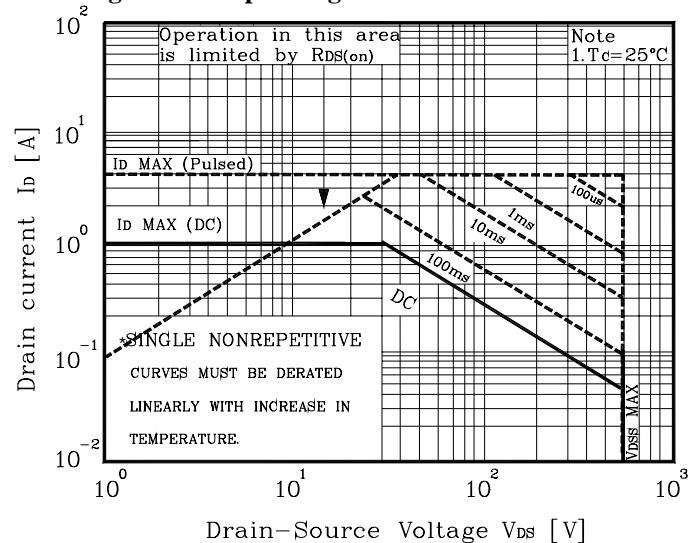
**Fig. 8  $R_{DS(on)}$  -  $T_J$**



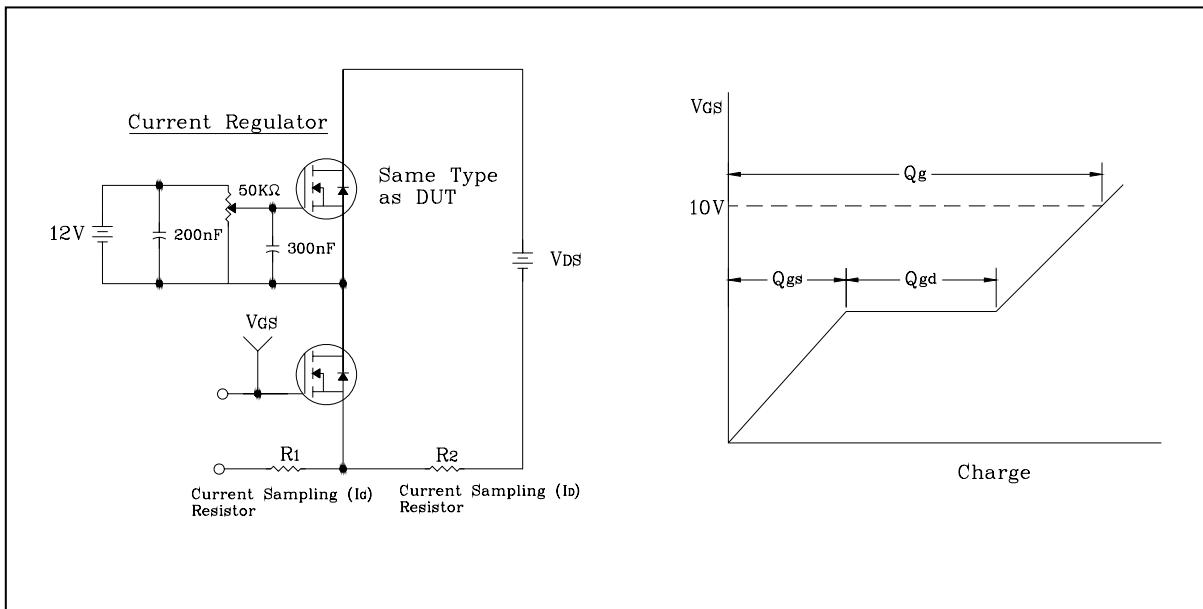
**Fig. 9  $I_D$  -  $T_a$**



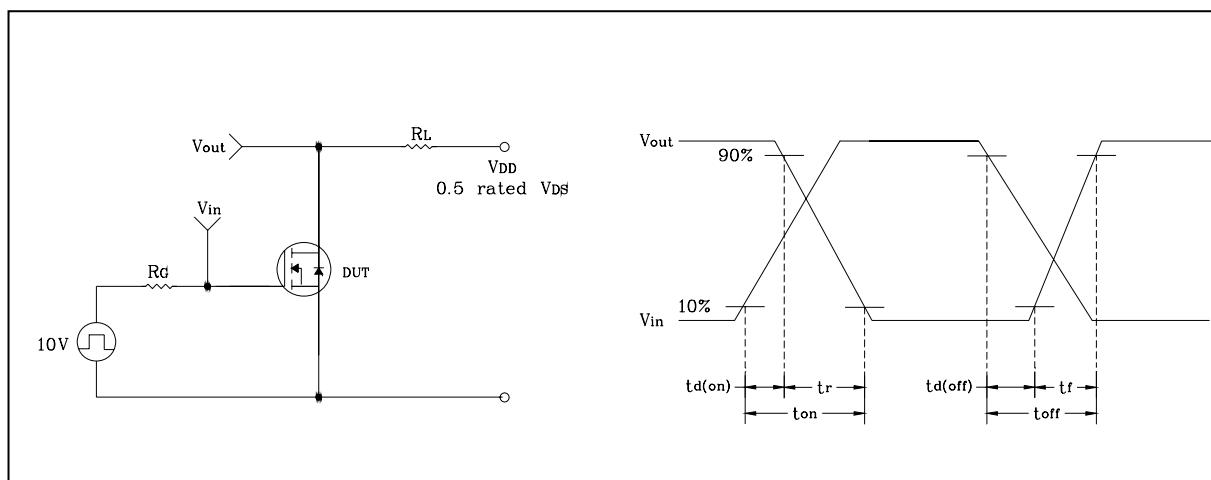
**Fig. 10 Safe Operating Area**



**Fig. 11 Gate Charge Test Circuit & Waveform**



**Fig. 12 Resistive Switching Test Circuit & Waveform**



**Fig. 13 E<sub>AS</sub> Test Circuit & Waveform**

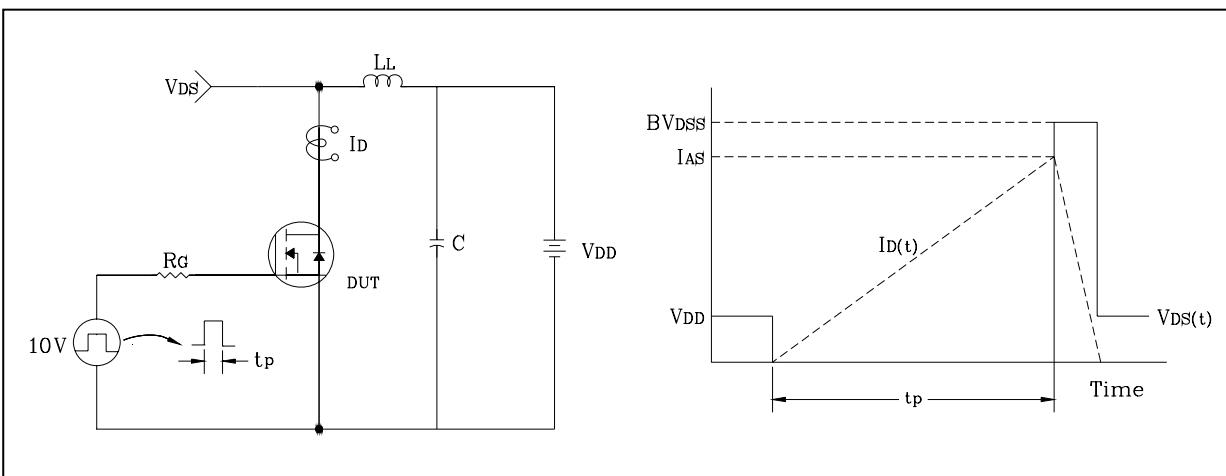
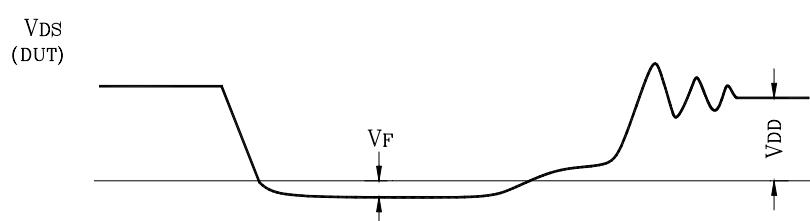
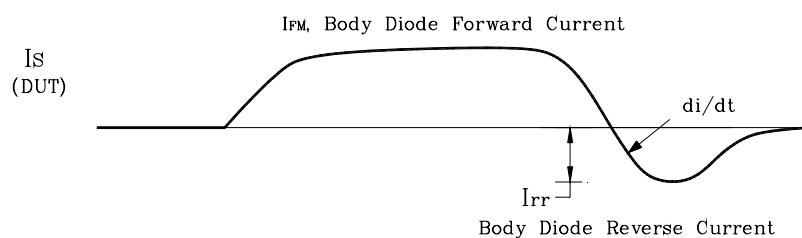
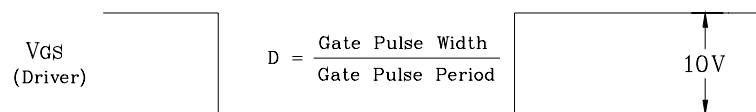
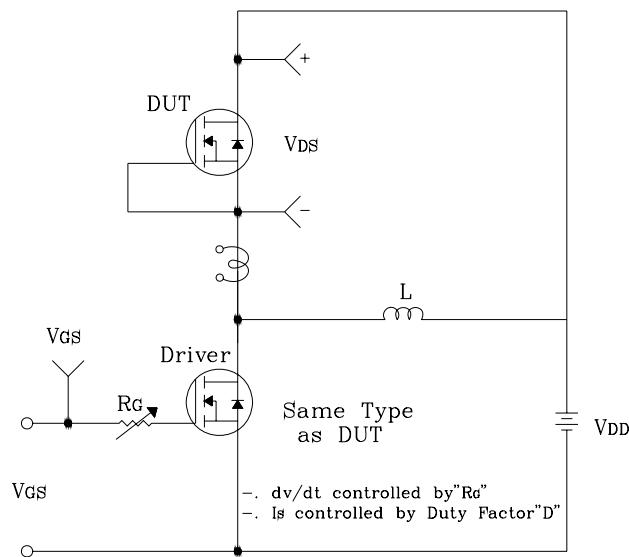
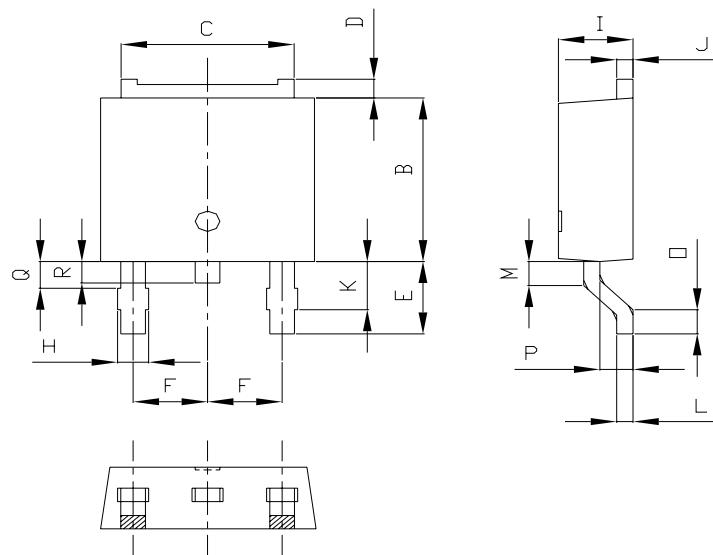


Fig. 14 Diode Reverse Recovery Time Test Circuit & Waveform

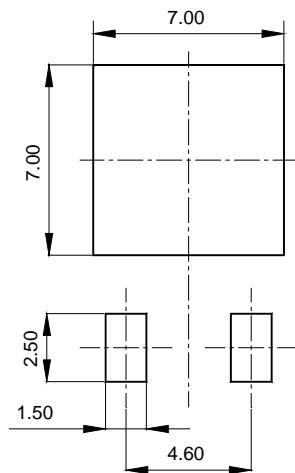


**Outline Dimension**

unit: mm



| SYMBOL | MILLIMETERS |         |         | NOTE |
|--------|-------------|---------|---------|------|
|        | MINIMUM     | NOMINAL | MAXIMUM |      |
| A      | 6.40        | 6.60    | 6.80    |      |
| B      | 5.90        | 6.10    | 6.30    |      |
| C      | 5.04        | 5.34    | 5.64    |      |
| D      | 0.50        | 0.70    | 0.90    |      |
| E      | 2.50        | 2.70    | 2.90    |      |
| F      | 2.10        | 2.30    | 2.50    |      |
| H      | 0.96 MAX    |         |         |      |
| I      | 2.20        | 2.30    | 2.40    |      |
| J      | 0.40        | 0.50    | 0.60    |      |
| K      | 1.60        | 1.80    | 2.00    |      |
| L      | 0.40        | 0.50    | 0.60    |      |
| M      | 0.81        | 0.91    | 1.01    |      |
| O      | 0.80        | 0.90    | 1.00    |      |
| P      | 0.90        | 1.00    | 1.10    |      |
| Q      | 0.95 MAX    |         |         |      |
| R      | 0.60        | 0.80    | 1.00    |      |

**Recommended Land Pattern [unit: mm]**

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