

February 2009

74LCX244 Low Voltage Buffer/Line Driver with 5V Tolerant Inputs and Outputs

Features

- 5V tolerant inputs and outputs
- 2.3V to 3.6V V_{CC} specifications provided
- 6.5ns t_{PD} max. $(V_{CC} = 3.3V)$, $10\mu A I_{CC}$ max.
- Power down high impedance inputs and outputs
- Supports live insertion/withdrawal⁽¹⁾
- ± 24 mA output drive ($V_{CC} = 3.0$ V)
- Implements proprietary noise/EMI reduction circuitry
- Latch-up performance exceeds 500mA
- ESD performance:
 - Human body model > 2000V
 - Machine model > 200V
- Leadless DQFN package

Note:

 To ensure the high-impedance state during power up or down, OE should be tied to V_{CC} through a pull-up resistor: the minimum value or the resistor is determined by the current-sourcing capability of the driver.

General Description

The LCX244 contains eight non-inverting buffers with 3-STATE outputs. The device may be employed as a memory address driver, clock driver and bus-oriented transmitter/receiver. The LCX244 is designed for low voltage (2.5V or 3.3V) V_{CC} applications with capability of interfacing to a 5V signal environment.

The LCX244 is fabricated with an advanced CMOS technology to achieve high speed operation while maintaining CMOS low power dissipation.

Ordering Information

Order Number	Package Number	Package Description
74LCX244WM	M20B	20-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300" Wide
74LCX244SJ	M20D	20-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
74LCX244BQX ⁽²⁾	MLP20B	20-Terminal Depopulated Quad Very-Thin Flat Pack No Leads (DQFN), JEDEC MO-241, 2.5 x 4.5mm
74LCX244MSA	MSA20	20-Lead Shrink Small Outline Package (SSOP), JEDEC MO-150, 5.3mm Wide
74LCX244MTC	MTC20	20-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide

Note:

2. DQFN package available in Tape and Reel only.

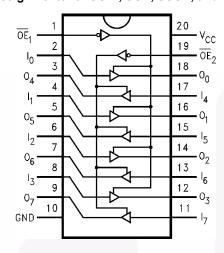
Device also available in Tape and Reel. Specify by appending suffix letter "X" to the ordering number.



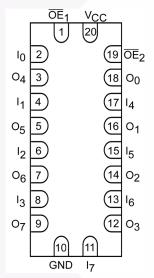
All packages are lead free per JEDEC: J-STD-020B standard.

Connection Diagram

Pin Assignments for SOIC, SOP, SSOP, and TSSOP



Pad Assignments for DQFN

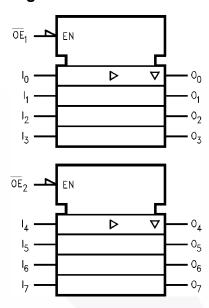


(Top Through View)

Pin Description

Pin Names	Description
$\overline{\text{OE}}_1, \overline{\text{OE}}_2$	3-STATE Output Enable Inputs
I ₀ —I ₇	Inputs
$\overline{O}_0 - \overline{O}_7$	Outputs

Logic Diagram



Truth Tables

Inp	uts	Outputs	
OE ₁	I _n	(Pins 12, 14, 16, 18)	
L	L	L	
L	Н	Н	
Н	Х	Z	

Inp	uts	Outputs
OE ₂	l _n	(Pins 3, 5, 7, 9)
L	L	L
L	Н	Н
Н	Х	Z

H = HIGH Voltage Level

L = LOW Voltage Level

X = Immaterial

Z = High Impedance

e absolut	Maximum Ratings ceeding the absolute maximum ratings may damage the decove the recommended operating ditions and stressing the extended exposure to stress above the recommended operate maximum ratings are stress ratings only.	iting condition	ons may affe	ct device reliabili	
Symbol	Parameter		Ratin	g	
V _{CC}	Supply Voltage		(0.5V to +7.0V	
V _I	DC Input Voltage		(0.5V to +7.0V	
Vo	DC Output Voltage				
	Output in 3-STATE			0.5V to +7.0V	
	Output in HIGH or LOW State		0.5V t	o V _{CC} + 0.5V	
I _{IK}	DC Input Diode Current, W# GND			50mA	
I _{OK}	DC Output Diode Current				
	У, # GND		50mA		
	R . Acc		+50mA		
I _O	DC Output Source/Sink Current		-50m		
I _{CC}	DC Supply Current per Supply Pin		–100mA		
I _{GND}	DC Ground Current per Ground Pin		–100mA		
T _{STG}	Storage Temperature		65°C to +150°C		
ecomme e Recomr	te Maximum Raing must be observed. ended Oper ating Conditions (4) mended Operating Conditions table defines dibreditions for accorditions are specified to ensure optimal impartions to the dat exceeding them or designing to absolute maximum rating:	asheet spicca			
commend					
		Min.	Max.	Units	
Symbol	Parameter	Min.	Max.	Units	
		Min. 2.0	Max. 3.6	Units	
Symbol	Parameter Supply Voltage				
Symbol	Parameter Supply Voltage Operating	2.0	3.6		

Symbol	Parameter	Min.	Max.	Units
V _{CC}	Supply Voltage			
	Operating	2.0	3.6	V
	Data Retention	1.5	3.6	
V _I	Input Voltage	0	5.5	V
V _O	Output Voltage			
	3-STATE	0	5.5	V
	HIGH or LOW State	0	сĄ	
I _{OH} /I _{OL}	Output Current			
	V _C ! 3.0V 3.6V		-24	mA
	₹ _C ! 2.7V 3.0V		-12	
	₹ _C ! 2.3V 2.7V		-8	
T _A	Free-Air Operating Temperature	40	85	°C
\$t/\$V	Input Edge Rate, W _N ! 0.8V 2.0V, V _{CC} ! 3.0V	0	10	n≰V

4. Unused inputs must be held HIGH or LOW. They may not float.

DC Electrical Characteristics

				$T_A = -40$ °C	to +85°C	
Symbol	Parameter	V _{CC} (V)	Conditions	Min.	Max.	Units
V _{IH}	HIGH Level Input Voltage	2.3–2.7		1.7		V
		2.7–3.6	-	2.0		
V _{IL}	LOW Level Input Voltage	2.3–2.7			0.7	V
		2.7–3.6			0.8	
V _{OH}	HIGH Level Output Voltage	2.3–3.6	$I_{OH} = -100\mu A$	V _{CC} – 0.2		V
		2.3	$I_{OH} = -8mA$	1.8		
		2.7	$I_{OH} = -12mA$	2.2		
		3.0	$I_{OH} = -18mA$	2.4		
			$I_{OH} = -24mA$	2.2		
V _{OL}	LOW Level Output Voltage	2.3–3.6	$I_{OL} = 100 \mu A$		0.2	V
		2.3	$I_{OL} = 8mA$		0.6	
		2.7	$I_{OL} = 12mA$		0.4	
		3.0	$I_{OL} = 16mA$		0.4	
			$I_{OL} = 24mA$		0.55	
l _l	Input Leakage Current	2.3–3.6	$0 \leq V_I \leq 5.5V$		±5.0	μA
l _{OZ}	3-STATE Output Leakage	2.3–3.6	$0 \le V_O \le 5.5V$, $V_I = V_{IH}$ or V_{IL}		±5.0	μA
I _{OFF}	Power-Off Leakage Current	0	V_I or $V_O = 5.5V$		10	μA
I _{CC}	Quiescent Supply Current	2.3–3.6	$V_I = V_{CC}$ or GND		10	μA
			$3.6V \le V_I, V_O \le 5.5V^{(5)}$		±10	
Δl _{CC}	Increase in I _{CC} per Input	2.3–3.6	$V_{IH} = V_{CC} - 0.6V$		500	μA

Note:

5. Outputs disabled or 3-STATE only.

AC Electrical Characteristics

	$T_A = -40$ °C to +85°C, $R_L = 500\Omega$							
		V _{CC} = 3.3 C _L =	3V ± 0.3V, 50pF		2.7V, 50pF	V _{CC} = 2.5 C _L =	V ± 0.2V, 30pF	
Symbol	Parameter	Min.	Max.	Min.	Max.	Min.	Max.	Units
t _{PHL} , t _{PLH}	Propagation Delay, Data to Output	1.5	6.5	1.5	7.5	1.5	7.8	ns
t _{PZL} , t _{PZH}	Output Enable Time	1.5	8.0	1.5	9.0	1.5	10.0	ns
t_{PLZ} , t_{PHZ}	Output Disable Time	1.5	7.0	1.5	8.0	1.5	8.4	ns
t _{OSHL} , t _{OSLH}	Output to Output Skew ⁽⁶⁾		1.0					ns

Note:

6. Skew is defined as the absolute value of the difference between the actual propagation delay for any two separate outputs of the same device. The specification applies to any outputs switching in the same direction, either HIGH-to-LOW (t_{OSHL}) or LOW-to-HIGH (t_{OSLH}).

Dynamic Switching Characteristics

				$T_A = 25^{\circ}C$	
Symbol	Parameter	V _{CC} (V)	Conditions	Typical	Unit
V _{OLP}	Quiet Output Dynamic Peak V _{OL}	3.3	$C_L = 50 pF, V_{IH} = 3.3 V, V_{IL} = 0 V$	0.8	V
		2.5	$C_L = 30 pF, V_{IH} = 2.5 V, V_{IL} = 0 V$	0.6	
V _{OLV}	Quiet Output Dynamic Valley V _{OL}	3.3	$C_L = 50 pF, V_{IH} = 3.3 V, V_{IL} = 0 V$	-0.8	V
		2.5	$C_L = 30pF, V_{IH} = 2.5V, V_{IL} = 0V$	-0.6	

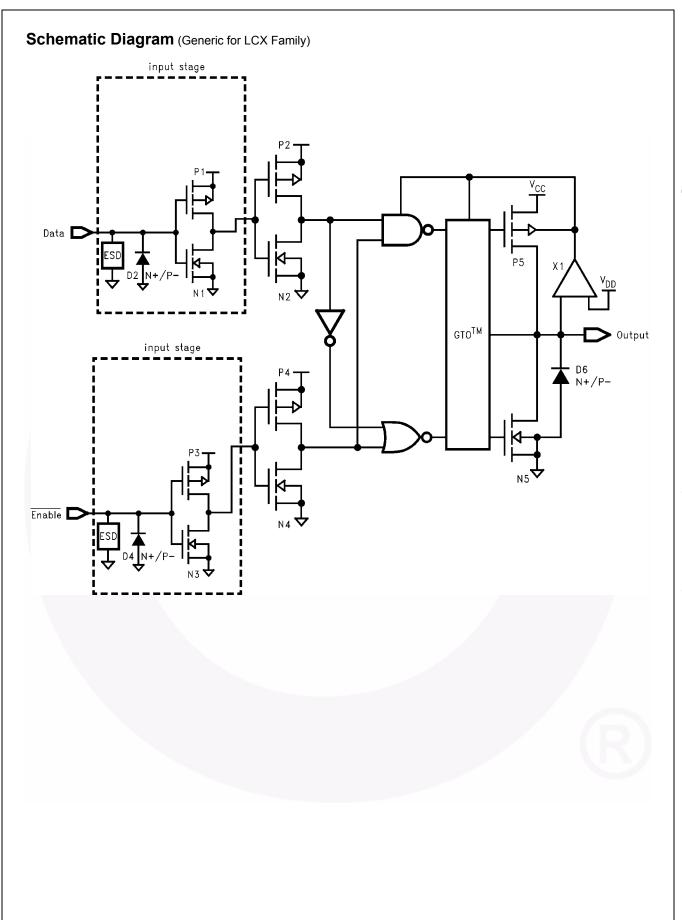
Capacitance

Symbol	Parameter	Conditions	Typical	Units
C _{IN}	Input Capacitance	$V_{CC} = Open, V_I = 0V \text{ or } V_{CC}$	7.0	pF
C _{OUT}	Output Capacitance	$V_{CC} = 3.3V$, $V_I = 0V$ or V_{CC}	8.0	pF
C _{PD}	Power Dissipation Capacitance	$V_{CC} = 3.3V$, $V_I = 0V$ or V_{CC} , $f = 10MHz$	25.0	pF

e absolut	Maximum Ratings ceeding the absolute maximum ratings may damage the decove the recommended operating ditions and stressing the extended exposure to stress above the recommended operate maximum ratings are stress ratings only.	iting condition	ons may affe	ct device reliabili	
Symbol	Parameter		Ratin	g	
V _{CC}	Supply Voltage		(0.5V to +7.0V	
V _I	DC Input Voltage		(0.5V to +7.0V	
Vo	DC Output Voltage				
	Output in 3-STATE			0.5V to +7.0V	
	Output in HIGH or LOW State		0.5V t	o V _{CC} + 0.5V	
I _{IK}	DC Input Diode Current, W# GND			50mA	
I _{OK}	DC Output Diode Current				
	У, # GND		50mA		
	R . Acc		+50mA		
I _O	DC Output Source/Sink Current		-50m		
I _{CC}	DC Supply Current per Supply Pin		–100mA		
I _{GND}	DC Ground Current per Ground Pin		–100mA		
T _{STG}	Storage Temperature		65°C to +150°C		
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		Min.	Max.	Units	
Symbol	Parameter	Min.	Max.	Units	
		Min. 2.0	Max. 3.6	Units	
Symbol	Parameter Supply Voltage				
Symbol	Parameter Supply Voltage Operating	2.0	3.6		

Symbol	Parameter	Min.	Max.	Units
V _{CC}	Supply Voltage			
	Operating	2.0	3.6	V
	Data Retention	1.5	3.6	
V _I	Input Voltage	0	5.5	V
V _O	Output Voltage			
	3-STATE	0	5.5	V
	HIGH or LOW State	0	сĄ	
I _{OH} /I _{OL}	Output Current			
	V _C ! 3.0V 3.6V		-24	mA
	₹ _C ! 2.7V 3.0V		-12	
	₹ _C ! 2.3V 2.7V		-8	
T _A	Free-Air Operating Temperature	40	85	°C
\$t/\$V	Input Edge Rate, W _N ! 0.8V 2.0V, V _{CC} ! 3.0V	0	10	n≰V

4. Unused inputs must be held HIGH or LOW. They may not float.

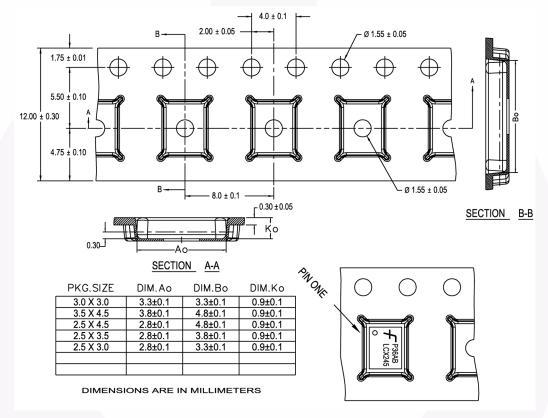


Tape and Reel Specification

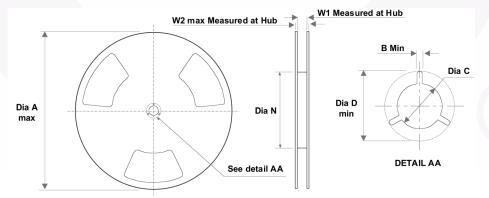
Tape Format for DQFN

Package Designator	Tape Section	Number of Cavities	Cavity Status	Cover Tape Status	
BQX	Leader (Start End)	125 (typ.)	Empty	Sealed	
	Carrier	3000	Filled	Sealed	
	Trailer (Hub End)	75 (typ.)	Empty	Sealed	

Tape Dimension inches (millimeters)



Reel Dimensions inches (millimeters)



Tape Size	Α	В	С	D	N	W1	W2
12mm	13.0 (330.0)	0.059 (1.50)	0.512 (13.00)	0.795 (20.20)	2.165 (55.00)	0.488 (12.4)	0.724 (18.4)

Physical Dimensions 13.00 12.60 11.43 В 9.50 10.65 7.60 10.00 7.40 2.25 10 0.51 1.27 PIN ONE 1.27 0.35 **INDICATOR** ⊕ 0.25 M C B A LAND PATTERN RECOMMENDATION 2.65 MAX SEE DETAIL A 0.33 0.20 0.30 0.10 0.75 X 45° SEATING PLANE NOTES: UNLESS OTHERWISE SPECIFIED (R0.10) A) THIS PACKAGE CONFORMS TO JEDEC **GAGE PLANE** MS-013, VARIATION AC, ISSUE E (R0.10) B) ALL DIMENSIONS ARE IN MILLIMETERS. 0.25 C) DIMENSIONS DO NOT INCLUDE MOLD FLASH OR BURRS. D) CONFORMS TO ASME Y14.5M-1994 1.27 0.40 SEATING PLANE E) LANDPATTERN STANDARD: SOIC127P1030X265-20L -(1.40)F) DRAWING FILENAME: MKT-M20BREV3 **DETAIL A** SCALE: 2:1

Figure 3. 20-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300" Wide

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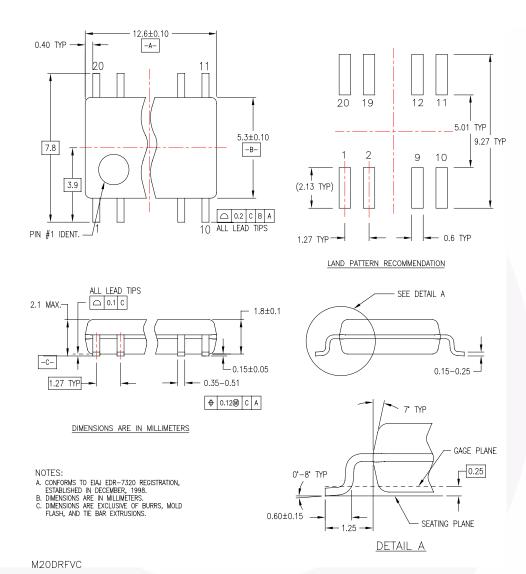
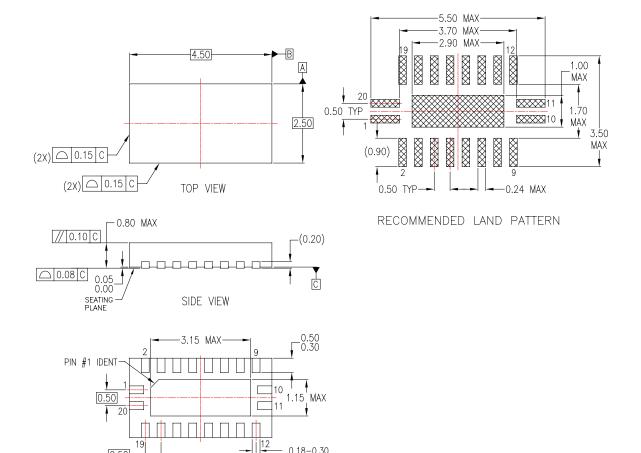


Figure 4. 20-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide

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0.10M

NOTES:

A. CONFORMS TO JEDEC REGISTRATION MO-241, VARIATION AC

BOTTOM VIEW

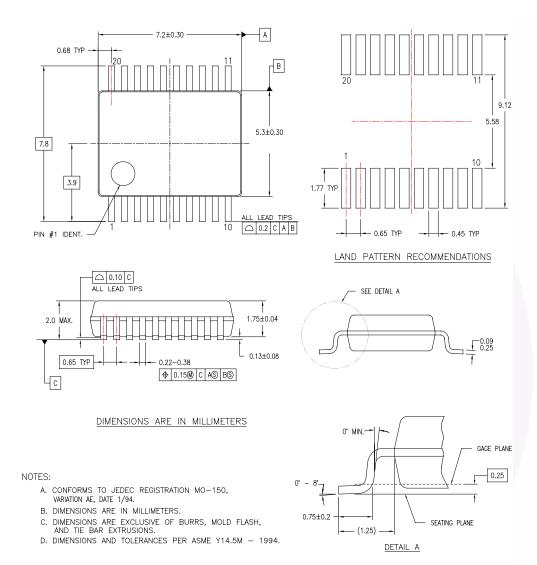
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994

MLP20BrevA

Figure 5. 20-Terminal Depopulated Quad Very-Thin FI at Pack No Leads (DQFN), JEDEC MO-241, 2.5 x 4.5mm

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MSA20RFVB

Figure 6. 20-Lead Shrink Small Outline Package (SSOP), JEDEC MO-150, 5.3mm Wide

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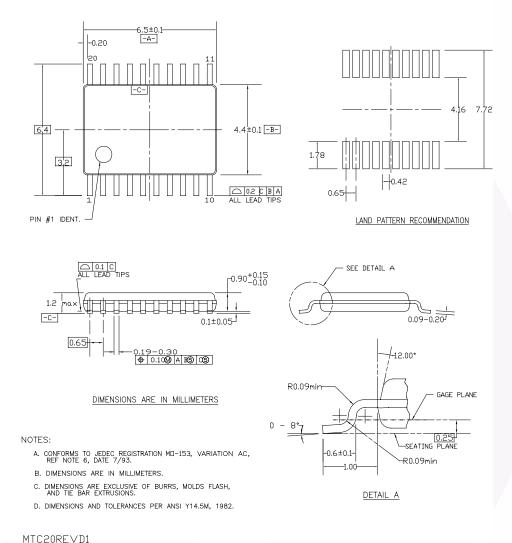


Figure 7. 20-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide

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Definition of Terms

Definition of Terms					
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchi Semiconductor reserves the right to make changes at any time without notice to improve design.			
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