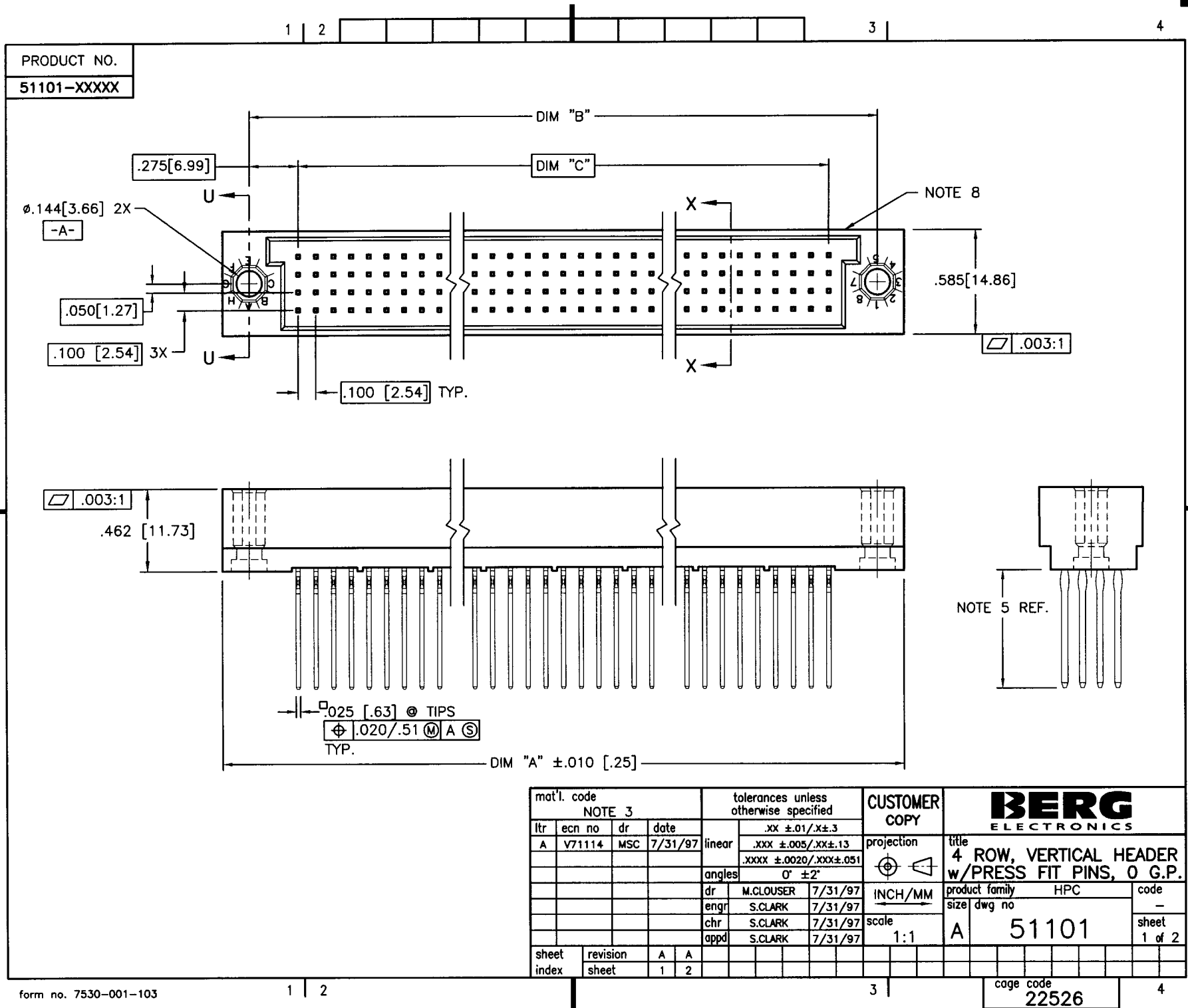


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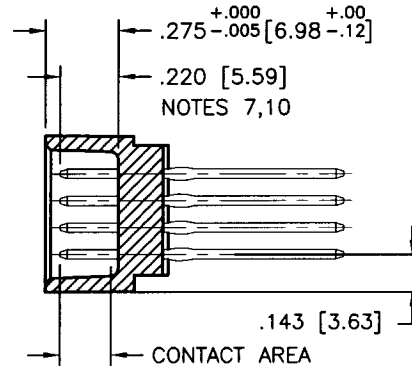
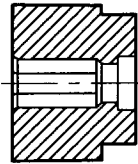
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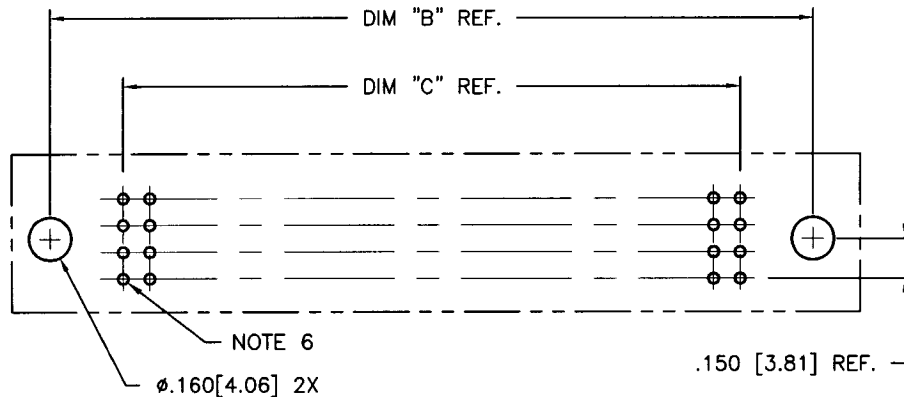
mat'l. code				tolerances unless otherwise specified				CUSTOMER COPY		BERG ELECTRONICS	
ltr	ecr no	dr	date	linear	linear	linear	linear	projection	projection		
A	V71114	MSC	7/31/97	linear	.XX ±.01/.X±.3	.XXX ±.005/.XX±.13	.XXXX ±.0020/.XXX±.051	0' ±2"	INCH/MM	title 4 ROW, VERTICAL HEADER w/PRESS FIT PINS, 0 G.P.	
				angles							
				dr	M.CLOUSER	7/31/97				product family HPC	
				enr	S.CLARK	7/31/97					
				chr	S.CLARK	7/31/97				size dwg no	
				appd	S.CLARK	7/31/97					
sheet	revision	A	A							A 51101	
index	sheet	1	2								
										cage code 22526	

PRODUCT NO.	DIM.	LENGTH FORMULAS
51101-XXXXX	DIM "A"	.100/2.54 x NO. OF COLUMNS +.75/19.1
	DIM "B"	.100/2.54 x NO. OF COLUMNS +.450/11.43
	DIM "C"	.100/2.54 x NO. OF COLUMNS -.100/2.54

SECTION U-U



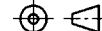
SECTION X-X



RECOMMENDED PCB LAYOUT
(ALL HOLES LOCATED ON .100/2.54 GRID UNLESS OTHERWISE SPECIFIED)

NOTES:

- PRODUCT NUMBERING CODE:
51101 - X XXX X
TAIL LENGTH(NOTE 5)
NO. OF POSITIONS(NOTE 2)
PLATING(NOTE 4)
BASE NUMBER
- THIS PRODUCT IS CONFIGURED IN SIZES OF 6 thru 80 COLUMNS IN INCREMENTS OF ONE.
i.e.: 6,7,8,9,.....79,80.
- HOUSING MATERIAL: GLASS FILLED PPS.
PIN MATERIAL: PHOS BRONZE
- PLATING CONTACT AREA OPTIONS:
1 = 30u"/.76u GOLD
3 = 50u"/1.27u GOLD
5 = 30u"/.76u GXT
- TAIL LENGTH OPTIONS:(±.015/.38)
A = .177/4.5 P = .470/11.94 .062 PCB/SHROUD
C = .533/13.54 L = .188/4.78 .062 PCB
D = .733/18.62 R = .250/6.35 .062 PCB
G = .250/6.35
- Ø.0453/1.151 DRILLED HOLE PLATED WITH .0003/.007 MIN. SnPb OVER .001/.03 to .003/.08 Cu. PLATING TO ACHIEVE A Ø.040±.003/1.02±.08 HOLE.
- TO ASSURE PROPER INSTALLATION: BERG TOOLING, AS SPECIFIED IN INSTRUCTION MANUAL 155915-001 MUST BE USED. PIN MATING LENGTH WILL BE ±.002/.05 AFTER INSTALLATION.
- "BERG" NAME, PART NUMBER AND LOT CODE TO APPEAR ON THIS SURFACE PER BUS-12-108.
- FOR DOCUMENTATION MENU, SEE BERG BUS-01-057.
- REFERENCE 50807 FOR F.M.L.B. AVAILABLE OPTIONS AND WORKSHEET.
- MANUFACTURE AS PER BUS-24-004.

mat'l. code NOTE 3				tolerances unless otherwise specified			CUSTOMER COPY		BERG ELECTRONICS			
ltr	ecn no	dr	date	linear	.XX ±.01/.X±.3			projection 	title 4 ROW, VERTICAL HEADER w/PRESS FIT PINS, 0 G.P.			
A					.XXX ±.005/.XX±.13							
					.XXX ±.0020/.XXX±.051							
				angles	0° ±2°			INCH/MM 	product family HPC size dwg no A 51101			
				dr	M.CLOUSER	7/31/97						
				enrg	S.CLARK	7/31/97						
				chr	S.CLARK	7/31/97						
				appd	S.CLARK	7/31/97						
sheet index	revision sheet							scale 1:1	sheet 2 of			