

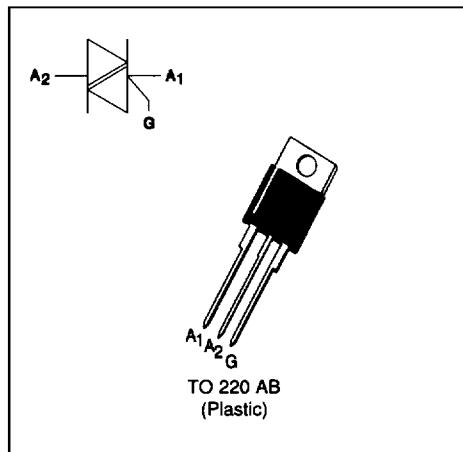
**SNUBBERLESS TRIACS**
**FEATURES**

- HIGH COMMUTATION :  $(dI/dt)_c > 14A/ms$  without snubber
- HIGH SURGE CURRENT :  $I_{TSM} = 160A$
- $V_{DRM}$  UP TO 800V
- BTA Family :
  - INSULATING VOLTAGE = 2500V(RMS)
  - (UL RECOGNIZED : E81734)

**DESCRIPTION**

The BTA/BTB16 BW/CW triacs use high performance glass passivated chips technology.

The SNUBBERLESS™ concept offer suppression of RC network and it is suitable for application such as phase control and static switching on inductive or resistive load.


**ABSOLUTE RATINGS (limiting values)**

Symbol	Parameter			Value	Unit
$I_T$ (RMS)	RMS on-state current (360° conduction angle)		<b>BTA</b>	$T_c = 80^\circ C$	<b>A</b>
	<b>BTB</b>	$T_c = 90^\circ C$			
$I_{TSM}$	Non repetitive surge peak on-state current ( $T_j$ initial = 25°C)		$t_p = 8.3 \text{ ms}$	170	<b>A</b>
			$t_p = 10 \text{ ms}$	160	
$I^2t$	$I^2t$ value	$t_p = 10 \text{ ms}$		128	<b>A<sup>2</sup>s</b>
$dl/dt$	Critical rate of rise of on-state current Gate supply : $I_G = 500\text{mA}$ $dI/dt = 1\text{A}/\mu\text{s}$		Repetitive $F = 50 \text{ Hz}$	20	<b>A/<math>\mu</math>s</b>
			Non Repetitive	100	
$T_{stg}$ $T_j$	Storage and operating junction temperature range		- 40 to + 150	$^\circ C$	$^\circ C$
			- 40 to + 125	$^\circ C$	
$T_l$	Maximum lead temperature for soldering during 10 s at 4.5 mm from case			230	$^\circ C$

Symbol	Parameter	BTA / BTB16... BW/CW				Unit
		400	600	700	800	
$V_{DRM}$ $V_{RRM}$	Repetitive peak off-state voltage $T_j = 125^\circ C$	400	600	700	800	<b>V</b>

## THERMAL RESISTANCES

Symbol	Parameter		Value		Unit
R <sub>th</sub> (j-a)	Junction to ambient		60		°C/W
R <sub>th</sub> (j-c) DC	Junction to case for DC		BTA	3.1	°C/W
		BTB	2.3		
R <sub>th</sub> (j-c) AC	Junction to case for 360° conduction angle (F = 50 Hz)		BTA	2.3	°C/W
		BTB	1.75		

## GATE CHARACTERISTICS (maximum values)

P<sub>G</sub> (AV) = 1W P<sub>GM</sub> = 40W (tp = 20 μs) I<sub>GM</sub> = 4A (tp = 20 μs) V<sub>GM</sub> = 16V (tp = 20 μs).

## ELECTRICAL CHARACTERISTICS

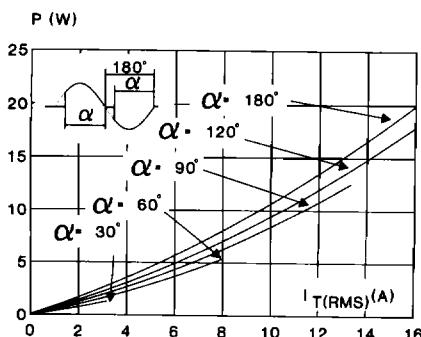
Symbol	Test Conditions	Quadrant		Suffix		Unit
				BW	CW	
I <sub>GT</sub>	V <sub>D</sub> =12V (DC) R <sub>L</sub> =33Ω	T <sub>j</sub> =25°C	I-II-III	MIN	2	mA
				MAX	50	
V <sub>GT</sub>	V <sub>D</sub> =12V (DC) R <sub>L</sub> =33Ω	T <sub>j</sub> =25°C	I-II-III	MAX	1.5	V
V <sub>GD</sub>	V <sub>D</sub> =V <sub>DRM</sub> R <sub>L</sub> =3.3kΩ	T <sub>j</sub> =125°C	I-II-III	MIN	0.2	V
t <sub>gt</sub>	V <sub>D</sub> =V <sub>DRM</sub> I <sub>G</sub> = 500mA dI <sub>G</sub> /dt = 3A/μs	T <sub>j</sub> =25°C	I-II-III	TYP	2	μs
I <sub>L</sub>	I <sub>G</sub> =1.2 I <sub>GT</sub>	T <sub>j</sub> =25°C	I-III	TYP	40	mA
			II	TYP	80	
			I-III	MAX	-	
			II	MAX	-	
I <sub>H</sub> *	I <sub>T</sub> = 500mA gate open	T <sub>j</sub> =25°C		MAX	50	mA
V <sub>TM</sub> *	I <sub>TM</sub> = 22.5A tp= 380μs	T <sub>j</sub> =25°C		MAX	1.60	V
I <sub>DRM</sub> I <sub>RRM</sub>	V <sub>DRM</sub> Rated V <sub>RRM</sub> Rated	T <sub>j</sub> =25°C		MAX	0.01	mA
		T <sub>j</sub> =125°C		MAX	2	
dV/dt *	Linear slope up to V <sub>D</sub> =67%V <sub>DRM</sub> gate open	T <sub>j</sub> =125°C		MIN	500	V/μs
				TYP	750	
(dI/dt) <sub>c</sub> *	Without snubber	T <sub>j</sub> =125°C		MIN	14	A/ms
				TYP	28	

\* For either polarity of electrode A<sub>2</sub> voltage with reference to electrode A<sub>1</sub>.

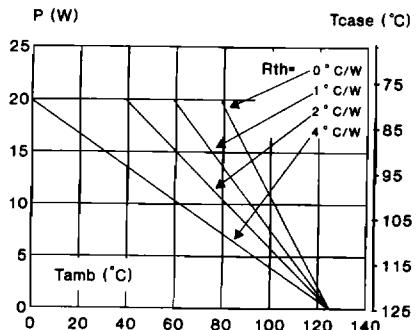
## ORDERING INFORMATION

Package	I <sub>T</sub> (RMS)	V <sub>DRM</sub> / V <sub>RRM</sub>	Sensitivity Specification	
			A	V
BTA (Insulated)	16	400	X	X
		600	X	X
		700	X	X
		800	X	X
BTB (Uninsulated)	16	400	X	X
		600	X	X
		700	X	X
		800	X	X

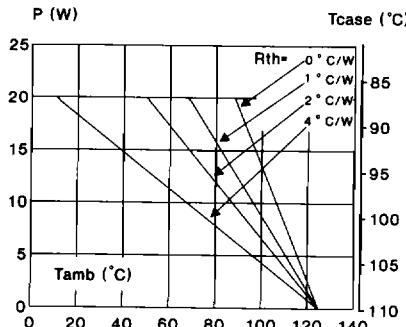
**Fig.1** : Maximum RMS power dissipation versus RMS on-state current ( $F=50\text{Hz}$ ).  
(Curves are cut off by  $(dI/dt)c$  limitation)



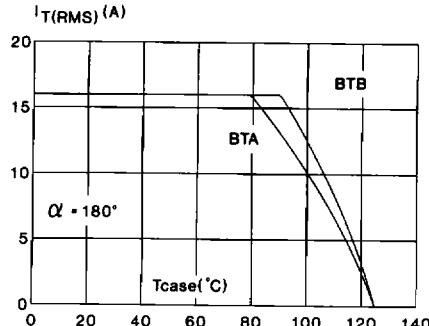
**Fig.2** : Correlation between maximum RMS power dissipation and maximum allowable temperatures ( $T_{amb}$  and  $T_{case}$ ) for different thermal resistances heatsink + contact (BTA).



**Fig.3** : Correlation between maximum RMS power dissipation and maximum allowable temperatures ( $T_{amb}$  and  $T_{case}$ ) for different thermal resistances heatsink + contact (BTB).

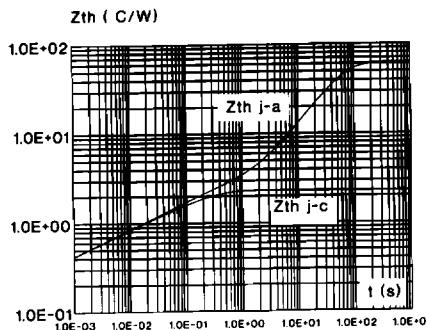


**Fig.4** : RMS on-state current versus case temperature.

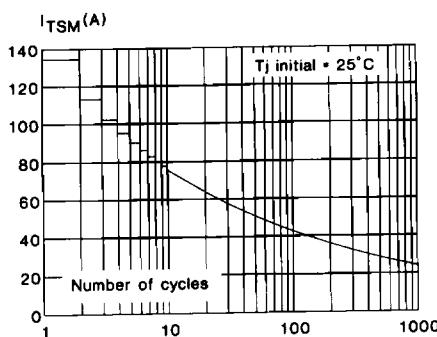


# BTA16 BW/CW / BTB16 BW/CW

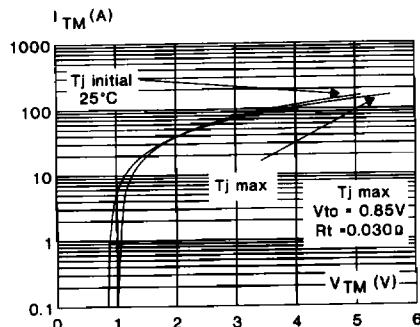
**Fig.5** : Thermal transient impedance junction to case and junction to ambient versus pulse duration.  
(Zth j-c : BTA version only)



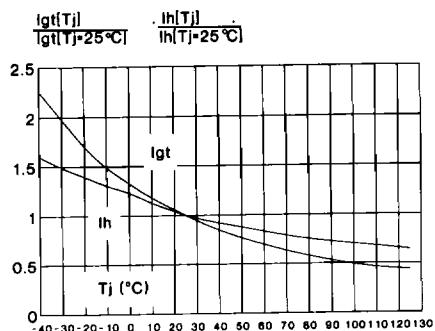
**Fig.7** : Non Repetitive surge peak on-state current versus number of cycles.



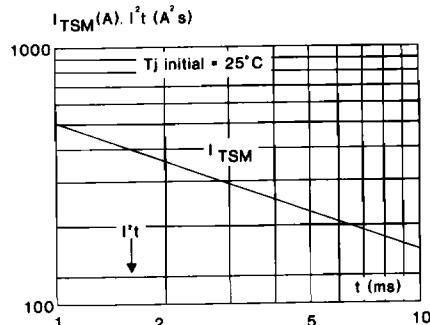
**Fig.9** : On-state characteristics (maximum values).



**Fig.6** : Relative variation of gate trigger current and holding current versus junction temperature.

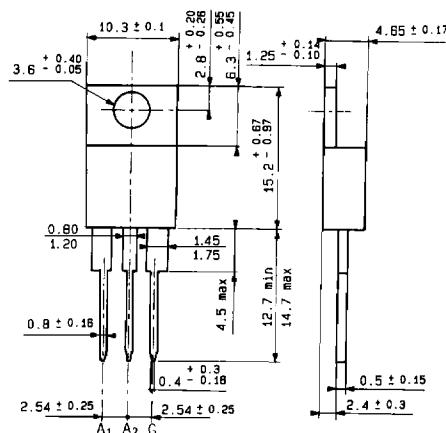


**Fig.8** : Non repetitive surge peak on-state current for a sinusoidal pulse with width :  $t \leq 10\text{ms}$ , and corresponding value of  $I^2t$ .



## PACKAGE MECHANICAL DATA (in millimeters)

TO 220 AB Plastic



Cooling method : by conduction (method C)

Marking : type number

Weight : 2 g

Polarity : N A