



# DVFL2800S Series

## HIGH RELIABILITY HYBRID DC-DC CONVERTERS

### DESCRIPTION

The DVFL series of high reliability DC-DC converters is operable over the full military (-55 °C to +125 °C) temperature range with no power derating. Unique to the DVFL series are robust and effective input and output filters which provide dramatically reduced input and output noise performance when compared to other manufacturers competing devices. Operating at a nominal fixed frequency of 500 kHz, these regulated, isolated units utilize a high speed magnetic feedback design and well controlled undervoltage lockout circuitry to eliminate slow start-up problems. The current sharing function allows a maximum of five units to be connected in parallel to boost the total output power to 5 times. The output voltage is trimmable up to +10% or down -20%.

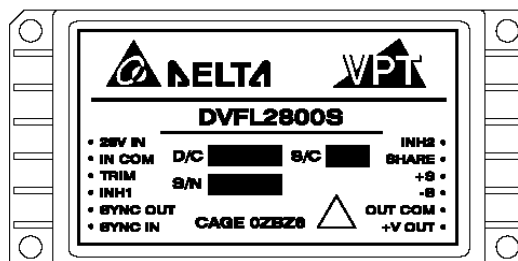
These converters are designed and manufactured in a facility qualified to ISO9001 and certified to MIL-PRF-38534 and MIL-STD-883.

This product may incorporate one or more of the following U.S. patents:

- 5,784,266
- 5,790,389
- 5,963,438
- 5,999,433
- 6,005,780
- 6,084,792
- 6,118,673

### FEATURES

- High Reliability
- Parallel Up to 5 Units With Current Sharing
- Output Voltage Trim Up +10% or Down -20%
- Wide Input Voltage Range: 16 to 40 Volts per MIL-STD-704
- Up to 120 Watts Output Power
- Fault Tolerant Magnetic Feedback Circuit
- NO Use of Optoisolators
- Undervoltage Lockout
- Indefinite Short Circuit Protection
- Current Limit Protection
- Industry Standard Pinout
- Input Transient Voltage: 50 Volts for 1 second
- Precision Seam Welded Hermetic Package
- High Power Density: > 80 W/in<sup>3</sup>
- Custom Versions Available
- Additional Environmental Screening Available
- Meets MIL-STD-461C and MIL-STD-461D EMC Requirements When Used With a DVME28 EMI Filter
- MIL-PRF-38534 Element Evaluated Components
- Space Applications should consider VPT's "S" Series of Radiation Tolerant Power Conversion Devices. Contact VPT for details.



**Figure 1** – DVFL2800S DC-DC Converter  
(Exact marking may differ from that shown)

SPECIFICATIONS ( $T_{CASE} = -55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ ,  $V_{IN} = +28\text{V} \pm 5\%$ , Full Load, Unless Otherwise Specified)

## ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	40 $V_{DC}$	Junction Temperature Rise to Case	+15°C
Input Voltage (Transient, 1 second)	50 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	120 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}\text{C}$ )	41 Watts	Weight (Maximum) (No Pin Extensions)	86 Grams

Parameter	Conditions	DVFL283R3S			DVFL2805S			Units
		Min	Typ	Max	Min	Typ	Max	
<b>STATIC</b>								
INPUT Voltage <sup>4</sup>	Continuous	16	28	40	16	28	40	V
	Transient, 1 sec	-	-	50	-	-	50	V
Current	Inhibited 1	-	1	6	-	1	6	mA
	Inhibited 2	-	40	70	-	40	70	mA
	No Load	-	60	120	-	65	120	mA
Ripple Current	Full Load, 20Hz to 10MHz	-	25	80	-	35	80	mA <sub>p-p</sub>
INH1 Pin Input <sup>4</sup>		0	-	1.5	0	-	1.5	V
INH2 Pin Input <sup>4</sup>		0	-	1	0	-	1	V
INH1 Pin Open Circuit Voltage <sup>4</sup>		10.5	-	15	10.5	-	15	V
INH2 Pin Open Circuit Voltage <sup>4</sup>		4	-	12	4	-	12	V
UVLO Turn On		14.5	-	16	14.5	-	16	V
UVLO Turn Off <sup>4</sup>		13.5	-	15.5	13.5	-	15.5	V
OUTPUT Voltage	$V_{OUT}$ $T_{CASE} = 25^{\circ}\text{C}$	3.267	3.30	3.333	4.95	5.00	5.05	V
	$V_{OUT}$ $T_{CASE} = -55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	3.25	3.30	3.35	4.925	5.00	5.075	V
Power		0	-	66	0	-	100	W
Current	$V_{OUT}$	-	-	20	-	-	20	A
Ripple Voltage	$V_{OUT}$ Full Load, 20Hz to 10MHz	-	15	80	-	15	80	mV <sub>p-p</sub>
Line Regulation	$V_{OUT}$ $V_{IN} = 16\text{V}$ to $40\text{V}$	-	2	20	-	2	20	mV
Load Regulation	$V_{OUT}$ No Load to Full Load	-	2	80	-	2	100	mV
Voltage Trim <sup>4</sup>	$V_{OUT}$ Full Load	-10	-	10	-20	-	10	%
Share Pin Voltage <sup>4</sup>		2	-	4	2	-	4	V
EFFICIENCY		68	72	-	75	78	-	%
LOAD FAULT POWER DISSIPATION	Overload <sup>4</sup>	-	-	50	-	-	50	W
	Short Circuit	-	-	50	-	-	50	W
CAPACITIVE LOAD <sup>4</sup>		-	-	1000	-	-	1000	$\mu\text{F}$
SWITCHING FREQUENCY		425	500	600	425	500	600	kHz
SYNC FREQUENCY RANGE	$V_H - V_L = 5\text{V}$ Duty Cycle = 20% - 80%	500	-	600	500	-	600	kHz
ISOLATION	500 $V_{DC}$	100	-	-	100	-	-	M $\Omega$
MTBF (MIL-HDBK-217F)	AIF @ $T_C = 55^{\circ}\text{C}$	-	400	-	-	400	-	kHrs

- Notes: 1. Dependant on output voltage. 2. Time for output voltage to settle within 1% of its nominal value.  
3. Derate linearly to 0 at 135°C. 4. Verified by qualification testing.

SPECIFICATIONS ( $T_{CASE} = -55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ ,  $V_{IN} = +28\text{V} \pm 5\%$ , Full Load, Unless Otherwise Specified)

## ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	40 $V_{DC}$	Junction Temperature Rise to Case	+15°C
Input Voltage (Transient, 1 second)	50 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	120 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}\text{C}$ )	41 Watts	Weight (Maximum) (No Pin Extensions)	86 Grams

Parameter	Conditions	DVFL283R3S			DVFL2805S			Units	
		Min	Typ	Max	Min	Typ	Max		
<b>DYNAMIC</b>									
Load Step Output Transient	$V_{OUT}$	Half Load to Full Load	-	140	400	-	160	400	mV <sub>PK</sub>
Load Step Recovery <sup>2</sup>			-	260	500	-	260	500	μSec
Line Step Output Transient <sup>4</sup>	$V_{OUT}$	$V_{IN} = 16\text{V}$ to $40\text{V}$	-	300	600	-	300	600	mV <sub>PK</sub>
Line Step Recovery <sup>2,4</sup>			-	300	500	-	300	500	μSec
Turn On Delay	$V_{OUT}$	$V_{IN} = 0\text{V}$ to $28\text{V}$	-	5	20	-	5	20	mSec
Turn On Overshoot			-	0	15	-	0	25	mV <sub>PK</sub>

- Notes:
1. Dependant on output voltage.
  2. Time for output voltage to settle within 1% of its nominal value.
  3. Derate linearly to 0 at 135°C.
  4. Verified by qualification testing.

SPECIFICATIONS ( $T_{CASE} = -55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ ,  $V_{IN} = +28\text{V} \pm 5\%$ , Full Load, Unless Otherwise Specified)

## ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	40 $V_{DC}$	Junction Temperature Rise to Case	+15°C
Input Voltage (Transient, 1 second)	50 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	120 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}\text{C}$ )	41 Watts	Weight (Maximum) (No Pin Extensions)	86 Grams

Parameter	Conditions	DVFL286R3S			DVFL2807S			Units
		Min	Typ	Max	Min	Typ	Max	
<b>STATIC</b>								
INPUT Voltage <sup>4</sup>	Continuous	16	28	40	16	28	40	V
	Transient, 1 sec	-	-	50	-	-	50	V
Current	Inhibited 1	-	1	6	-	1	6	mA
	Inhibited 2	-	40	70	-	40	70	mA
	No Load	-	65	120	-	65	120	mA
Ripple Current	Full Load, 20Hz to 10MHz	-	35	80	-	35	80	mA <sub>p-p</sub>
INH1 Pin Input <sup>4</sup>		0	-	1.5	0	-	1.5	V
INH2 Pin Input <sup>4</sup>		0	-	1	0	-	1	V
INH1 Pin Open Circuit Voltage <sup>4</sup>		10.5	-	15	10.5	-	15	V
INH2 Pin Open Circuit Voltage <sup>4</sup>		4	-	12	4	-	12	V
UVLO Turn On		14.5	-	16	14.5	-	16	V
UVLO Turn Off <sup>4</sup>		13.5	-	15.5	13.5	-	15.5	V
OUTPUT Voltage	$V_{OUT}$ $T_{CASE} = 25^{\circ}\text{C}$	6.237	6.3	6.363	6.93	7	7.07	V
	$V_{OUT}$ $T_{CASE} = -55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	6.205	6.3	6.395	6.895	7	7.105	V
Power		-	-	100	-	-	100	W
Current	$V_{OUT}$	-	-	15.9	-	-	14.3	A
Ripple Voltage	$V_{OUT}$ Full Load, 20Hz to 10MHz	-	15	80	-	15	80	mV <sub>p-p</sub>
Line Regulation	$V_{OUT}$ $V_{IN} = 16\text{V}$ to $40\text{V}$	-	2	20	-	2	20	mV
Load Regulation	$V_{OUT}$ No Load to Full Load	-	2	100	-	2	100	mV
Voltage Trim <sup>4</sup>	$V_{OUT}$ Full Load	-20	-	10	-20	-	10	%
Share Pin Voltage <sup>4</sup>		2	-	4	2	-	4	V
EFFICIENCY		74	79	-	74	80	-	%
LOAD FAULT POWER DISSIPATION	Overload <sup>4</sup>	-	-	50	-	-	50	W
	Short Circuit	-	-	50	-	-	50	W
CAPACITIVE LOAD <sup>4</sup>		-	-	1000	-	-	1000	$\mu\text{F}$
SWITCHING FREQUENCY		425	500	600	425	500	600	kHz
SYNC FREQUENCY RANGE	$V_H - V_L = 5\text{V}$ Duty Cycle = 20% - 80%	500	-	600	500	-	600	kHz
ISOLATION	500 $V_{DC}$	100	-	-	100	-	-	M $\Omega$
MTBF (MIL-HDBK-217F)	AIF @ $T_C = 55^{\circ}\text{C}$	-	400	-	-	400	-	kHrs

- Notes: 1. Dependant on output voltage. 2. Time for output voltage to settle within 1% of its nominal value.  
3. Derate linearly to 0 at 135°C. 4. Verified by qualification testing.

SPECIFICATIONS ( $T_{CASE} = -55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ ,  $V_{IN} = +28\text{V} \pm 5\%$ , Full Load, Unless Otherwise Specified)

### ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	40 $V_{DC}$	Junction Temperature Rise to Case	+15°C
Input Voltage (Transient, 1 second)	50 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	120 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}\text{C}$ )	41 Watts	Weight (Maximum) (No Pin Extensions)	86 Grams

Parameter	Conditions	DVFL286R3S			DVFL2807S			Units	
		Min	Typ	Max	Max	Typ	Max		
<b>DYNAMIC</b>									
Load Step Output Transient	$V_{OUT}$	Half Load to Full Load	-	150	300	-	150	300	mV <sub>PK</sub>
Load Step Recovery <sup>2</sup>			-	200	350		200	350	μSec
Line Step Output Transient <sup>4</sup>	$V_{OUT}$	$V_{IN} = 16\text{V}$ to $40\text{V}$	-	150	300	-	150	300	mV <sub>PK</sub>
Line Step Recovery <sup>2,4</sup>			-	150	300		200	400	μSec
Turn On Delay	$V_{OUT}$	$V_{IN} = 0\text{V}$ to $28\text{V}$	-	5	20	-	5	20	mSec
Turn On Overshoot			-	0	25	-	0	25	mV <sub>PK</sub>

- Notes: 1. Dependant on output voltage. 2. Time for output voltage to settle within 1% of its nominal value.  
 3. Derate linearly to 0 at 135°C. 4. Verified by qualification testing.

SPECIFICATIONS ( $T_{CASE} = -55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ ,  $V_{IN} = +28\text{V} \pm 5\%$ , Full Load, Unless Otherwise Specified)

## ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	40 $V_{DC}$	Junction Temperature Rise to Case	+15°C
Input Voltage (Transient, 1 second)	50 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	120 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}\text{C}$ )	41 Watts	Weight (Maximum) (No Pin Extensions)	86 Grams

Parameter	Conditions	DVFL2808S			DVFL2895S			Units
		Min	Typ	Max	Min	Typ	Max	
<b>STATIC</b>								
INPUT Voltage <sup>4</sup>	Continuous	16	28	40	16	28	40	V
	Transient, 1 sec	-	-	50	-	-	50	V
Current	Inhibited 1	-	1	6	-	1	6	mA
	Inhibited 2	-	40	70	-	40	70	mA
	No Load	-	70	120	-	80	120	mA
Ripple Current	Full Load, 20Hz to 10MHz	-	30	80	-	30	80	mA <sub>p-p</sub>
INH1 Pin Input <sup>4</sup>		0	-	1.5	0	-	1.5	V
INH2 Pin Input <sup>4</sup>		0	-	1	0	-	1	V
INH1 Pin Open Circuit Voltage <sup>4</sup>		10.5	-	15	10.5	-	15	V
INH2 Pin Open Circuit Voltage <sup>4</sup>		4	-	12	4	-	12	V
UVLO Turn On		14.5	-	16	14.5	-	16	V
UVLO Turn Off <sup>4</sup>		13.5	-	15.5	13.5	-	15.5	V
OUTPUT Voltage	$V_{OUT}$ $T_{CASE} = 25^{\circ}\text{C}$	7.92	8	8.08	9.405	9.5	9.595	V
	$V_{OUT}$ $T_{CASE} = -55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	7.88	8	8.12	9.357	9.5	9.643	V
Power		-	-	100	-	-	100	W
Current	$V_{OUT}$	-	-	12.5	-	-	10.5	A
Ripple Voltage	$V_{OUT}$ Full Load, 20Hz to 10MHz	-	15	80	-	25	80	mV <sub>p-p</sub>
Line Regulation	$V_{OUT}$ $V_{IN} = 16\text{V}$ to $40\text{V}$	-	2	20	-	2	20	mV
Load Regulation	$V_{OUT}$ No Load to Full Load	-	2	100	-	2	100	mV
Voltage Trim <sup>4</sup>	$V_{OUT}$ Full Load	-20	-	10	-20	-	10	%
Share Pin Voltage <sup>4</sup>		2	-	4	2	-	4	V
EFFICIENCY		76	82	-	78	83	-	%
LOAD FAULT POWER DISSIPATION	Overload <sup>4</sup>	-	-	50	-	-	50	W
	Short Circuit	-	-	50	-	-	50	W
CAPACITIVE LOAD <sup>4</sup>		-	-	1000	-	-	500	$\mu\text{F}$
SWITCHING FREQUENCY		425	500	600	425	500	600	kHz
SYNC FREQUENCY RANGE	$V_H - V_L = 5\text{V}$ Duty Cycle = 20% - 80%	500	-	600	500	-	600	kHz
ISOLATION	500 $V_{DC}$	100	-	-	100	-	-	M $\Omega$
MTBF (MIL-HDBK-217F)	AIF @ $T_C = 55^{\circ}\text{C}$	-	400	-	-	400	-	kHrs

- Notes: 1. Dependant on output voltage. 2. Time for output voltage to settle within 1% of its nominal value.  
3. Derate linearly to 0 at 135°C. 4. Verified by qualification testing.

SPECIFICATIONS ( $T_{CASE} = -55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ ,  $V_{IN} = +28\text{V} \pm 5\%$ , Full Load, Unless Otherwise Specified)

### ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	40 $V_{DC}$	Junction Temperature Rise to Case	+15°C
Input Voltage (Transient, 1 second)	50 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	120 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}\text{C}$ )	41 Watts	Weight (Maximum) (No Pin Extensions)	86 Grams

Parameter	Conditions	DVFL2808S			DVFL289R5S			Units	
		Min	Typ	Max	Max	Typ	Max		
<b>DYNAMIC</b>									
Load Step Output Transient	$V_{OUT}$	Half Load to Full Load	-	200	350	-	1000	1300	mV <sub>PK</sub>
Load Step Recovery <sup>2</sup>			-	200	350		450	700	μSec
Line Step Output Transient <sup>4</sup>	$V_{OUT}$	$V_{IN} = 16\text{V}$ to $40\text{V}$	-	250	450	-	850	1400	mV <sub>PK</sub>
Line Step Recovery <sup>2,4</sup>			-	250	350		500	700	μSec
Turn On Delay	$V_{OUT}$	$V_{IN} = 0\text{V}$ to $28\text{V}$	-	5	20	-	5	20	mSec
Turn On Overshoot			-	0	25	-	0	25	mV <sub>PK</sub>

- Notes: 1. Dependant on output voltage. 2. Time for output voltage to settle within 1% of its nominal value.  
 3. Derate linearly to 0 at 135°C. 4. Verified by qualification testing.

## SPECIFICATIONS ( $T_{CASE} = -55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$ , $V_{IN} = +28\text{V} \pm 5\%$ , Full Load, Unless Otherwise Specified)

### ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	40 $V_{DC}$	Junction Temperature Rise to Case	+15°C
Input Voltage (Transient, 1 second)	50 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	120 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}\text{C}$ )	41 Watts	Weight (Maximum) (No Pin Extensions)	86 Grams

Parameter	Conditions	DVFL2812S			DVFL2815S			Units
		Min	Typ	Max	Min	Typ	Max	
<b>STATIC</b>								
INPUT Voltage <sup>4</sup>	Continuous	16	28	40	16	28	40	V
	Transient, 1 sec	-	-	50	-	-	50	V
Current	Inhibited 1	-	1	6	-	1	6	mA
	Inhibited 2	-	40	70	-	40	70	mA
	No Load	-	80	120	-	85	120	mA
Ripple Current	Full Load, 20Hz to 10MHz	-	30	80	-	30	80	mA <sub>p-p</sub>
INH1 Pin Input <sup>4</sup>		0	-	1.5	0	-	1.5	V
INH2 Pin Input <sup>4</sup>		0	-	1	0	-	1	V
INH1 Pin Open Circuit Voltage <sup>4</sup>		10.5	-	15	10.5	-	15	V
INH2 Pin Open Circuit Voltage <sup>4</sup>		4	-	12	4	-	12	V
UVLO Turn On		14.5	-	16	14.5	-	16	V
UVLO Turn Off <sup>4</sup>		13.5	-	15.5	13.5	-	15.5	V
OUTPUT Voltage	$V_{OUT}$ $T_{CASE} = 25^{\circ}\text{C}$	11.88	12.00	12.12	14.85	15.00	15.15	V
	$V_{OUT}$ $T_{CASE} = -55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	11.82	12.00	12.18	14.775	15.00	15.225	V
Power		-	-	110	-	-	120	W
Current	$V_{OUT}$	-	-	9.2	-	-	8.0	A
Ripple Voltage	$V_{OUT}$ Full Load, 20Hz to 10MHz	-	25	80	-	25	80	mV <sub>p-p</sub>
Line Regulation	$V_{OUT}$ $V_{IN} = 16\text{V}$ to $40\text{V}$	-	2	20	-	2	20	mV
Load Regulation	$V_{OUT}$ No Load to Full Load	-	2	120	-	2	120	mV
Voltage Trim <sup>4</sup>	$V_{OUT}$ Full Load	-20	-	10	-20	-	10	%
Share Pin Voltage <sup>4</sup>		2	-	4	2	-	4	V
EFFICIENCY		83	86	-	84	86	-	%
LOAD FAULT POWER DISSIPATION	Overload <sup>4</sup>	-	-	50	-	-	50	W
	Short Circuit	-	-	50	-	-	50	W
CAPACITIVE LOAD <sup>4</sup>		-	-	500	-	-	500	$\mu\text{F}$
SWITCHING FREQUENCY		425	500	600	425	500	600	kHz
SYNC FREQUENCY RANGE	$V_H - V_L = 5\text{V}$ Duty Cycle = 20% - 80%	500	-	600	500	-	600	kHz
ISOLATION	500 $V_{DC}$	100	-	-	100	-	-	M $\Omega$
MTBF (MIL-HDBK-217F)	AIF @ $T_C = 55^{\circ}\text{C}$	-	400	-	-	400	-	kHrs

- Notes: 1. Dependant on output voltage. 2. Time for output voltage to settle within 1% of its nominal value.  
3. Derate linearly to 0 at 135°C. 4. Verified by qualification testing.



SPECIFICATIONS ( $T_{CASE} = -55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ ,  $V_{IN} = +28\text{V} \pm 5\%$ , Full Load, Unless Otherwise Specified)

### ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	40 $V_{DC}$	Junction Temperature Rise to Case	+15°C
Input Voltage (Transient, 1 second)	50 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	120 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}\text{C}$ )	41 Watts	Weight (Maximum) (No Pin Extensions)	86 Grams

Parameter	Conditions	DVFL2812S			DVFL2815S			Units	
		Min	Typ	Max	Min	Typ	Max		
<b>DYNAMIC</b>									
Load Step Output Transient	$V_{OUT}$	Half Load to Full Load	-	570	1000	-	640	1000	mV <sub>PK</sub>
Load Step Recovery <sup>2</sup>			-	290	500	-	260	500	μSec
Line Step Output Transient <sup>4</sup>	$V_{OUT}$	$V_{IN} = 16\text{V}$ to $40\text{V}$	-	600	1200	-	600	1200	mV <sub>PK</sub>
Line Step Recovery <sup>2,4</sup>			-	300	500	-	300	500	μSec
Turn On Delay	$V_{OUT}$	$V_{IN} = 0\text{V}$ to $28\text{V}$	-	5	20	-	5	20	mSec
Turn On Overshoot			-	0	50	-	0	50	mV <sub>PK</sub>

- Notes: 1. Dependant on output voltage. 2. Time for output voltage to settle within 1% of its nominal value.  
 3. Derate linearly to 0 at 135°C. 4. Verified by qualification testing.

SPECIFICATIONS ( $T_{CASE} = -55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ ,  $V_{IN} = +28\text{V} \pm 5\%$ , Full Load, Unless Otherwise Specified)

## ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	40 $V_{DC}$	Junction Temperature Rise to Case	+15°C
Input Voltage (Transient, 1 second)	50 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	120 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}\text{C}$ )	41 Watts	Weight (Maximum) (No Pin Extensions)	86 Grams

Parameter	Conditions	DVFL2818S			DVFL2828S			Units
		Min	Typ	Max	Min	Typ	Max	
<b>STATIC</b>								
INPUT Voltage <sup>4</sup>	Continuous	16	28	40	16	28	40	V
	Transient, 1 sec	-	-	50	-	-	50	V
Current	Inhibited 1	-	1	6	-	1	6	mA
	Inhibited 2	-	40	70	-	40	70	mA
	No Load	-	90	120	-	110	160	mA
Ripple Current	Full Load, 20Hz to 10MHz	-	30	80	-	35	80	mA <sub>p-p</sub>
INH1 Pin Input <sup>4</sup>		0	-	1.5	0	-	1.5	V
INH2 Pin Input <sup>4</sup>		0	-	1	0	-	1	V
INH1 Pin Open Circuit Voltage <sup>4</sup>		10.5	-	15	10.5	-	15	V
INH2 Pin Open Circuit Voltage <sup>4</sup>		4	-	12	4	-	12	V
UVLO Turn On		14.5	-	16	14.5	-	16	V
UVLO Turn Off <sup>4</sup>		13.5	-	15.5	13.5	-	15.5	V
OUTPUT Voltage	$V_{OUT}$ $T_{CASE} = 25^{\circ}\text{C}$	17.82	18	18.18	27.72	28	28.28	V
	$V_{OUT}$ $T_{CASE} = -55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	17.73	18	18.27	27.58	28	28.42	V
Power		-	-	120	-	-	115	W
Current	$V_{OUT}$	-	-	6.67	-	-	4.1	A
Ripple Voltage	$V_{OUT}$ Full Load, 20Hz to 10MHz	-	30	80	-	50	120	mV <sub>p-p</sub>
Line Regulation	$V_{OUT}$ $V_{IN} = 16\text{V}$ to $40\text{V}$	-	2	20	-	20	80	mV
Load Regulation	$V_{OUT}$ No Load to Full Load	-	2	120	-	25	100	mV
Voltage Trim <sup>4</sup>	$V_{OUT}$ Full Load	-20	-	10	-20	-	10	%
Share Pin Voltage <sup>4</sup>		2	-	4	2	-	4	V
EFFICIENCY		80	85	-	81	85	-	%
LOAD FAULT POWER DISSIPATION	Overload <sup>4</sup>	-	-	50	-	-	50	W
	Short Circuit	-	-	50	-	-	50	W
CAPACITIVE LOAD <sup>4</sup>		-	-	500	-	-	500	$\mu\text{F}$
SWITCHING FREQUENCY		425	500	600	425	500	600	kHz
SYNC FREQUENCY RANGE	$V_H - V_L = 5\text{V}$ Duty Cycle = 20% - 80%	500	-	600	500	-	600	kHz
ISOLATION	500 $V_{DC}$	100	-	-	100	-	-	M $\Omega$
MTBF (MIL-HDBK-217F)	AIF @ $T_C = 55^{\circ}\text{C}$	-	400	-	-	400	-	kHrs

- Notes: 1. Dependant on output voltage. 2. Time for output voltage to settle within 1% of its nominal value.  
3. Derate linearly to 0 at 135°C. 4. Verified by qualification testing.

SPECIFICATIONS ( $T_{CASE} = -55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ ,  $V_{IN} = +28\text{V} \pm 5\%$ , Full Load, Unless Otherwise Specified)

### ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	40 $V_{DC}$	Junction Temperature Rise to Case	+15°C
Input Voltage (Transient, 1 second)	50 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	120 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}\text{C}$ )	41 Watts	Weight (Maximum) (No Pin Extensions)	86 Grams

Parameter	Conditions	DVFL2818S			DVFL2828S			Units	
		Min	Typ	Max	Min	Typ	Max		
<b>DYNAMIC</b>									
Load Step Output Transient	$V_{OUT}$	Half Load to Full Load	-	1000	1300	-	1000	1400	mV <sub>PK</sub>
Load Step Recovery <sup>2</sup>			-	350	600		200	350	μSec
Line Step Output Transient <sup>4</sup>	$V_{OUT}$	$V_{IN} = 16\text{V}$ to $40\text{V}$	-	1000	1900	-	1000	2200	mV <sub>PK</sub>
Line Step Recovery <sup>2,4</sup>			-	450	700		200	400	μSec
Turn On Delay	$V_{OUT}$	$V_{IN} = 0\text{V}$ to $28\text{V}$	-	5	20	-	5	20	mSec
Turn On Overshoot			-	0	50	-	0	50	mV <sub>PK</sub>

- Notes: 1. Dependant on output voltage. 2. Time for output voltage to settle within 1% of its nominal value.  
 3. Derate linearly to 0 at 135°C. 4. Verified by qualification testing.

BLOCK DIAGRAM

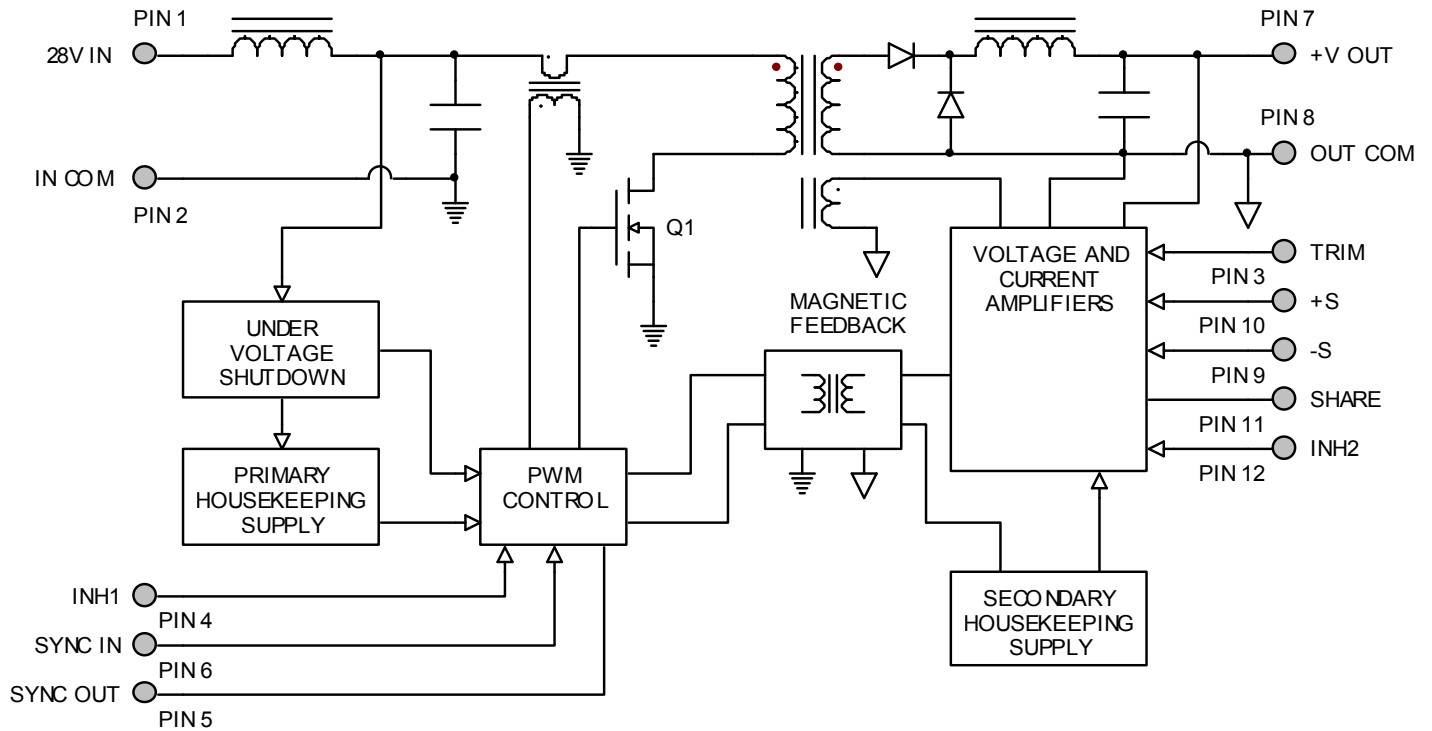


Figure 2

CONNECTION DIAGRAM

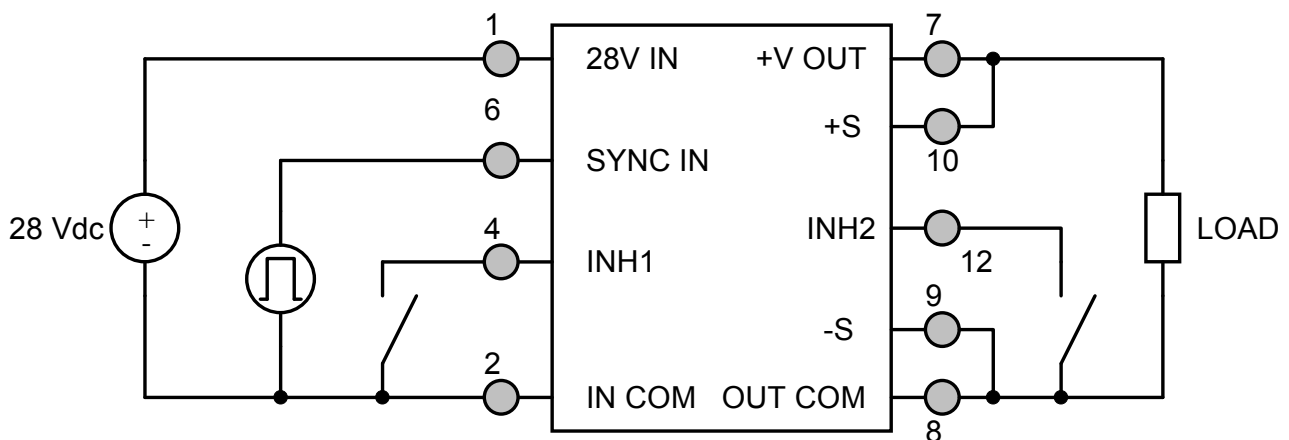
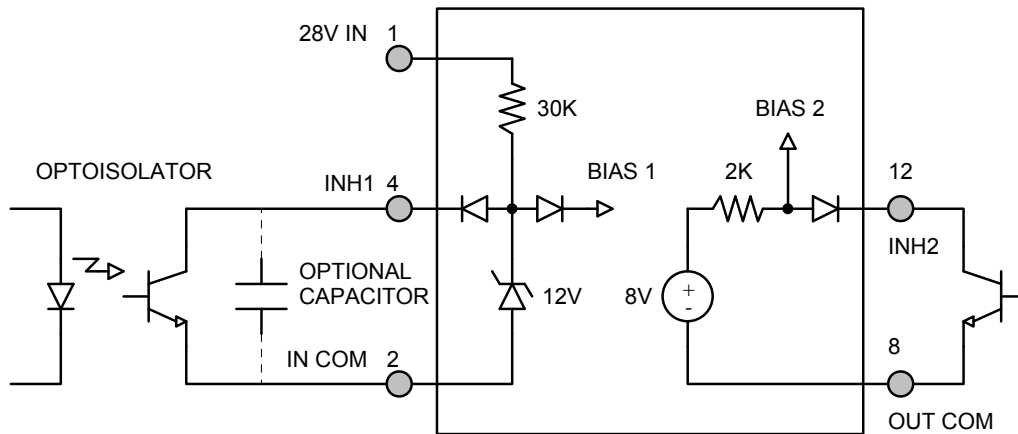


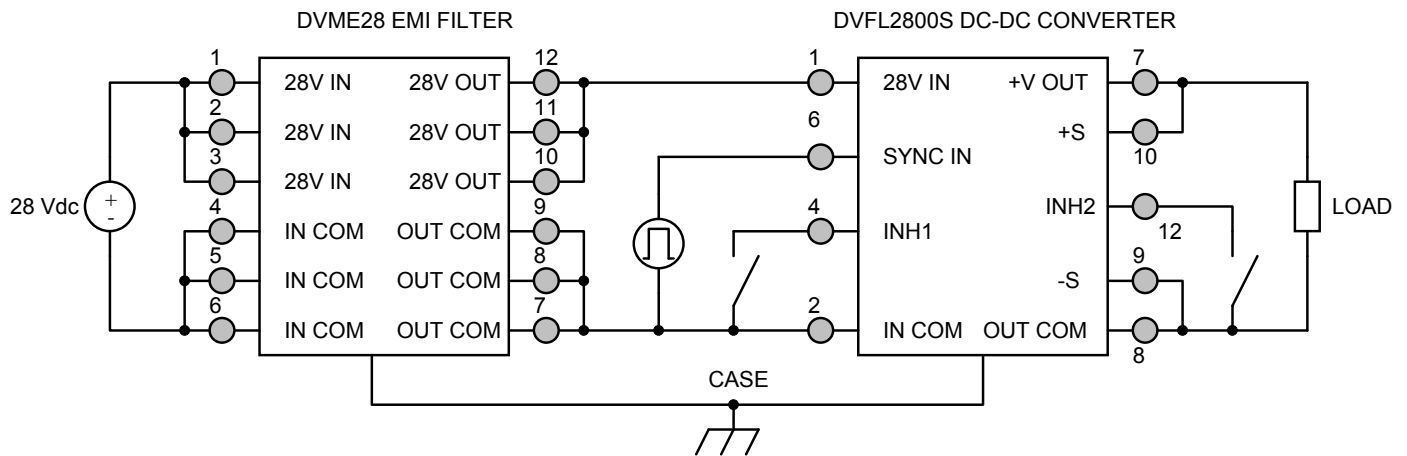
Figure 3

INHIBIT DRIVE CONNECTION DIAGRAM



**Figure 4** – Isolated Inhibit Drive and Internal Equivalent Circuit  
(Shown with optional capacitor for turn-on delay)

EMI FILTER HOOKUP DIAGRAM



**Figure 5** – Converter with EMI Filter

PARALLEL CONNECTION DIAGRAMS

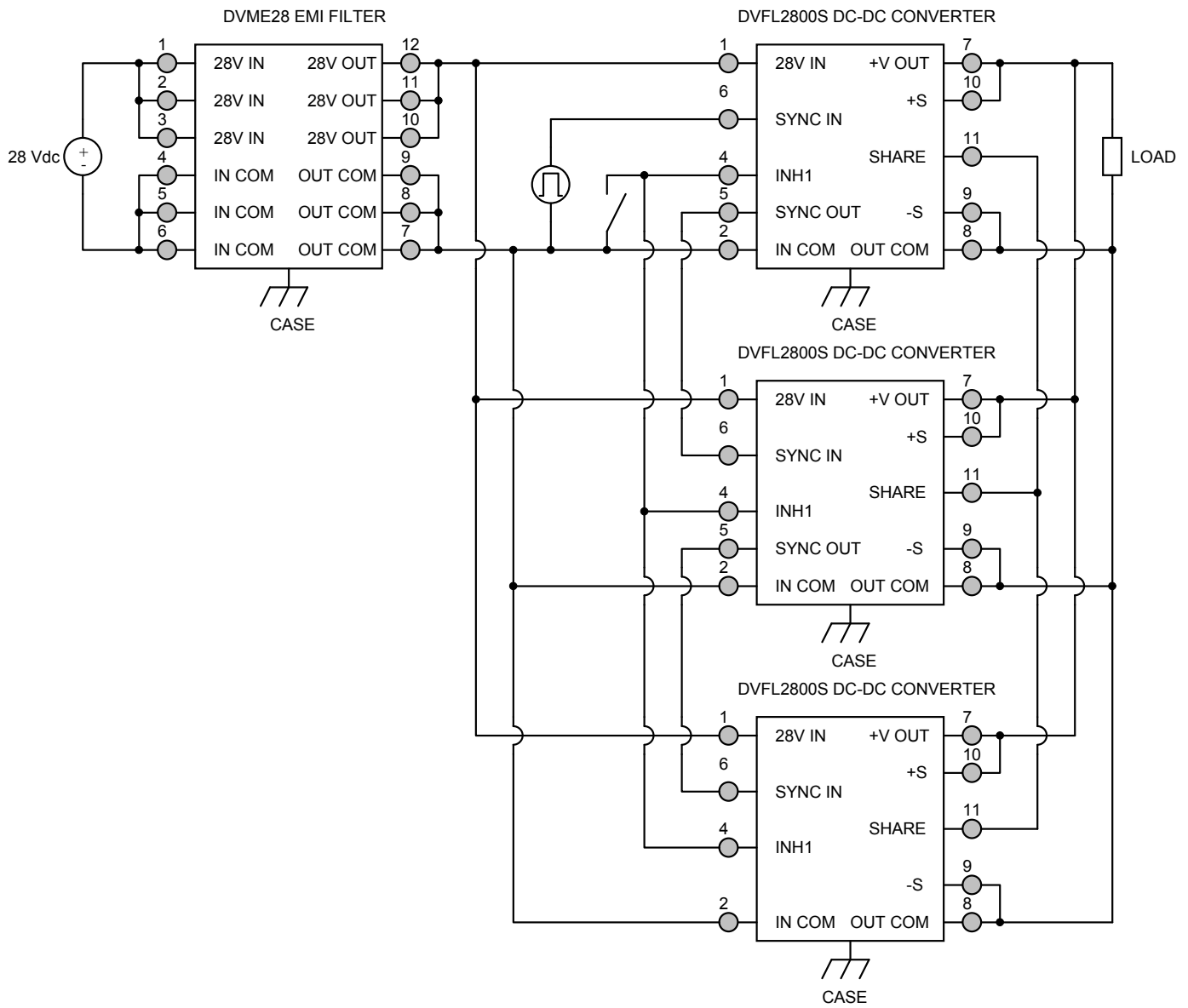
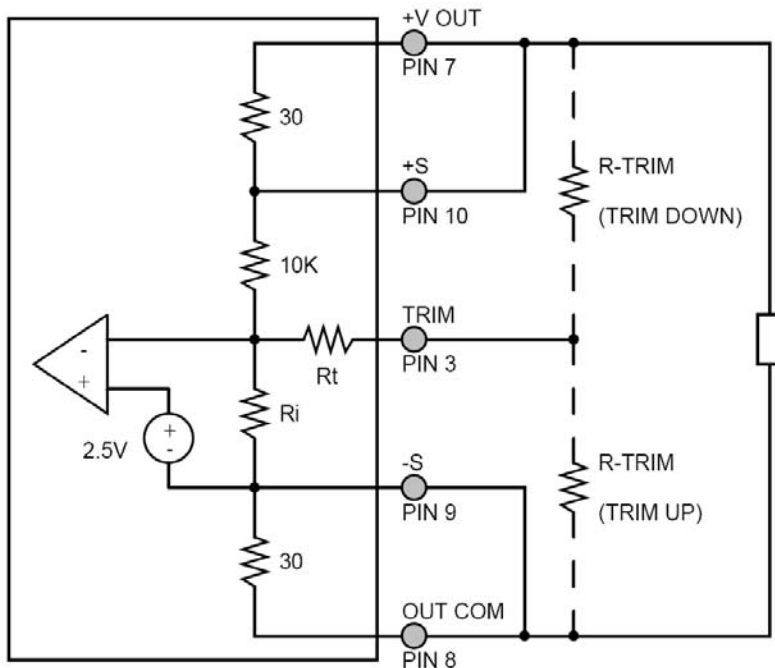


Figure 6 – Current Sharing Parallel Connection for Multiple Converters

OUTPUT VOLTAGE TRIM



The output voltage can be trimmed down by connecting a resistor between the TRIM pin (PIN 3) and the +V OUT pin (PIN 7), or can be trimmed up by connecting a resistor between the TRIM pin (PIN 3) and the OUT COM pin (PIN 8). The maximum trim range is +10% up and -20% down. The appropriate resistor values versus the output voltage are given in the trim table below.

Figure 7 – Output Voltage Trim

DVFL283R3S		DVFL2805S		DVFL286R3S		DVFL2807S		DVFL2808S	
+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)
3.60	68.3k	5.5	35.0k	6.9	25.8k	7.70	20.8k	8.80	16.3k
3.55	85.0k	5.4	47.5k	6.8	33.8k	7.55	30.6k	8.65	23.6k
3.50	110k	5.3	68.3k	6.7	45.7k	7.40	47.7k	8.50	35.2k
3.45	152k	5.2	110k	6.6	65.1k	7.25	85.4k	8.35	56.7k
3.40	235k	5.1	235k	6.5	103k	7.10	236k	8.20	110k
3.35	485k	5.0	-	6.4	208k	7.00	-	8.05	488k
3.30	-	4.9	225k	6.3	-	6.85	276k	8.00	-
3.25	135k	4.8	100k	6.2	406k	6.70	125k	7.90	526k
3.20	55.0k	4.7	58.3k	6.1	177k	6.55	75.2k	7.75	196k
3.15	28.3k	4.6	37.5k	6.0	107k	6.40	50.2k	7.60	113k
3.10	15.0k	4.5	25.0k	5.9	72.7k	6.25	35.1k	7.45	75.3k
3.05	7.0k	4.4	16.7k	5.8	52.6k	6.10	25.1k	7.30	53.8k
3.00	1.7k	4.3	10.7k	5.7	39.4k	5.95	18.0k	7.15	39.9k
		4.2	6.3k	5.6	30.1k	5.80	12.6k	7.00	30.1k
		4.1	2.8k	5.5	23.1k	5.65	8.4k	6.85	22.9k
		4.0	0	5.4	17.7k			6.70	17.4k
				5.3	13.3k			6.55	13.0k
				5.2	9.82k			6.40	9.4k
				5.1	6.89k				

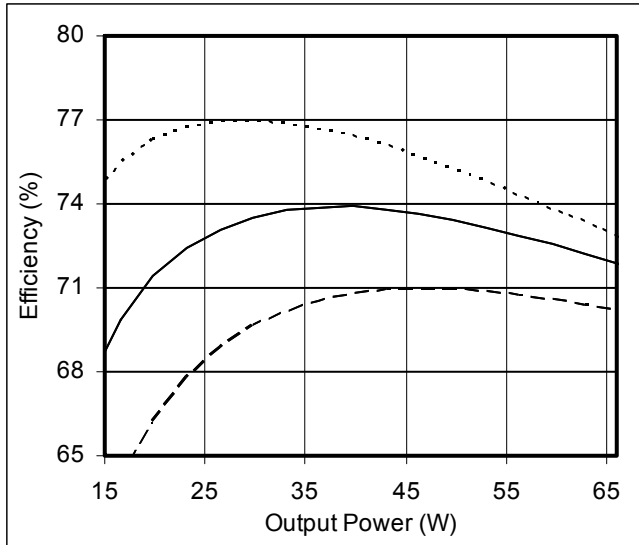
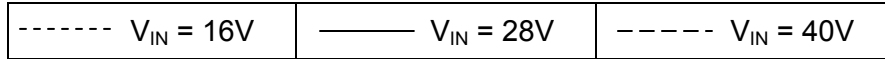
(continued on next page)

## OUTPUT VOLTAGE TRIM

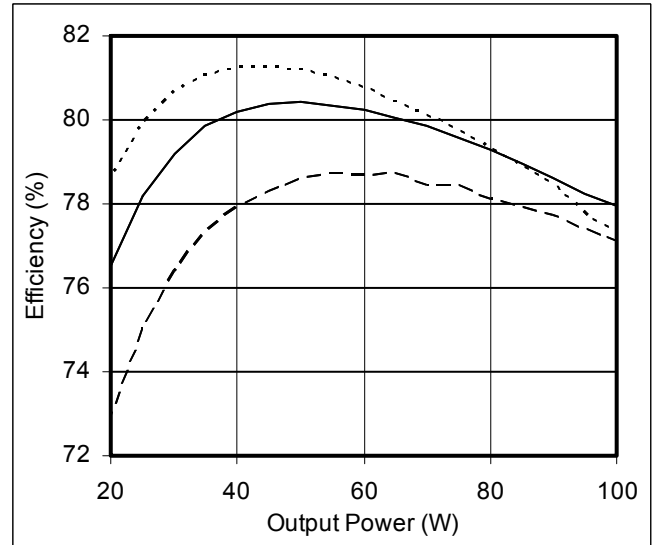
DVFL289R5S		DVFL2812S		DVFL2815S		DVFL2818S		DVFL2828S	
+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)
10.45	11.4k	13.2	5.8k	16.50	1.7k	19.8	3.99k	30.8	0.52k
10.3	16.3k	13.0	10.0k	16.25	5.0k	19.5	6.80k	30.4	1.93k
10.1	26.8k	12.8	16.2k	16.00	10.0k	19.2	11.0k	30.0	3.89k
9.9	47.7k	12.6	26.6k	15.75	18.3k	18.9	18.1k	29.6	6.79k
9.7	110k	12.4	47.3k	15.50	35.0k	18.6	32.3k	29.2	11.5k
9.5	-	12.2	109k	15.25	85.0k	18.3	75.6k	28.8	20.6k
9.4	677k	12.0	-	15.00	-	18.0	-	28.4	45.3k
9.2	209k	11.8	454k	14.75	475k	17.7	486k	28.0	-
9.0	115k	11.6	213k	14.50	225k	17.4	236k	27.6	747k
8.8	75.3k	11.4	134k	14.25	142k	17.1	151k	27.2	329k
8.6	53.0k	11.2	94.0k	14.00	100k	16.8	109k	26.8	206k
8.4	38.8k	11.0	70.1k	13.75	75.0k	16.5	83.2k	26.4	148k
8.2	29.0k	10.8	54.3k	13.50	58.3k	16.2	66.0k	26.0	113k
8.0	21.8k	10.6	42.9k	13.25	46.4k	15.9	53.8k	25.6	90.8k
7.8	16.3k	10.4	34.4k	13.00	37.5k	15.6	44.6k	25.2	74.9k
7.6	11.9k	10.2	27.8k	12.75	30.6k	15.3	37.4k	24.8	63.0k
		10.0	22.5k	12.50	25.0k	15.0	31.7k	24.4	53.8k
		9.8	18.2k	12.25	20.5k	14.7	27.0k	24.0	46.5k
		9.6	14.6k	12.00	16.7k	14.4	23.1k	23.6	40.5k
								23.2	35.5k
								22.8	31.4k
								22.4	27.8k



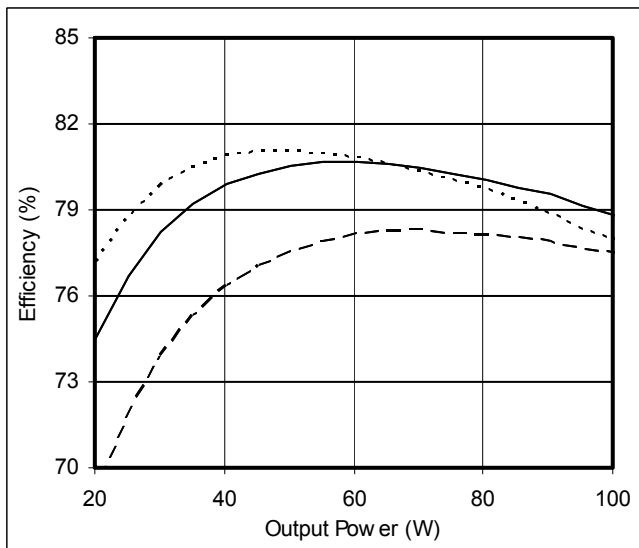
EFFICIENCY PERFORMANCE CURVES ( $T_{CASE} = 25^{\circ}C$ , Full Load, Unless Otherwise Specified)



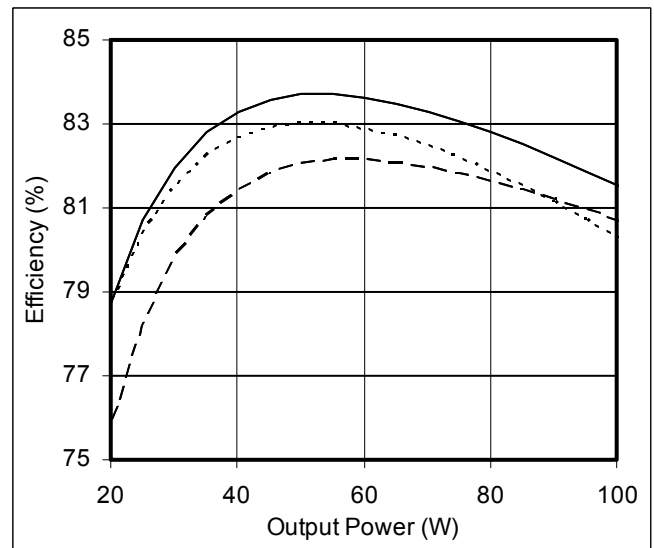
**Figure 8 – DVFL283R3S**  
Efficiency (%) vs. Output Power (W)



**Figure 9 – DVFL2805S**  
Efficiency (%) vs. Output Power (W)



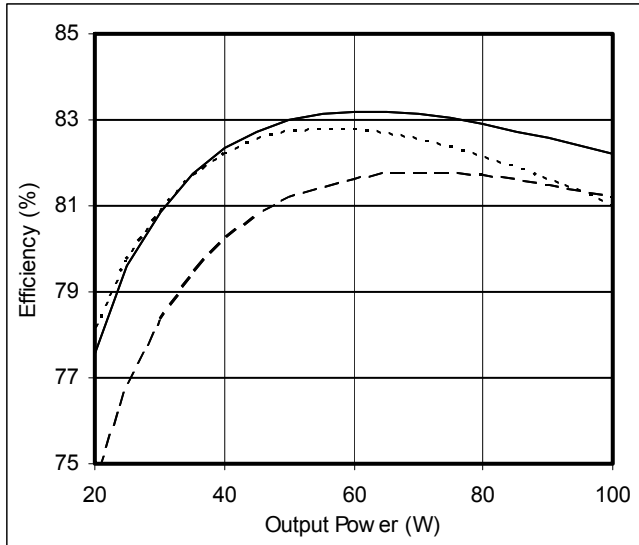
**Figure 10 – DVFL286R3S**  
Efficiency (%) vs. Output Power (W)



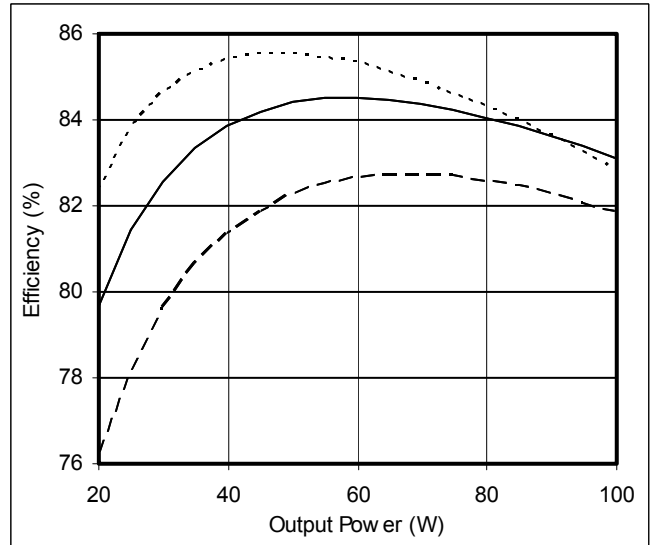
**Figure 11 – DVFL2807S**  
Efficiency (%) vs. Output Power (W)

EFFICIENCY PERFORMANCE CURVES ( $T_{CASE} = 25^{\circ}C$ , Full Load, Unless Otherwise Specified)

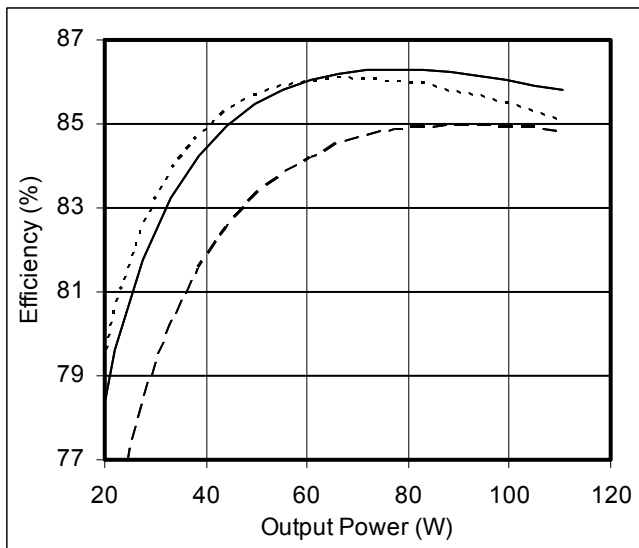
-----  $V_{IN} = 16V$       ———  $V_{IN} = 28V$       - - - -  $V_{IN} = 40V$



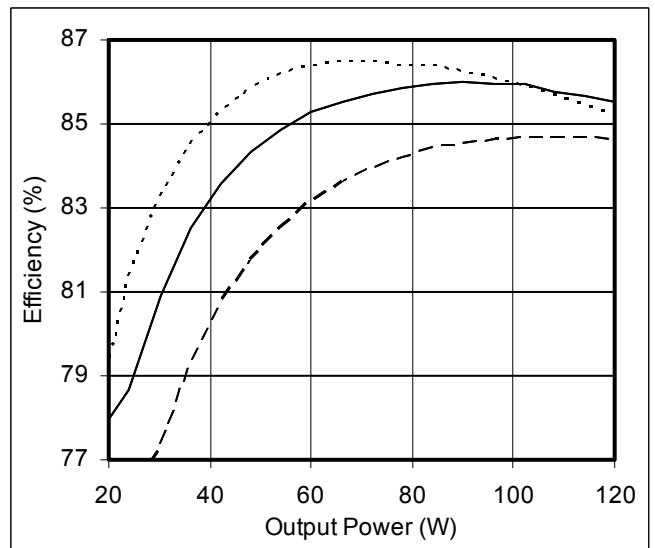
**Figure 12 – DVFL2808S**  
Efficiency (%) vs. Output Power (W)



**Figure 13 – DVFL289R5S**  
Efficiency (%) vs. Output Power (W)

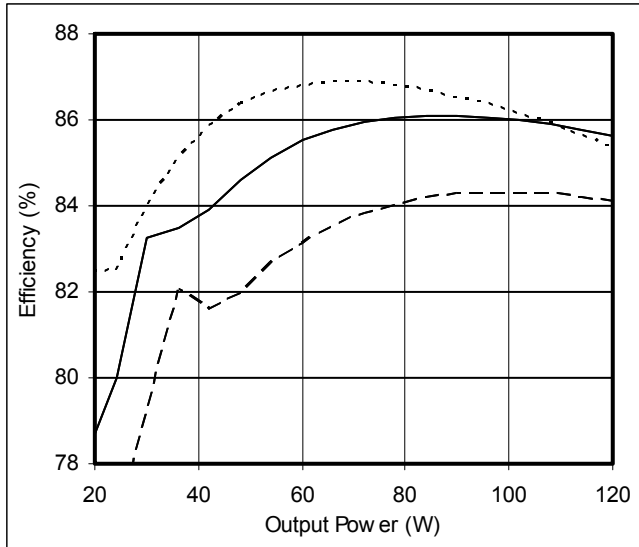


**Figure 14 – DVFL2812S**  
Efficiency (%) vs. Output Power (W)

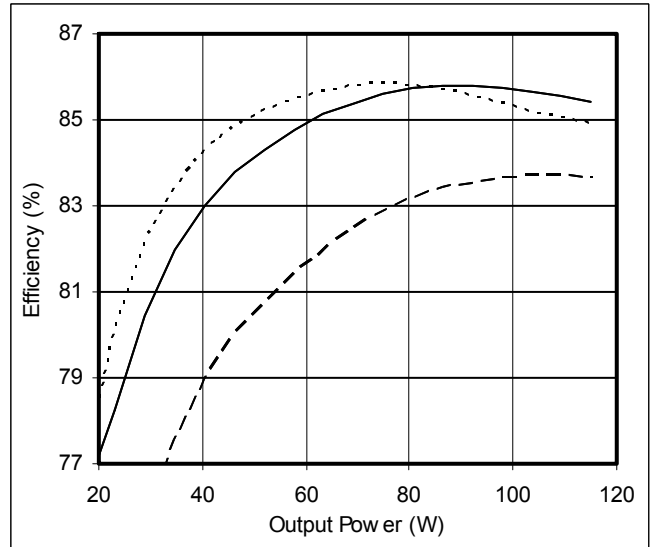


**Figure 15 – DVFL2815S**  
Efficiency (%) vs. Output Power (W)

EFFICIENCY PERFORMANCE CURVES ( $T_{CASE} = 25^{\circ}C$ , Full Load, Unless Otherwise Specified)



**Figure 16 – DVFL2818S**  
Efficiency (%) vs. Output Power (W)



**Figure 17 – DVFL2828S**  
Efficiency (%) vs. Output Power (W)

EMI PERFORMANCE CURVES

( $T_{CASE} = 25^{\circ}C$ ,  $V_{IN} = +28V \pm 5\%$ , Full Load, Unless Otherwise Specified)

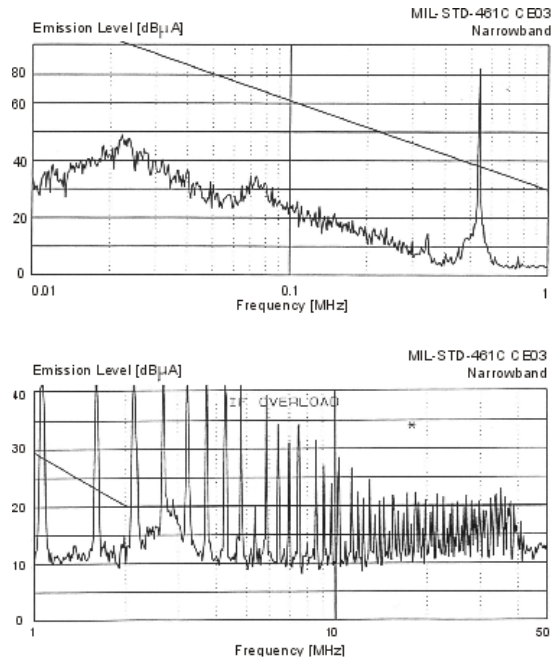


Figure 18 – DVFL2800S without EMI Filter

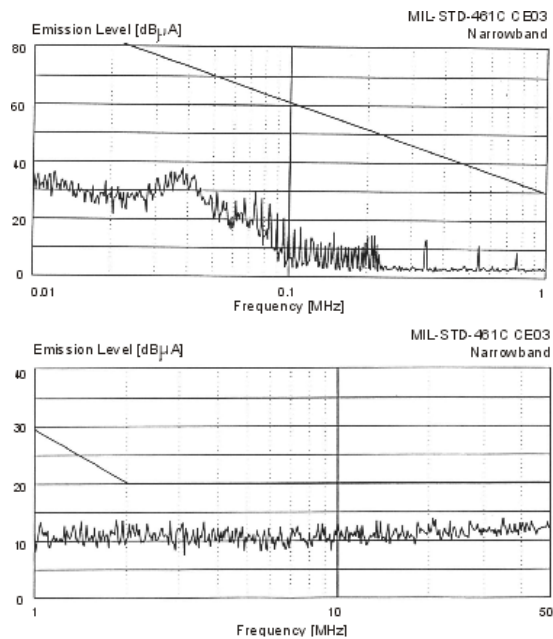
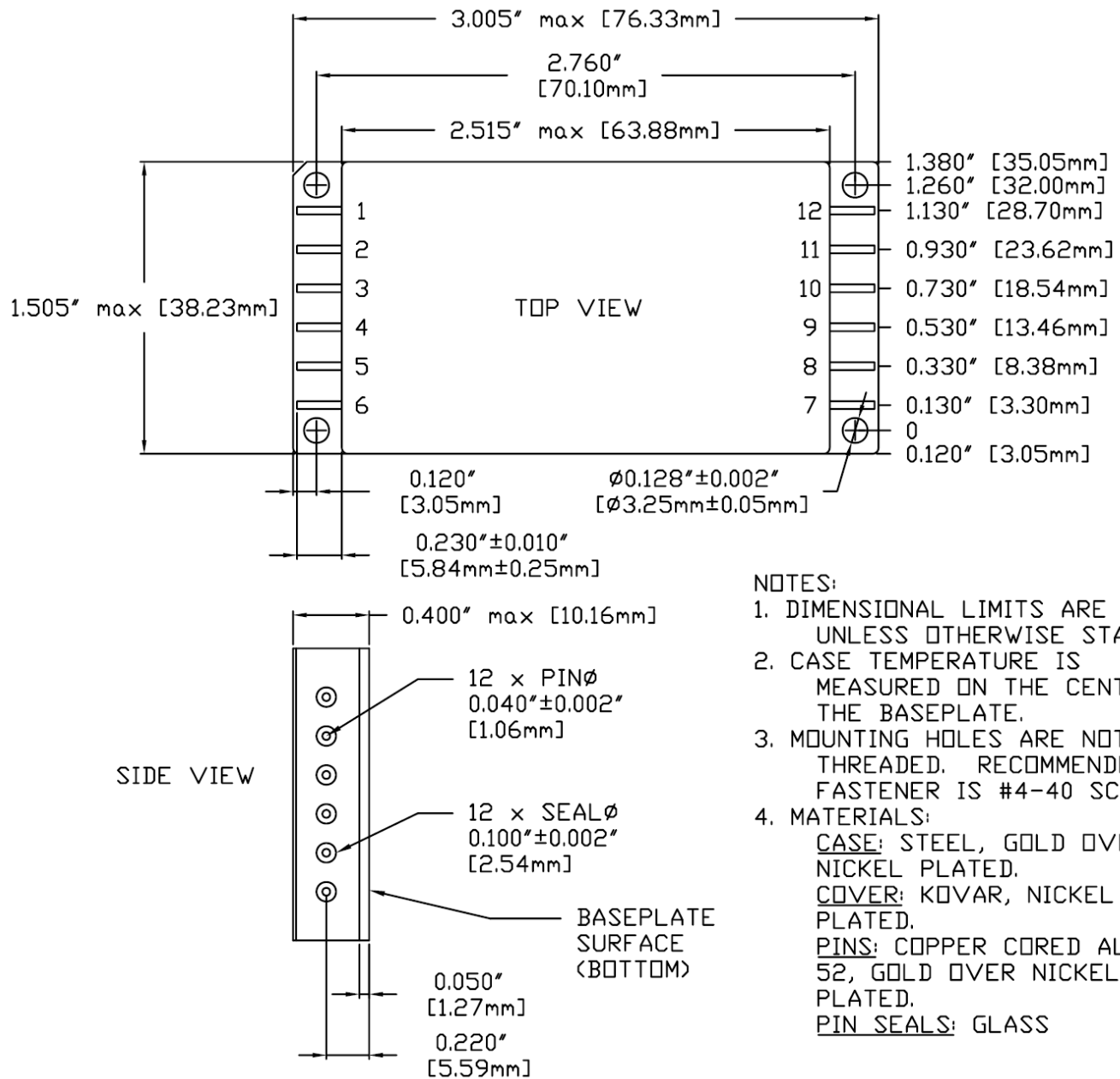


Figure 19 – DVFL2800S with EMI Filter

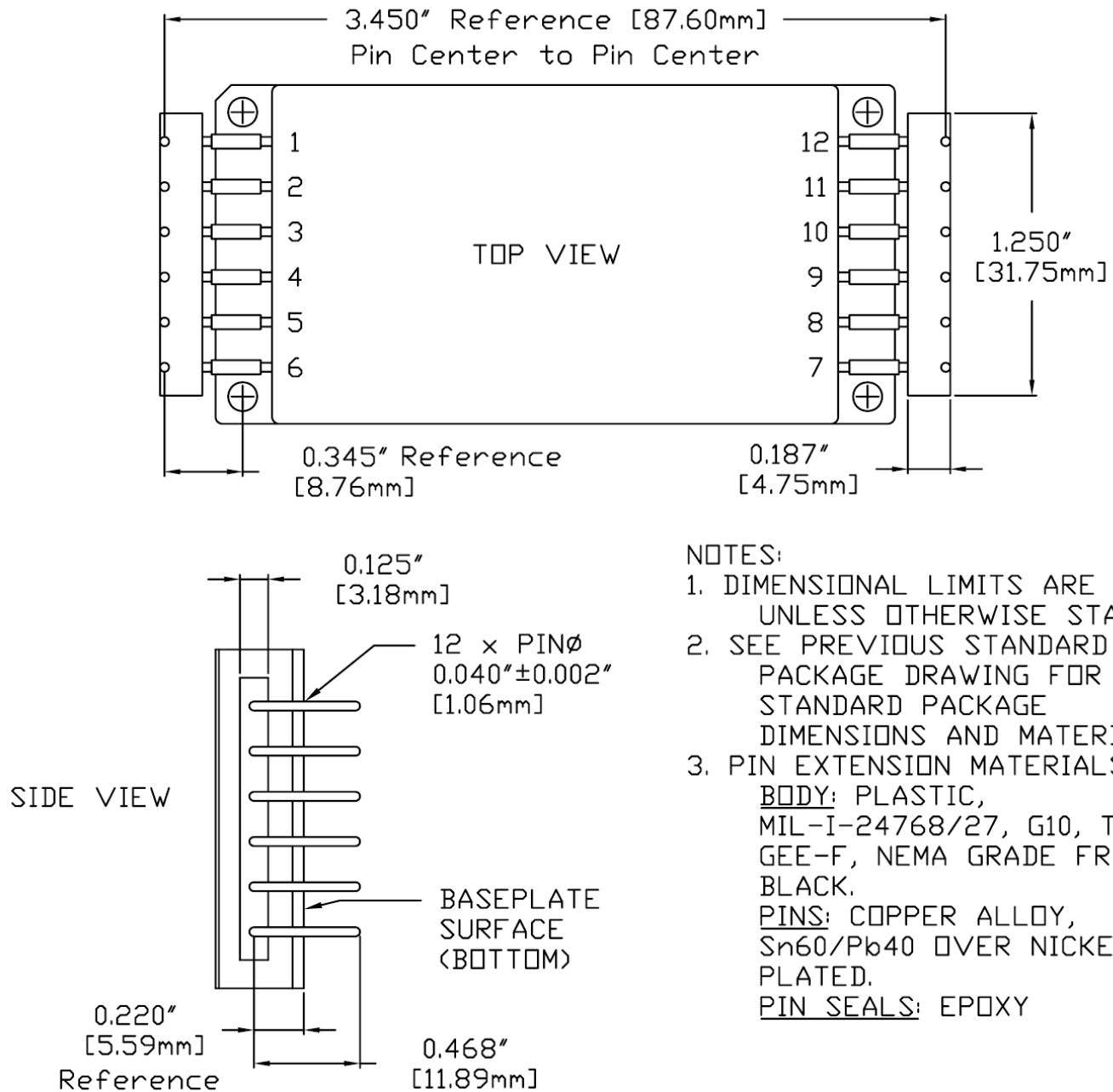
PACKAGE SPECIFICATIONS



PIN	FUNCTION	PIN	FUNCTION	PIN	FUNCTION	PIN	FUNCTION
1	28V IN	4	INH1	7	+V OUT	10	+S
2	IN COM	5	SYNC OUT	8	OUT COM	11	SHARE
3	TRIM	6	SYNC IN	9	-S	12	INH2

Figure 20 – Package and Pinout

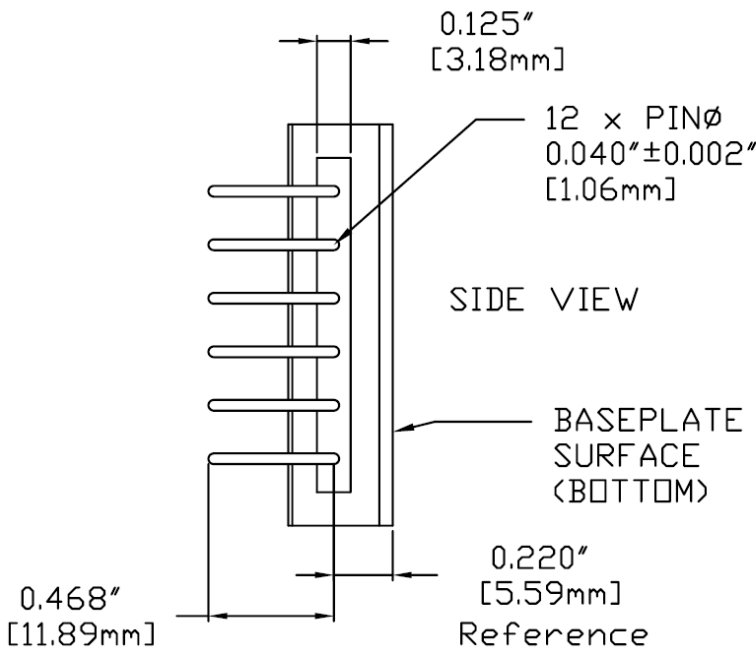
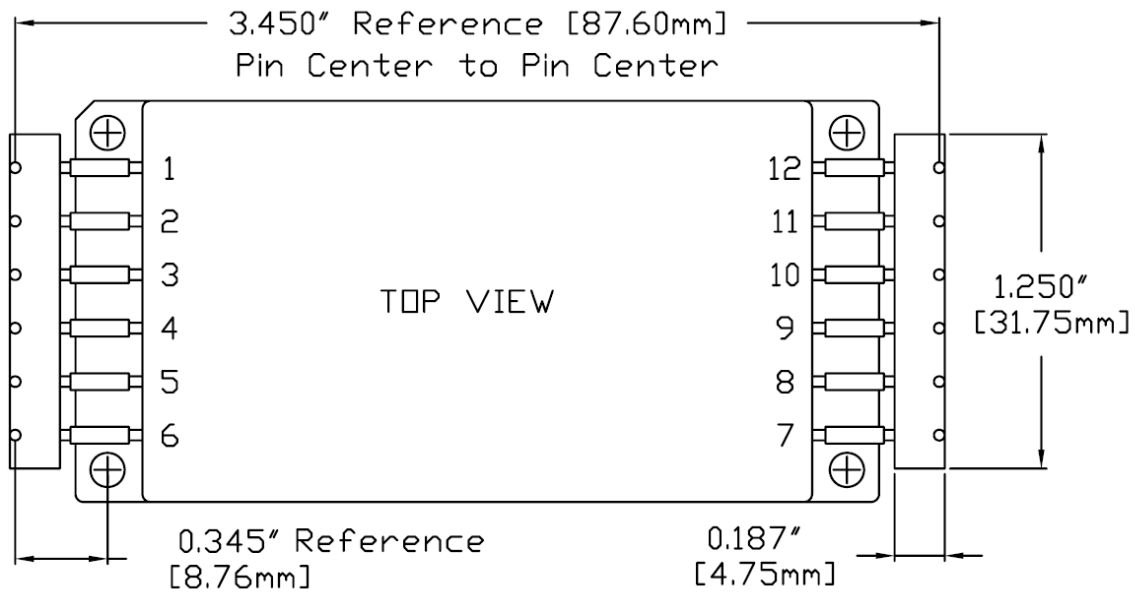
PACKAGE SPECIFICATIONS (DOWN-LEADED)



PIN	FUNCTION	PIN	FUNCTION	PIN	FUNCTION	PIN	FUNCTION
1	28V IN	4	INH1	7	+V OUT	10	+S
2	IN COM	5	SYNC OUT	8	OUT COM	11	SHARE
3	TRIM	6	SYNC IN	9	-S	12	INH2

Figure 21 – Package and Pinout (With Down-Leaded Pin Extensions Added)

PACKAGE SPECIFICATIONS (UP-LEADED)



NOTES:

1. DIMENSIONAL LIMITS ARE ±0.005" UNLESS OTHERWISE STATED.
2. SEE PREVIOUS STANDARD PACKAGE DRAWING FOR STANDARD PACKAGE DIMENSIONS AND MATERIALS.
3. PIN EXTENSION MATERIALS:  
BODY: PLASTIC, MIL-I-24768/27, G10, TYPE GEE-F, NEMA GRADE FR4, BLACK.  
PINS: COPPER ALLOY, Sn60/Pb40 OVER NICKEL PLATED.  
PIN SEALS: EPOXY

PIN	FUNCTION	PIN	FUNCTION	PIN	FUNCTION	PIN	FUNCTION
1	28V IN	4	INH1	7	+V OUT	10	+S
2	IN COM	5	SYNC OUT	8	OUT COM	11	SHARE
3	TRIM	6	SYNC IN	9	-S	12	INH2

Figure 22 – Package and Pinout (With Up-Leaded Pin Extensions Added)

## PACKAGE PIN DESCRIPTION

Pin	Function	Description
1	28V IN	Positive Input Voltage Connection
2	IN COM	Input Common Connection
3	TRIM	Trim Output Voltage to +10%, -20% of Nominal Value
4	INH1	Logic Low = Disabled Output. Connecting the inhibit(1) pin to input common causes converter shutdown. Logic High = Enabled Output. Unconnected or open collector TTL.
5	SYNC OUT	Output Synchronization Signal
6	SYNC IN	Input Synchronization Signal
7	+V OUT	Positive Output Voltage Connection
8	OUT COM	Output Common Connection
9	-S	Return Sense
10	+S	Positive Sense
11	SHARE	Current Share
12	INH2	Logic Low = Disabled Output. Connecting the inhibit(2) pin to output common causes converter shutdown. Logic High = Enabled Output. Unconnected or open collector TTL.



## ENVIRONMENTAL SCREENING (100% Tested Per MIL-STD-883 as referenced to MIL-PRF-38534)

Screening	MIL-STD-883	Standard (No Suffix)	Extended /ES	HB /HB	Class H /H	Class K /K
Non-Destructive Bond Pull	Method 2023	•	•	•	•	•
Internal Visual	Method 2017, 2032 Internal Procedure	•	•	•	•	•
Temperature Cycling	Method 1010, Condition C Method 1010, -55°C to 125°C		•	•	•	•
Constant Acceleration	Method 2001, 3000g, Y1 Direction Method 2001, 500g, Y1 Direction		•	•	•	•
PIND	Method 2020, Condition A <sup>2</sup>					•
Pre Burn-In Electrical	100% at 25°C					•
Burn-In	Method 1015, 320 hours at +125°C Method 1015, 160 hours at +125°C 96 hours at +125°C 24 hours at +125°C	•	•	•	•	•
Final Electrical	MIL-PRF-38534, Group A <sup>1</sup> 100% at 25°C	•	•	•	•	•
Hermeticity	Method 1014, Fine Leak, Condition A Method 1014, Gross Leak, Condition C Dip (1 x 10 <sup>-3</sup> )	•	•	•	•	•
Radiography	Method 2012 <sup>3</sup>					•
External Visual	Method 2009	•	•	•	•	•

- Notes:
1. 100% R&R testing at -55°C, +25°C, and +125°C with all test data included in product shipment.
  2. PIND test Certificate of Compliance included in product shipment.
  3. Radiographic test Certificate of Compliance and film(s) included in product shipment.

## ORDERING INFORMATION

<b>DVFL</b>	<b>28</b>	<b>05</b>	<b>S</b>	<b>DL</b>	<b>/HB</b>	-	<b>XXX</b>
1	2	3	4	5	6		7

(1)	(2)		(3)		(4)	
Product Series	Nominal Input Voltage		Output Voltage		Number of Outputs	
<b>DVFL</b>	<b>28</b>	28 Volts	<b>3R3</b> <b>05</b> <b>6R3</b> <b>07</b> <b>08</b> <b>9R5</b> <b>12</b> <b>15</b> <b>18</b> <b>28</b>	3.3 Volts 5 Volts 6.3 Volts 7 Volts 8 Volts 9.5 Volts 12 Volts 15 Volts 18 volts 28 volts	<b>S</b>	Single

(5)		(6)		(7)
Package Option		Screening Code <sup>1,2</sup>		Additional Screening Code
<b>None</b> <b>DL</b> <b>UL</b>	Standard Down-Lead Up-Lead	<b>None</b> <b>/ES</b> <b>/HB</b> <b>/H</b> <b>/K</b>	Standard Extended HB Class H Class K	Contact Sales

- Notes:
1. Contact the VPT Inc. Sales Department for availability of Class H (/H) or Class K (/K) qualified products.
  2. VPT Inc. reserves the right to ship higher screened or SMD products to meet lower screened orders at our sole discretion unless specifically forbidden by customer contract.

Please contact your sales representative or the VPT Inc. Sales Department for more information concerning additional environmental screening and testing, different input voltage, output voltage, power requirement, source inspection, and/or special element evaluation for space or other higher quality applications.

## SMD (STANDARD MICROCIRCUIT DRAWING) NUMBERS

Standard Microcircuit Drawing (SMD)	DVFL2800S Series Similar Part Number
5962-0724301HXC	DVFL283R3S/H
5962-0724302HXC	DVFL2805S/H
TBD	DVFL286R3S/H
5962-0724305HXC	DVFL2807S/H
TBD	DVFL2808S/H
TBD	DVFL289R5S/H
5962-0724303HXC	DVFL2812S/H
5962-0724304HXC	DVFL2815S/H
TBD	DVFL2818S/H
TBD	DVFL2828S/H

Do not use the DVFL2800S Series similar part number for SMD product acquisition. It is listed for reference only. For exact specifications for the SMD product, refer to the SMD drawing. SMD's can be downloaded from the DSCC website at <http://www.dscclia.mil/programs/smcr/>. The SMD number listed above is for MIL-PRF-38534 Class H screening, standard gold plated lead finish, and no RHA (Radiation Hardness Assurance) level. Please reference the SMD for other screening levels, lead finishes, and radiation levels. All SMD products are marked with a "Q" on the cover as specified by the QML certification mark requirement of MIL-PRF-38534.

## CONTACT INFORMATION

To request a quotation or place orders please contact your sales representative or the VPT Inc. Sales Department at:

**Phone:** (425) 353-3010  
**Fax:** (425) 353-4030  
**E-mail:** [vptsales@vpt-inc.com](mailto:vptsales@vpt-inc.com)

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