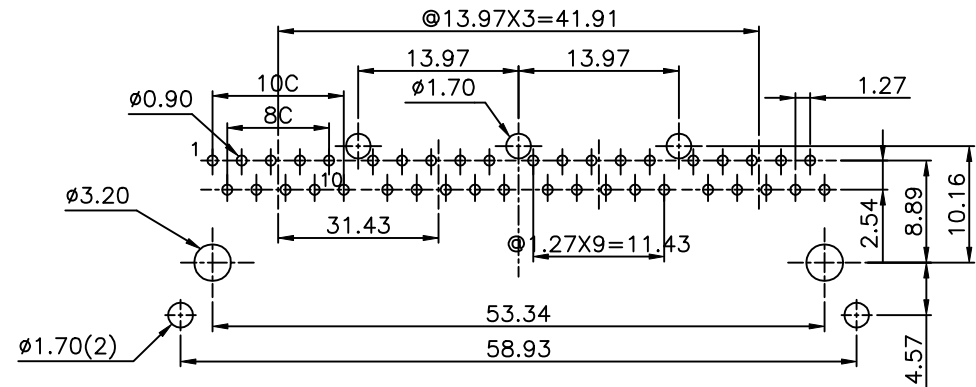
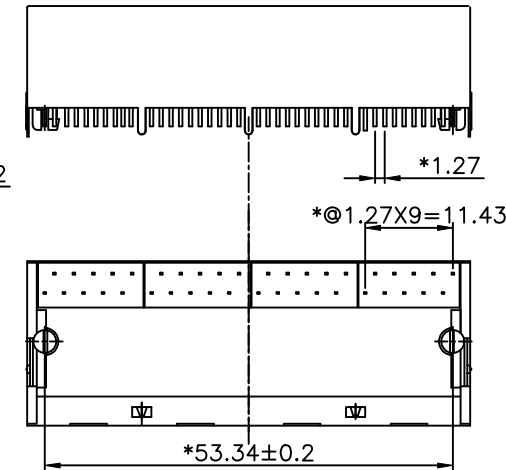
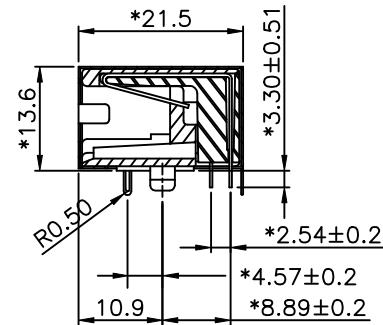
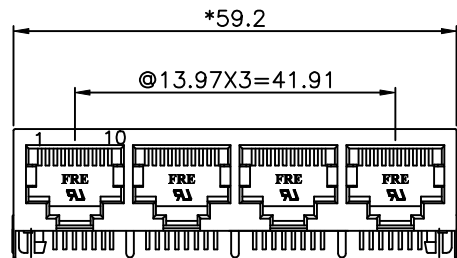


REVISION RECORD				
REV	ECO	DESCRIPTION	DRFT	CHKD



PC Board Layout Component Side Shown

NOTES:**ELECTRICAL:**

1. VOLTAGE RATING: 125 VAC RMS .
2. CURRENT RATING: 1.5 AMP .
3. CONTACT RESISTANCE: 50 MILLIOHMS MAX.
4. INSULATION RESISTANCE: 1000 MEGOHMS MIN @ 500 VDC.
5. DIELECTRIC STRENGTH: 1000 VAC RMS 60Hz, 1MIN.

MECHANICAL:

1. HOUSING MATERIAL: PBT+30%G.F UL 94V-0.
2. HOUSING COLOUR : BLACK OR YELLOW.
3. CONTACT MATERIAL: PHOSPHOR BRONZE t=0.35mm.
4. PLATING: : GOLD PLATING OVER NICKEL.
5. OPERATING LIFE: 750 CYCLES MIN.
6. PCB RETENTION PRE-SOLDER: 1 LB MIN.
7. PCB RETENTION POST-SOLDER: 10 LBS MIN.

ENVIRONMENTAL:

1. STORAGE: -40°C TO +85°C.
 2. OPERATION: 0°C TO 70°C.
 3. WAVE SOLDERING TEMPERATURE: 230°C~250°C (3~5 SECONDS)
- MATES WITH MODULAR PLUG CONFORMING TO
FCC PART 68, SUBPART F.

ROHS PART NUMBER: E590X-1XCXXX-L

NO. OF CONTACT
8- 8 CONTACTS
1- 10 CONTACTS

SHIELD:
3- TIN
5- NICKEL

1- 3u" 2- 6u" 3-15u"
4-30u" 5-50u"

4- W/ FRE LOGO
J-W/O FRE LOGO

1- FRONT PEG 4.57
2- FRONT PEG 4.57 ,WITH 3 GROUNDING TABS
3- FRONT PEG 4.57 ,WITH 5 GROUNDING TABS

DETACHED LISTS	MM (INCH)	DFTO JAMY	DATE 5/24/05	FULL RISE ELECTRONIC CO., LTD	
	TOLERANCES EXCEPT AS NOTED	CHKD QIANPU	DATE 2005/5/24		
	MM	MFO	DATE		
	.0 ± 0.2 ±	APPVLSAMMY	DATE 2005/5/24		
	.00 ± 0.15 ±				
	.000 ± 0.075 ±	MATERIAL :		TITLE	
	ANGLES ± 0.5	QT'Y :		20.57 SIDE ENTRY PCB JACK 10P	
	THIRD ANGLE PROJECTION	FINISH :		DRAWING NO. GE593111-LC	SIZE A3
		SCALE : 1:1		/PART NO. SEE NOTE	REV 0
				DO NOT SCALE DRAWING	SHEET OF