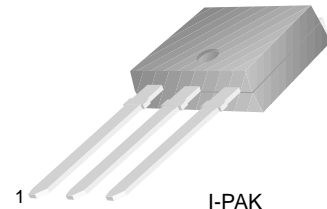


# KSC5054

KSC5054

**High Speed High Voltage Switching  
Industrial Use**



I-PAK  
1. Base 2. Collector 3. Emitter

## NPN Epitaxial Silicon Transistor

### Absolute Maximum Ratings $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Value	Units
$V_{CBO}$	Collector-Base Voltage	500	V
$V_{CEO}$	Collector-Emitter Voltage	400	V
$V_{EBO}$	Emitter-Base Voltage	7	V
$I_B$	Base Current	0.25	A
$I_C$	Collector Current (DC)	0.5	A
$I_{CP}$	*Collector Current (Pulse)	1	A
$P_C$	Collector Dissipation ( $T_a=25^\circ\text{C}$ )	1	W
$P_C$	Collector Dissipation ( $T_C=25^\circ\text{C}$ )	10	W
$T_J$	Junction Temperature	150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature	- 55 ~ 150	$^\circ\text{C}$

\*  $PW \leq 300\mu\text{s}$ , Duty Cycle  $\leq 10\%$

### Electrical Characteristics $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
$V_{CEO(sus)}$	Collector-Emitter Sustaining Voltage	$I_C = 0.3\text{A}$ , $I_{B1} = 0.06\text{A}$ , $L = 10\text{mH}$	400		V
$V_{CEX(sus)1}$	Collector-Emitter Sustaining Voltage	$I_C = 0.3\text{A}$ , $I_{B1} = -I_{B2} = 0.06\text{A}$ $V_{BE(off)} = -5\text{V}$ , $L = 10\text{mH}$	450		V
$V_{CEX(sus)2}$	Collector-Emitter Sustaining Voltage	$I_C = 0.6\text{A}$ , $I_{B1} = 0.2$ , $L = 10\text{mH}$ $I_{B2} = -0.06$ , $V_{BE(off)} = -5\text{V}$	400		V
$I_{CBO}$	Collector Cut-off Current	$V_{CB} = 400\text{V}$ , $I_E = 0$		10	$\mu\text{A}$
$I_{CER}$	Collector Cut-off Current	$V_{CE} = 400\text{V}$ , $R_{BE} = 51\Omega$ , $T_C = 125^\circ\text{C}$		1	mA
$I_{EBO}$	Emitter Cut-off Current	$V_{EB} = 5\text{V}$ , $I_C = 0$		10	$\mu\text{A}$
$I_{CEX1}$ $I_{CEX2}$	Collector Cut-off Current	$V_{CE} = 400\text{V}$ , $V_{BE(off)} = -1.5\text{V}$ $V_{CE} = 400\text{V}$ , $V_{BE(off)} = -1.5\text{V}$ @ $T_C = 125^\circ\text{C}$		10 1	$\mu\text{A}$ mA
$h_{FE1}$ $h_{FE2}$	DC Current Gain	$V_{CE} = 5\text{V}$ , $I_C = 0.05\text{A}$ $V_{CE} = 5\text{V}$ , $I_C = 0.3\text{A}$	20 10	80	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = 0.3\text{A}$ , $I_B = 0.06\text{A}$		1	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C = 0.3\text{A}$ , $I_B = 0.06\text{A}$		1.2	V
$t_{ON}$	Turn ON Time	$V_{CC} = 150\text{V}$ , $I_C = 0.3\text{A}$		1	$\mu\text{s}$
$t_{STG}$	Storage Time	$I_{B1} = -I_{B2} = 0.06\text{A}$ , $R_L = 500\Omega$		2.5	$\mu\text{s}$
$t_F$	Fall Time	$PW = 50\mu\text{s}$ , Duty Cycle $\leq 2\%$		1	$\mu\text{s}$

\* Pulse Test:  $PW \leq 350\mu\text{s}$ , Duty Cycle  $\leq 2\%$  Pulsed

### $h_{FE}$ Classification

Classification	R	O	Y
$h_{FE1}$	20 ~ 40	30 ~ 60	40 ~ 80

# Typical Characteristics

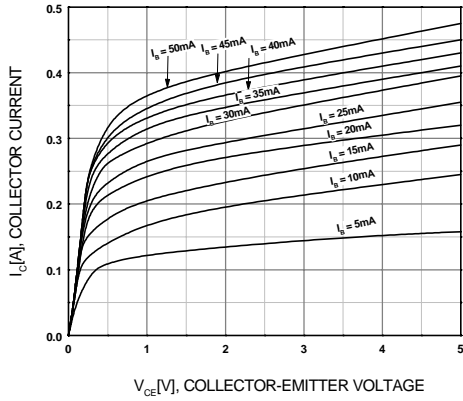


Figure 1. Static Characteristic

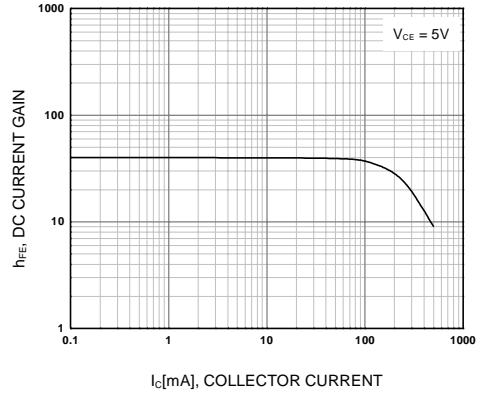


Figure 2. DC current Gain

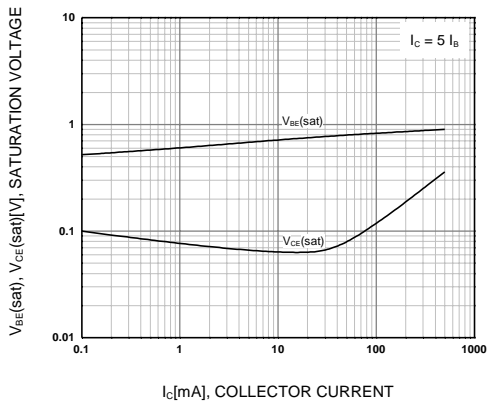


Figure 3. Base-Emitter Saturation Voltage  
Collector-Emitter Saturation Voltage

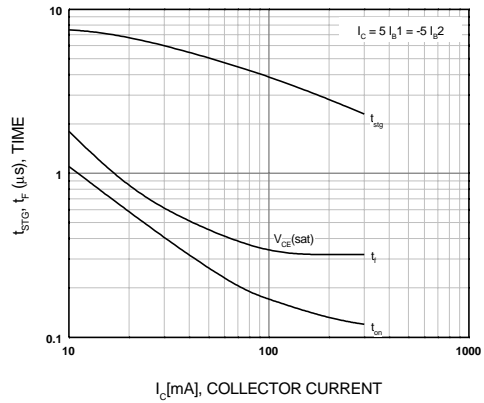


Figure 4. Switching Time

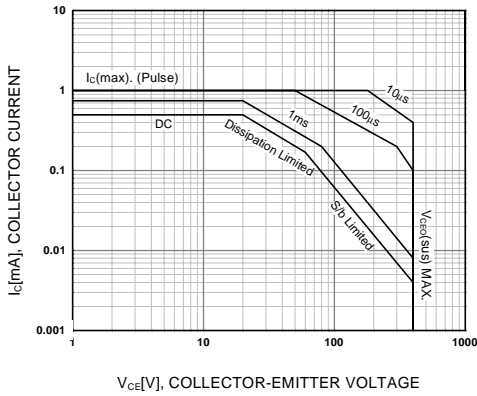


Figure 5. Safe Operating Area

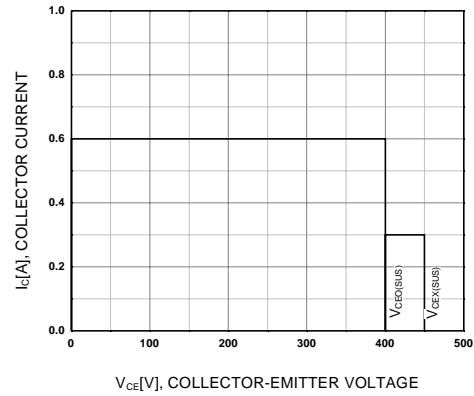


Figure 6. Reverse Bias Safe Operating Area

### Typical Characteristics (Continued)

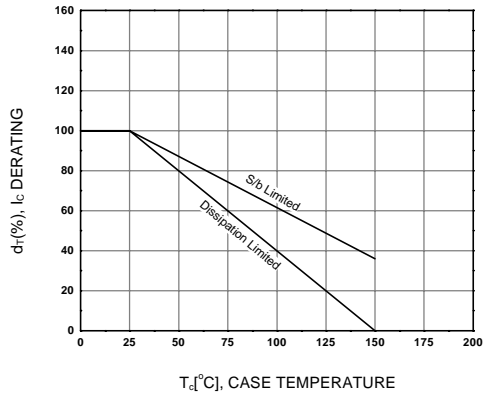


Figure 7. Derating Curve of Safe Operating Area

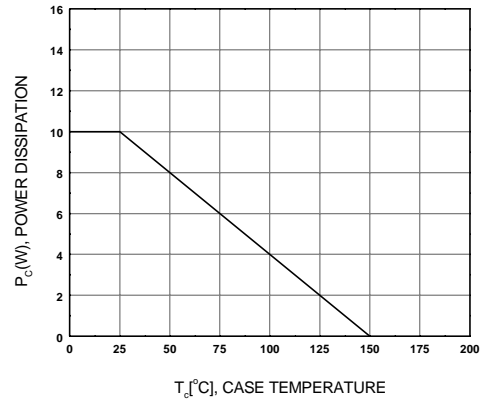


Figure 8. Power Derating

# Package Dimensions

## I-PAK



Dimensions in Millimeters

## TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx™	FAST®	OPTOPLANAR™	STAR*POWER™
Bottomless™	FASTr™	PACMAN™	Stealth™
CoolFET™	FRFET™	POP™	SuperSOT™-3
CROSSVOLT™	GlobalOptoisolator™	Power247™	SuperSOT™-6
DenseTrench™	GTO™	PowerTrench®	SuperSOT™-8
DOME™	HiSeC™	QFET™	SyncFET™
EcoSPARK™	ISOPLANAR™	QS™	TruTranslation™
E <sup>2</sup> CMOS™	LittleFET™	QT Optoelectronics™	TinyLogic™
EnSigna™	MicroFET™	Quiet Series™	UHC™
FACT™	MICROWIRE™	SLIENT SWITCHER®	UltraFET®
FACT Quiet Series™	OPTOLOGIC™	SMART START™	VCX™

STAR\*POWER is used under license

## DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

## LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

## PRODUCT STATUS DEFINITIONS

### Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

Fairchild Semiconductor

SEARCH | [Parametric](#) | [Cross Reference](#)

space

Product Folders and

Applies

find products

[Home](#) >> [Find products](#) >>

[Products groups](#)

[Analog and Mixed](#)

[Signal](#)

[Discrete](#)

[Interface](#)

[Logic](#)

[Microcontrollers](#)

[Non-Volatile](#)

[Memory](#)

[Optoelectronics](#)

[Markets and](#)

[applications](#)

[New products](#)

[Product selection and](#)

[parametric search](#)

[Cross-reference](#)

[search](#)

KSC5054

NPN Epitaxial Silicon Transistor

Contents

[Applications](#) | [Product status/pricing/packageing](#)

Applications

**High Speed High Voltage Switching**  
**Industrial Use**

[back to top](#)

Product status/pricing/packageing

Datasheet

[Download this datasheet](#)

PDF

[e-mail this datasheet](#)

[E-]

This page [Print version](#)

Related Links

[Request samples](#)

[How to order products](#)

[Product Change Notices \(PCNs\)](#)

[Support](#)

[Distributor and field sales representatives](#)

[Quality and reliability](#)

[Design tools](#)

technical information

buy products

technical support

my Fairchild

company

Product	Product status	Pricing*	Package type	Leads	Packing method
KSC5054OTU	Full Production	\$0.295	TO-251(IPAK)	3	RAIL
KSC5054YTU	Full Production	\$0.295	TO-251(IPAK)	3	RAIL
KSC5054RTU	Full Production	\$0.295	TO-251(IPAK)	3	RAIL

\* 1,000 piece Budgetary Pricing

[back to top](#)

[Home](#) | [Find products](#) | [Technical information](#) | [Buy products](#) | [Support](#) | [Company](#) | [Contact us](#) | [Site index](#) | [Privacy policy](#)

© Copyright 2002 Fairchild Semiconductor