

Features

- High Slew Rate
EHA2539—600 V/ μ s
EHA2540—400 V/ μ s
- Large open loop gain 15 kV/V (83 dB)
- Wide gain bandwidth
EHA2539—600 MHz
EHA2540—400 MHz
- Power bandwidth
EHA2539—9.5 MHz
EHA2540—6.3 MHz
- Low offset voltage 0.5 mV
- Low supply current 13 mA
- Output voltage swing ± 10 V
- MIL-STD-883 Rev. C compliant
- Exact replacements for HA2539 and HA2540

Applications

- Pulse and video amplifiers
- Wideband amplifiers
- High speed sample-hold circuits
- Local area networks

Ordering Information

Part No.	Temp. Range	Pkg.	Outline #
EHA1-2539-2	-55°C to +125°C	14-Pin CerDIP	MDP0014
EHA1-2539-5	0°C to +75°C	14-Pin CerDIP	MDP0014
EHA1-2539/883B	-55°C to +125°C	14-Pin CerDIP	MDP0014
EHA3-2539-5	0°C to +75°C	14-Pin P-DIP	MDP0031
EHA4-2539/883B	-55°C to +125°C	20-Pad LCC	MDP0007
EHA1-2540-2	-55°C to +125°C	14-Pin CerDIP	MDP0014
EHA1-2540-5	0°C to +75°C	14-Pin CerDIP	MDP0014
EHA1-2540/883B	-55°C to +125°C	14-Pin CerDIP	MDP0014
EHA3-2540-5	0°C to +75°C	14-Pin P-DIP	MDP0031
EHA4-2540/883B	-55°C to +125°C	20-Pad LCC	MDP0007

5962-87787 is the SMD version of EHA2539.
5962-89648 is the SMD version of EHA2540.

General Description

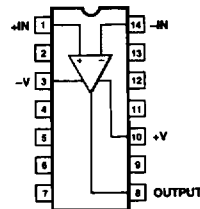
The EHA2539 and EHA2540 monolithic amplifiers are exact replacements for the HA2539 and HA2540 amplifiers. These Elantec amplifiers are well behaved in the presence of capacitive loading, and are stable for closed loop gains of 10 or greater. The EHA2539 is the fastest in the series with a 600 V/ μ s Slew Rate and 600 MHz gain bandwidth, while the EHA2540 slews at 400 V/ μ s and has a 400 MHz gain bandwidth. Both amplifiers are fabricated using Elantec's Complementary Bipolar process. For improved guaranteed specifications on offset voltage and supply current see the EL2039/EL2040.

Elantec's high speed amplifiers are widely used in military, video and medical applications. They are especially suited for high speed video amplifiers, pulse detectors, and wide bandwidth filters.

Elantec's EHA2539/883B and EHA2540/883B comply with MIL-STD-883 Revision C in all aspects, including burn-in at 125°C. Elantec's facilities comply with MIL-I-45208A and other applicable quality specifications. For information on Elantec's military processing, see the Elantec document, QRA-2: *Elantec's Military Processing—Monolithic Products.*

Connection Diagrams

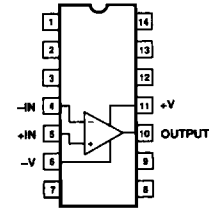
EHA1-2539 EHA3-2539



Top View

2539-1

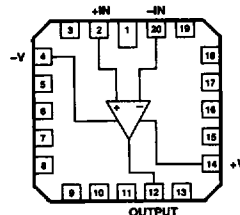
EHA1-2540 EHA3-2540



Top View

2539-2

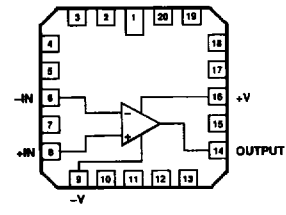
EHA4-2539



Top View

2539-3

EHA4-2540



Top View

2539-4

Note: Non-designated pins are no connects and are not electrically connected internally.

Manufactured under U.S. Patent No. 4,837,523

EHA2539/EHA2540

Very High Slew Rate Wideband Operational Amplifier

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$)

V_S	Voltage between $V+$ and $V-$	35V	T_J	Operating Junction Temperature	
V_{DIFF}	Differential Input Voltage	6V		CerDIP, Ceramic LCC	175°C
I_{OP}	Output Current, Peak	50 mA		Plastic DIP	150°C
I_{OC}	Output Current, Continuous	25 mA	T_{ST}	Storage Temperature	-65°C to +150°C
P_D	Internal Power Dissipation	See Curves	T_{LT}	Lead Temperature	
T_A	Operating Temperature Range			(Soldering, 5 seconds)	300°C
	-2, /883B	-55°C to +125°C			
	-5	0°C to +75°C			

Important Note:

All parameters having Min/Max specifications are guaranteed. The Test Level column indicates the specific device testing actually performed during production and Quality inspection. Elantec performs most electrical tests using modern high-speed automatic test equipment, specifically the LTX77 Series system. Unless otherwise noted, all tests are pulsed tests, therefore $T_J = T_C = T_A$.

Test Level	Test Procedure
I	100% production tested and QA sample tested per QA test plan QCX0002.
II	100% production tested at $T_A = 25^\circ\text{C}$ and QA sample tested at $T_A = 25^\circ\text{C}$, T_{MAX} and T_{MIN} per QA test plan QCX0002.
III	QA sample tested per QA test plan QCX0002.
IV	Parameter is guaranteed (but not tested) by Design and Characterization Data.
V	Parameter is typical value at $T_A = 25^\circ\text{C}$ for information purposes only.

DC Electrical Characteristics $V_S = \pm 15\text{V}$, $R_L = 1\text{ k}\Omega$; unless otherwise specified

Parameter	Description	Temp	Min	Typ	Max	-2, /883B		-5		Units
						Test Level	Test Level	Test Level	Test Level	
V_{OS}	Offset Voltage (-2, /883B)	25°C		0.5	10	I				mV
		Full			15	I				mV
V_{OS}	Offset Voltage (-5)	25°C		0.5	15			I		mV
		Full			20			III		μV
TCV_{OS}	Average Offset Voltage Drift	Full		20		V		V		$\mu\text{V}/^\circ\text{C}$
I_B	Bias Current	25°C		5	20	I		I		μA
		Full			25	I		III		μA
I_{OS}	Offset Current	25°C		1	6	I		I		μA
		Full			8	I		III		μA
R_{IN}	Input Resistance	25°C		10		V		V		k Ω
C_{IN}	Input Capacitance	25°C		1		V		V		pF
V_{CM}	Common Mode Range	Full	± 10	± 12		I		II		V
e_{IN}	Input Noise Voltage ($f = 1\text{ kHz}$, $R_G = 0\Omega$)	25°C		6		V		V		$\text{nV}/\sqrt{\text{Hz}}$
A_{VOL}	Large Signal Voltage Gain (Note 1)	25°C	10k	15k		I		I		V/V
		Full	5k			I		III		V/V
$CMRR$	Common-Mode Rejection Ratio (Note 2)	Full	60	90		I		II		dB
V_O	Output Voltage Swing	Full	± 10			I		II		V
I_O	Output Current	Full	± 10			I		II		mA
R_O	Output Resistance	25°C		30		V		V		Ω
I_S	Supply Current	Full		13	25	I		II		mA
$PSRR$	Power Supply Rejection Ratio (Note 7)	Full	60	85		I		II		dB

EHA2539/EHA2540

Very High Slew Rate Wideband Operational Amplifier

EHA2539/EHA2540

AC Electrical Characteristics—EHA2539

Parameter	Description	Temp	Min	Typ	Max	-2, /883B	-5	Units
						Test Level	Test Level	
GBW	Gain-Bandwidth Product (Notes 3, 4)	25°C		600		V	V	MHz
FPBW	Full Power Bandwidth (Notes 1, 5, 8)	25°C	8.7	9.5		I	I	MHz
t_r	Rise Time (Note 6)	25°C		7		V	V	ns
OS	Overshoot (Note 6)	25°C		35		V	V	%
SR	Slew Rate (Note 6)	25°C	550	600		I	I	V/ μ s
t_s	Settling Time (Notes 9, 10) 10V Step to 0.1%	25°C		100		V	V	ns

AC Electrical Characteristics—EHA2540

Parameter	Description	Temp	Min	Typ	Max	-2, /883B	-5	Units
						Test Level	Test Level	
GBW	Gain-Bandwidth Product (Notes 3, 4)	25°C		400		V	V	MHz
FPBW	Full Power Bandwidth (Notes 1, 5, 8)	25°C	5.5	6		I	I	MHz
t_r	Rise Time (Note 6)	25°C		14		V	V	ns
OS	Overshoot (Note 6)	25°C		15		V	V	%
SR	Slew Rate (Note 6)	25°C	350	400		I	I	V/ μ s
t_s	Settling Time (Notes 9, 10) 10V Step to 0.1%	25°C		70		V	V	ns

Note 1: $V_O = \pm 10V$.

Note 2: Two tests are performed. $V_{CM} = 0V$ to 10V and $V_{CM} = 0V$ to -10V.

Note 3: $V_O = 90$ mV.

Note 4: $A_V = 10$.

Note 5: Full Power Bandwidth guaranteed based on slew rate measurement using: $FPBW = \frac{\text{Slew Rate}}{2\pi V_{\text{peak}}}$

Note 6: Refer to Test Circuits section of data sheet.

Note 7: Two tests are performed. $V^+ = 15V$, and V^- is changed from -5V to -15V. $V^- = -15V$, and V^+ is changed from 5V to 15V.

Note 8: $R_L = 1$ k Ω .

Note 9: Settling time measurements are made with techniques in the following reference: "Take the Guesswork Out of Settling-Time Measurements," EDN, September 19, 1985.

Note 10: $A_V = -10$, $R_L = 1k$.

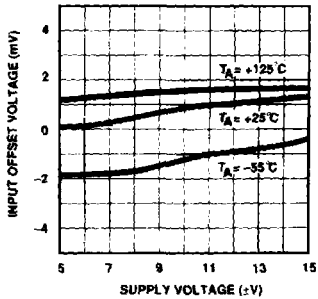
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EHA2539/EHA2540

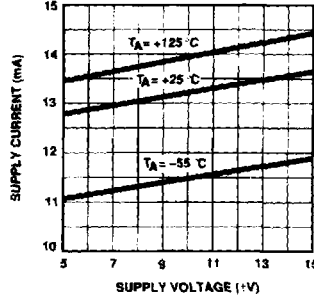
Very High Slew Rate Wideband Operational Amplifier

EHA2539/EHA2540 Typical DC Performance Curves

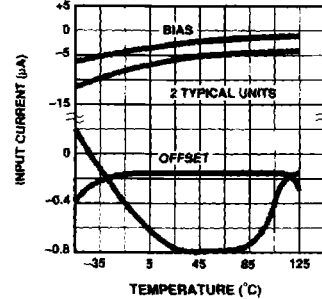
Input Offset Voltage vs Supply Voltage



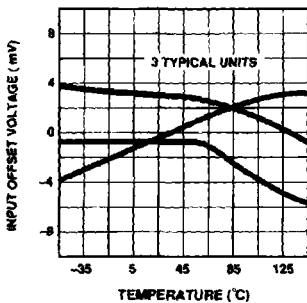
Supply Current vs Supply Voltage



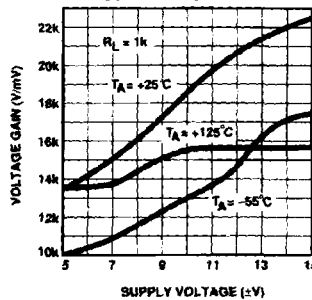
Input Currents vs Temperature



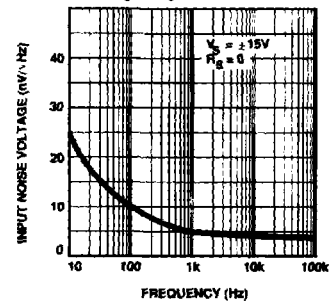
Input Offset Voltage vs Temperature



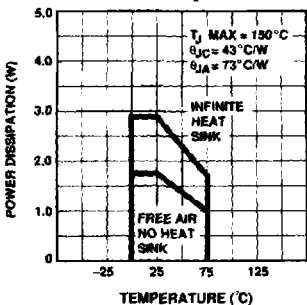
Voltage Gain vs Supply Voltage



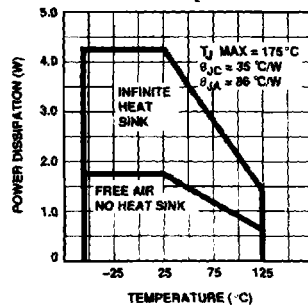
Input Noise Voltage vs Frequency



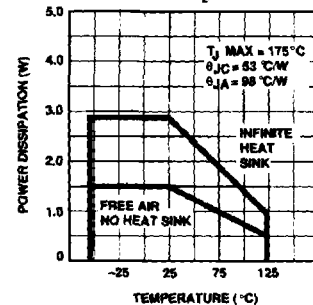
**14-Lead Plastic DIP
Maximum Power Dissipation vs Ambient Temperature**



**14-Lead CerDIP
Maximum Power Dissipation vs Ambient Temperature**



**20-Lead LCC
Maximum Power Dissipation vs Ambient Temperature**

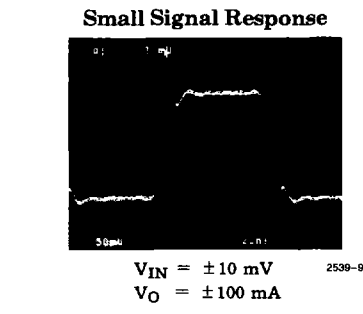
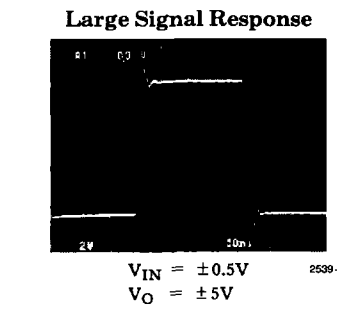
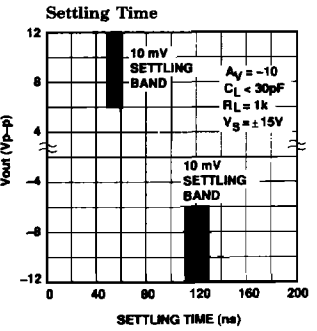
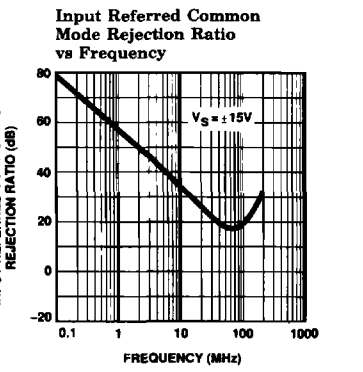
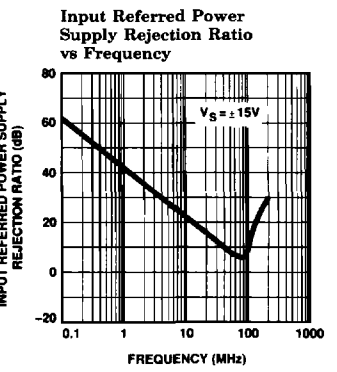
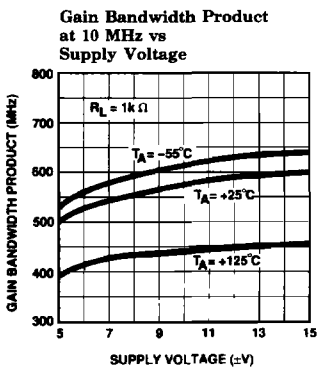
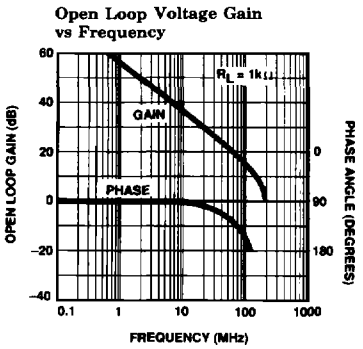
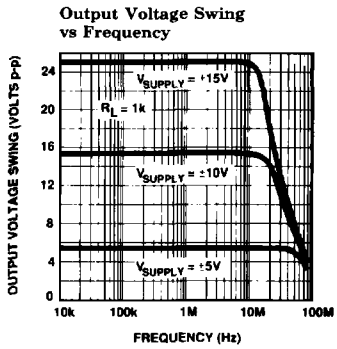
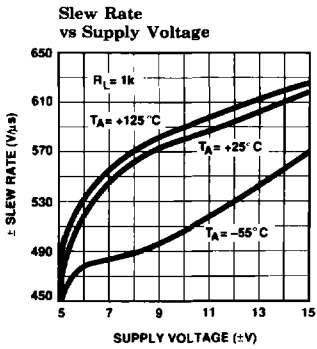


EHA2539/EHA2540

Very High Slew Rate Wideband Operational Amplifier

EHA2539/EHA2540

EHA2539 Typical AC Performance Curves



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2539-6

2539-7

2539-8

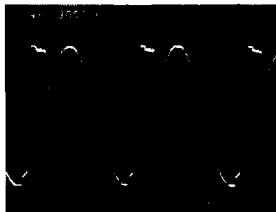
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EHA2539/EHA2540

Very High Slew Rate Wideband Operational Amplifier

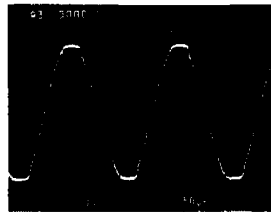
EHA2539 Typical AC Performance Curves — Contd.

HA2539 at Onset of Clipping



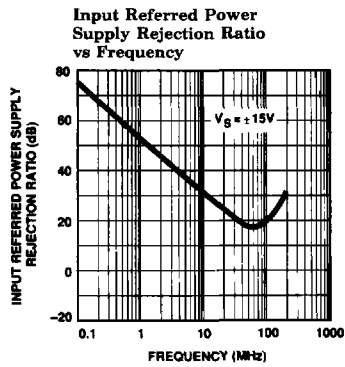
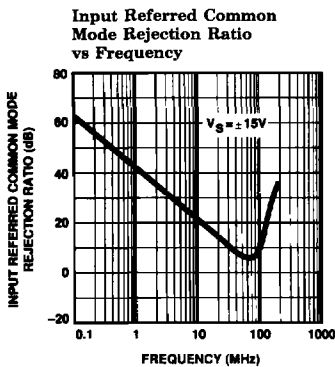
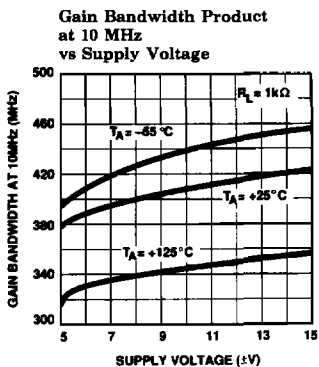
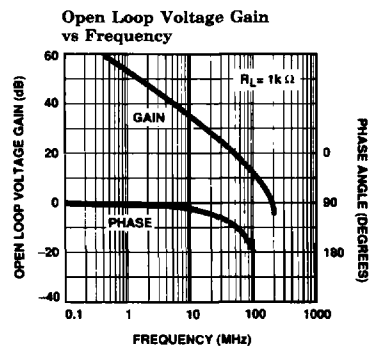
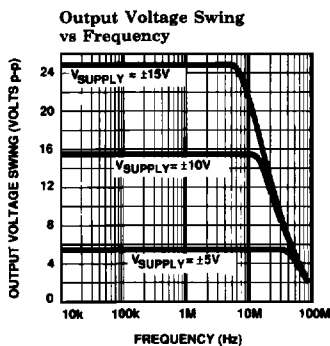
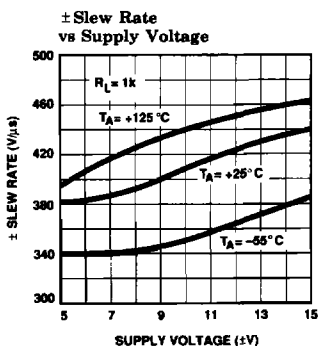
2539-10

EHA2539 at Onset of Clipping



2539-11

EHA2540 Typical AC Performance Curves



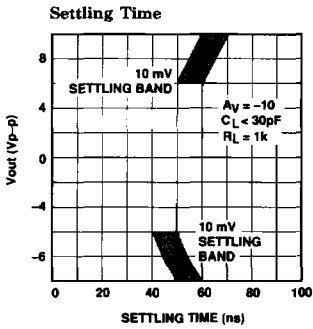
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EHA2539/EHA2540

Very High Slew Rate Wideband Operational Amplifier

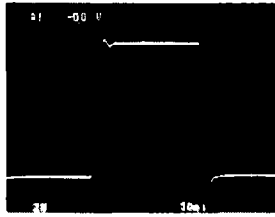
EHA2539/EHA2540

EHA2540 Typical AC Performance Curves — Contd.



2539-13

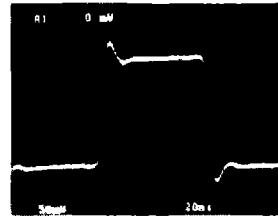
Large Signal Response



$V_{IN} = \pm 0.5V$
 $V_O = \pm 5V$

2539-14

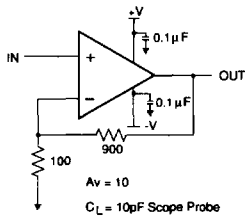
Small Signal Response



$V_{IN} = \pm 10 mV$
 $V_O = \pm 100 mV$

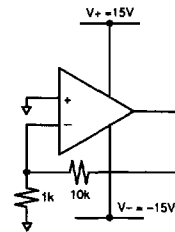
2539-15

Test Circuit



2539-16

Burn-In Circuit



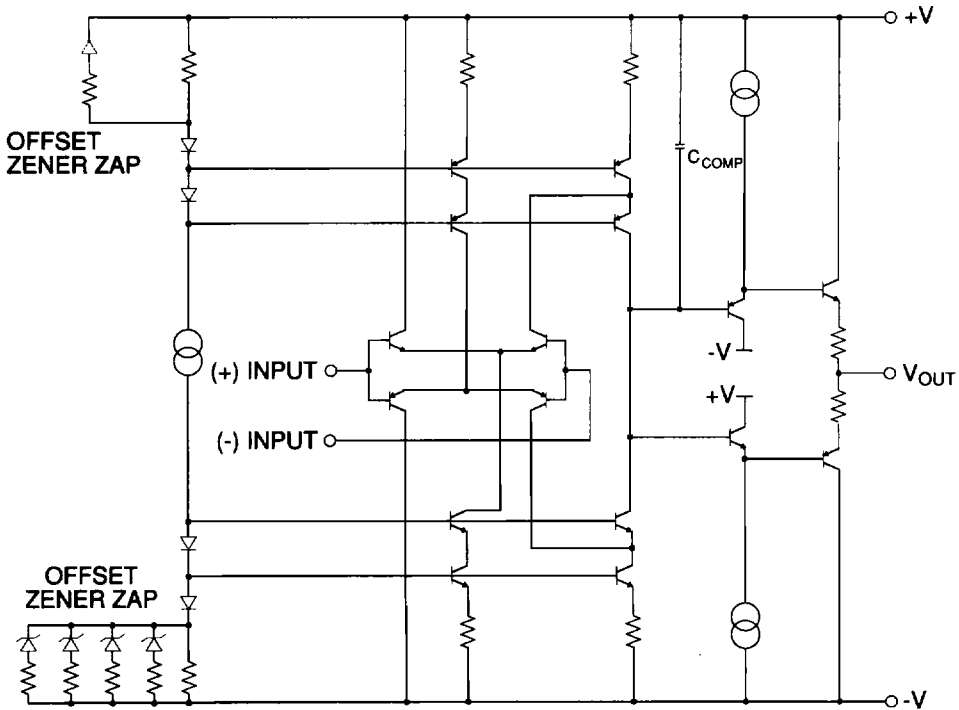
2539-17

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EHA2539/EHA2540

Very High Slew Rate Wideband Operational Amplifier

Schematic

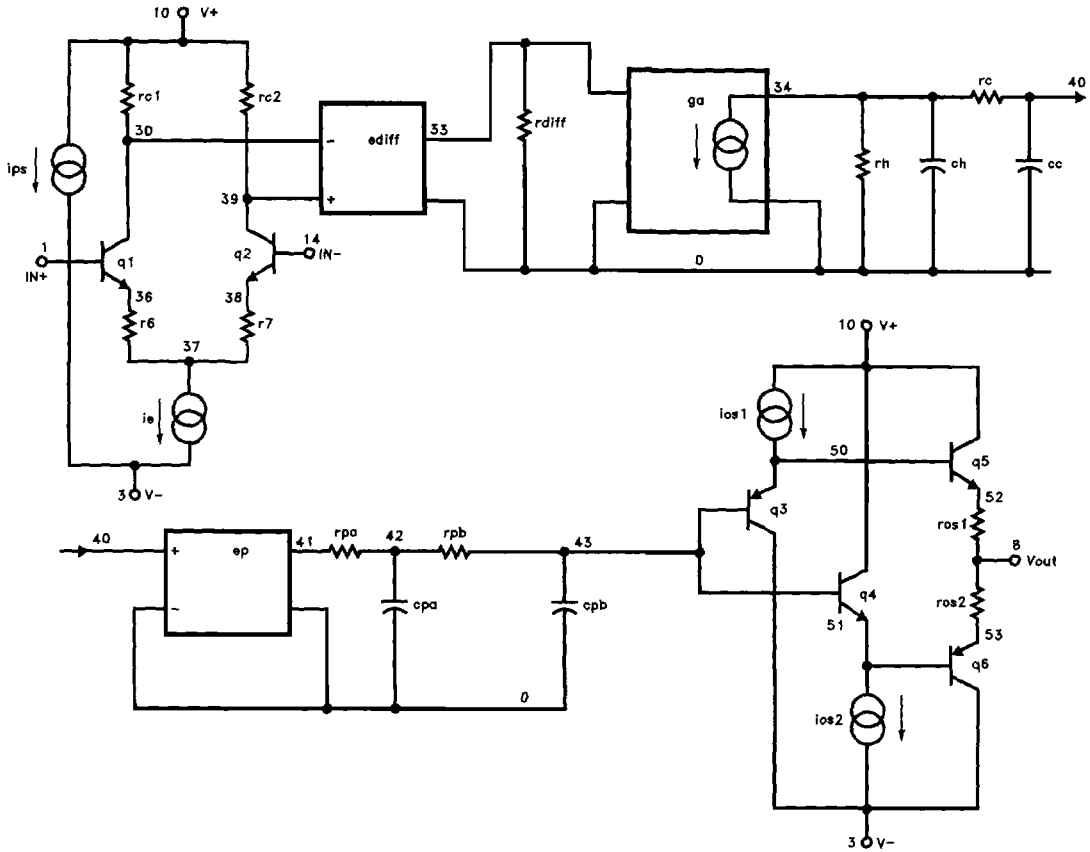


2539-18

EHA2539/EHA2540

Very High Slew Rate Wideband Operational Amplifier

EHA2539 Macromodel — Contd.



2539-10

EHA2539/EHA2540

Very High Slew Rate Wideband Operational Amplifier

EHA2539/EHA2540

EHA2540 Macromodel

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* Connections:      + input
*                  |
*                  | -input
*                  | |
*                  | | + Vsupply
*                  | | - Vsupply
*                  | |
*                  | | output
*                  |
.subckt M2540      5  4  11  6  10
* Input Stage
ie 37 6 1.3mA
r6 36 37 60
r7 38 37 60
rc1 11 30 75
rc2 11 39 75
q1 30 5 36 qn
q2 39 4 38 qna
ediff 33 0 39 30 7.25
rdiff 33 0 1Meg
* Compensation Section
ga 0 34 33 0 5.2m
rh 34 0 .525Meg
ch 34 0 1.5pF
rc 34 40 600
cc 40 0 7pF
* Poles
ep 41 0 40 0 1
rpa 41 42 75
cpa 42 0 7pF
rpb 42 43 50
cpb 43 0 3pF
* Output Stage
ios1 11 50 1.25mA
ios2 51 6 1.25mA
q3 6 43 50 qp
q4 11 43 51 qn
q5 11 50 52 qn
q6 6 51 53 qp
ros1 52 10 25
ros2 10 53 25
* Power Supply Current
ips 11 6 9.5mA
* Models
.model qn npn(is=800.0E-18 bf=130 tf=0.2nS)
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.model qp pnp(is=800E-18 bf=60 tf=0.2nS)
.ends

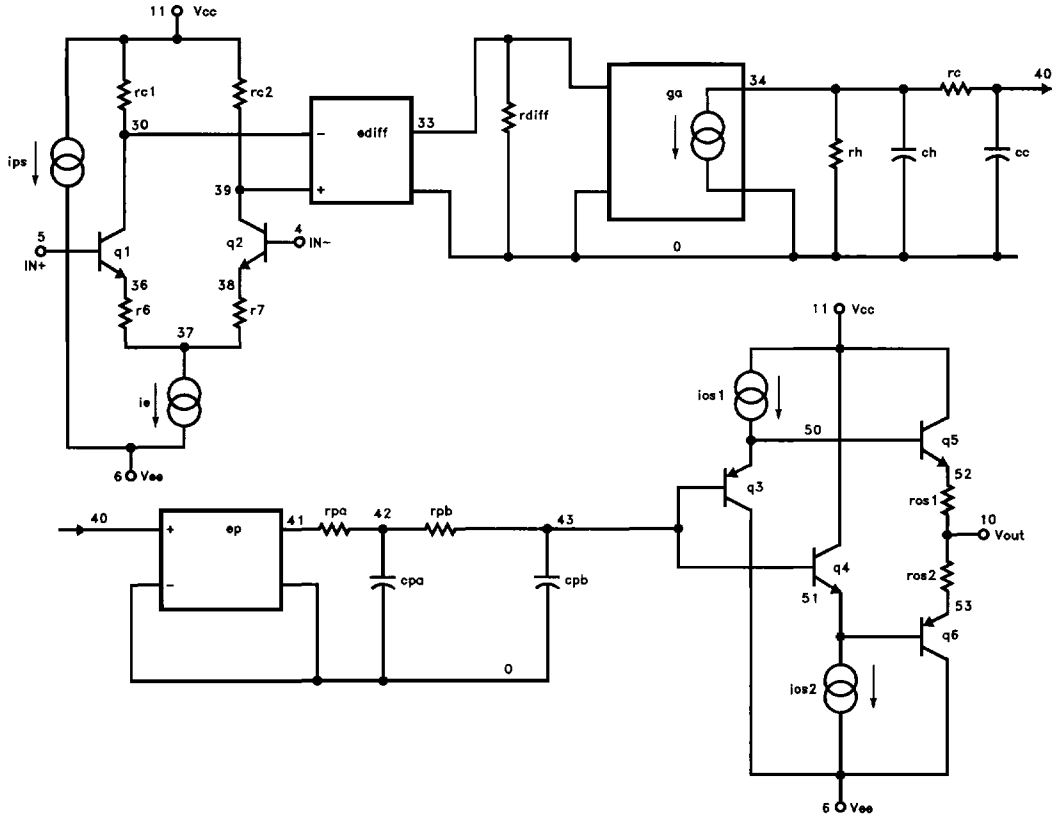
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EHA2539/EHA2540

Very High Slew Rate Wideband Operational Amplifier

EHA2540 Macromodel — Contd.



2538-20