N-Channel 60-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

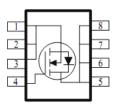
Typical	Appl	licatior	IS:
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- White LED boost converters
- · Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY				
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I□ (A)		
60	$38 @ V_{GS} = 10V$	7.4		
	50 @ V _{GS} = 4.5V	6.5		







ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)					
Parameter			Limit	Units	
Drain-Source Voltage		V_{DS}	60	V	
Gate-Source Voltage			±20	V	
Continuous Drain Current ^a	T _A =25°C		7.4		
Continuous Drain Current	T _A =70°C	I _D	6.3	Α	
Pulsed Drain Current ^b			30		
Continuous Source Current (Diode Conduction) a			4.3	Α	
Power Dissipation ^a	T _A =25°C	P _D	3.1	W	
Prower Dissipation	T _A =70°C	'D	2.2	v v	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter			Maximum	Units	
Maximum Junction-to-Ambient ^a	t <= 10 sec	$R_{\theta JA}$	40	°C/W	
Maximum Junction-to-Ambient	Steady State	IN _θ JΑ	80	C/VV	

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

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Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current	lass	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}$	1		1		
Zero Gate Voltage Brain Gurrent	I _{DSS}	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25	uA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α	
Drain Cauras On Basistanas a	r	$V_{GS} = 10 \text{ V}, I_D = 5.9 \text{ A}$			38	mΩ	
Drain-Source On-Resistance ^a	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 4.8 \text{ A}$			50	11122	
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 5.9 \text{ A}$		15		S	
Diode Forward Voltage ^a	V_{SD}	$I_S = 2.2 \text{ A}, V_{GS} = 0 \text{ V}$		0.76		V	
		Dynamic ^b					
Total Gate Charge	Q_g	$V_{DS} = 30 \text{ V}, V_{GS} = 4.5 \text{ V},$		16			
Gate-Source Charge	Q_{gs}	$I_{DS} = 50 \text{ V}, \text{ V}_{GS} = 4.3 \text{ V},$ $I_{D} = 5.9 \text{ A}$		3.9		nC	
Gate-Drain Charge	Q_{gd}	1B = 3.3 A		7.9			
Turn-On Delay Time	t _{d(on)}			8			
Rise Time	t _r	$V_{DS} = 30 \text{ V}, R_L = 5.1 \Omega, I_D = 5.9 \text{ A},$		12		no	
Turn-Off Delay Time	$t_{d(off)}$	V_{GEN} = 10 V, R_{GEN} = 6 Ω		48		ns	
Fall Time	t _f			14			
Input Capacitance	C _{iss}			1465			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ Mhz}$		126		pF	
Reverse Transfer Capacitance	C_{rss}			114			

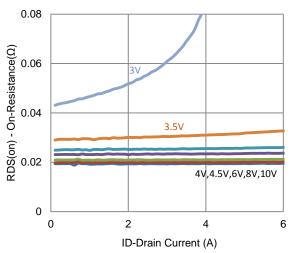
Notes

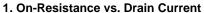
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- Guaranteed by design, not subject to production testing.

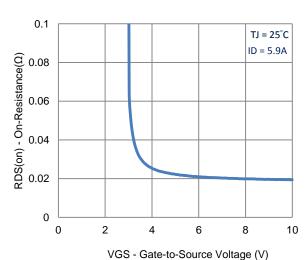
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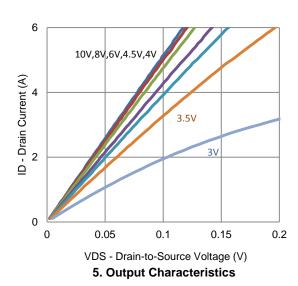
Typical Electrical Characteristics

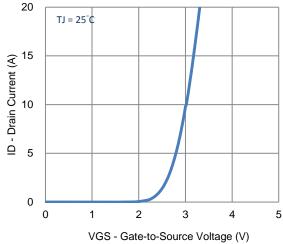




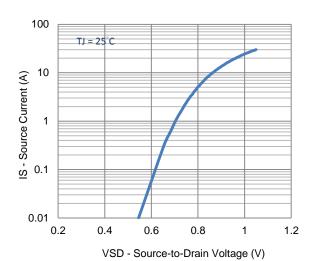


3. On-Resistance vs. Gate-to-Source Voltage

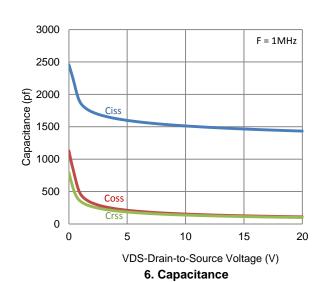




2. Transfer Characteristics

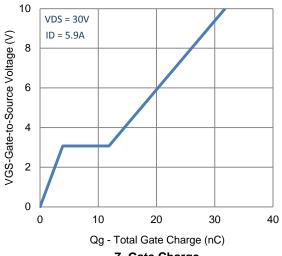


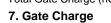
4. Drain-to-Source Forward Voltage

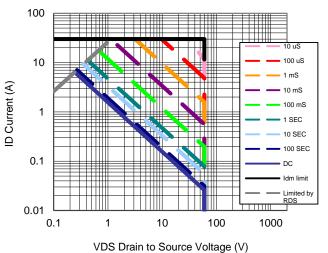


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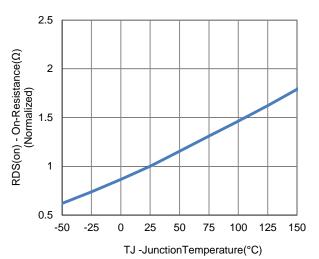
Typical Electrical Characteristics



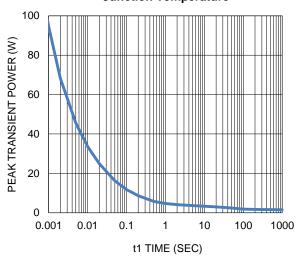




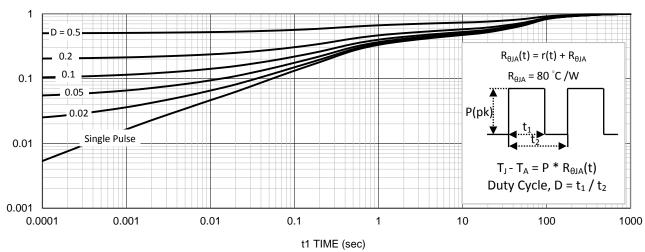
9. Safe Operating Area



8. Normalized On-Resistance Vs **Junction Temperature**



10. Single Pulse Maximum Power Dissipation

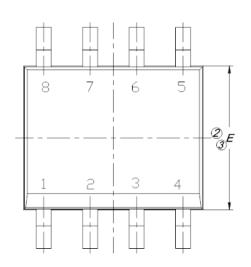


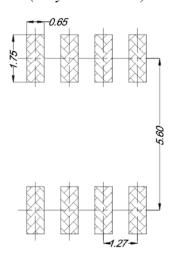
11. Normalized Thermal Transient Junction to Ambient

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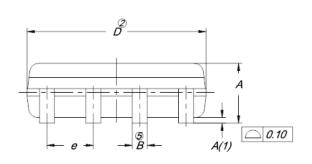
Package Information

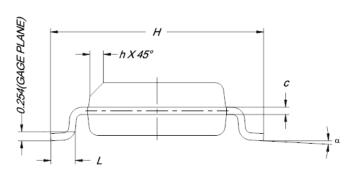
Land Pattern (Only for Reference)





5114	MILLIMETERS				
DIM.	MIN.	NOM.	MAX.		
Α	1.35	1.55	1.75		
A(1)	0.10	0.18	0.25		
В	0.38	0.45	0.51		
С	0.19	0.22	0.25		
D	4.80	4.90	5.00		
E	3.80	3.90	4.00		
е	1.27 BSC				
Н	5.80	6.00	6.20		
L	0.50	0.72	0.93		
α	0°	4°	8°		
h	0.25	0.38	0.50		





Note:

- 1. All Dimension Are In mm.
- Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.