

RJM0603JSC

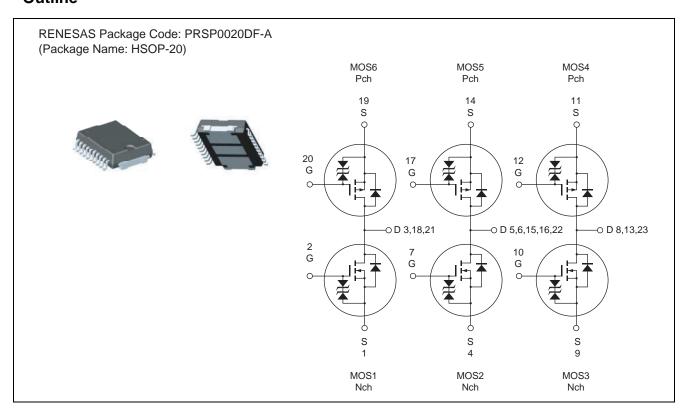
Silicon N/P Channel Power MOS FET (6 in 1 Type) High Speed Power Switching

R07DS0339EJ0501 Rev.5.01 Jul 22, 2011

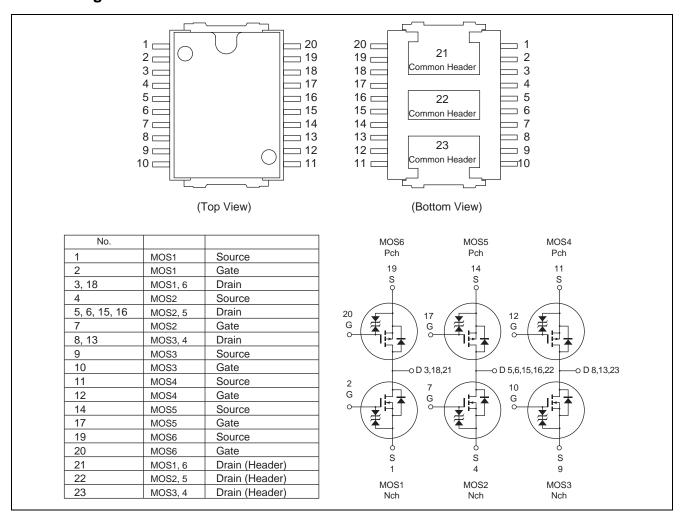
Features

- For Automotive applications
- AEC-Q101 compliant
- N/P Channel MOS FET (6 in 1 Type). High density mounting
- Low on-resistance
- Capable of 4.5 V gate drive

Outline



Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Cumbal	Va	Heit	
	Symbol	MOS1, 2, 3 (Nch)	MOS4, 5, 6 (Pch)	Unit
Drain to source voltage	V_{DSS}	60	-60	V
Gate to source voltage	V_{GSS}	+20 / -5	-20 / +5	V
Drain current	I _D	20	-20	Α
Drain peak current	I _D (pulse) Note1	80	-80	Α
Channel dissipation	Pch Note2	54	54	W
Avalanche current	I _{AP} Note3	20	20	А
Avalanche energy	E _{AR} Note3	34	34	mJ
Channel temperature	Tch Note4	175	175	°C
Storage temperature	Tstg	-55 to +150	-55 to +150	°C

Notes: 1. $PW \le 10\mu s$ duty cycle $\le 1\%$

2. 1 Drive Operation ; Value at $Tc = 25^{\circ}C$

3. Value at Tch = 25°C, Rg \geq 50 Ω

4. AEC-Q101 compliant

Thermal Impedance Characteristics

• Channel to case thermal impedance θ ch-c: 2.78°C/W

Electrical Characteristics

• MOS1, MOS2, MOS3 (N Channel)

 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Zero gate voltage drain current	I _{DSS}	_	_	10	μΑ	$V_{DS} = 60 \text{ V}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±10	μΑ	$V_{GS} = +20 \text{ V} / -5 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	_	2.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	16	20	mΩ	$I_D = 10 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note5}}$
resistance		_	21	32	mΩ	$I_D = 10 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note5}}$
Input capacitance	Ciss	_	2600		pF	$V_{DS} = 10V, V_{GS} = 0,$
Output capacitance	Coss	_	290	_	рF	f = 1 MHz
Reverse transfer capacitance	Crss	_	140	_	рF	
Total gate charge	Qg	_	43	_	nC	$V_{DD} = 25 \text{ V}, V_{GS} = 10 \text{ V},$
Gate to source charge	Qgs	_	6.2	_	nC	I _D = 20 A
Gate to drain charge	Qgd	_	7.2	_	nC	
Turn-on delay time	t _{d(on)}	_	13	_	ns	$V_{GS} = 10 \text{ V}, I_D = 10 \text{ A},$
Rise time	t _r	_	6	_	ns	$V_{DD} \cong 30 \text{ V}, \text{ R}_{L} = 3 \Omega,$
Turn-off delay time	t _{d(off)}	_	65	_	ns	$R_G = 4.7 \Omega$
Fall time	t _f	_	4.5	_	ns	
Body-drain diode forward voltage	V_{DF}	_	0.91	1.18	V	I _F = 20 A, V _{GS} = 0 Note5
Body-drain diode reverse recovery	t _{rr}	_	35	_	ns	I _F = 20 A, V _{GS} = 0
time						di _F /dt = 100 A/μs

Note: 5. Pulse test

• MOS4, MOS5, MOS6 (P Channel)

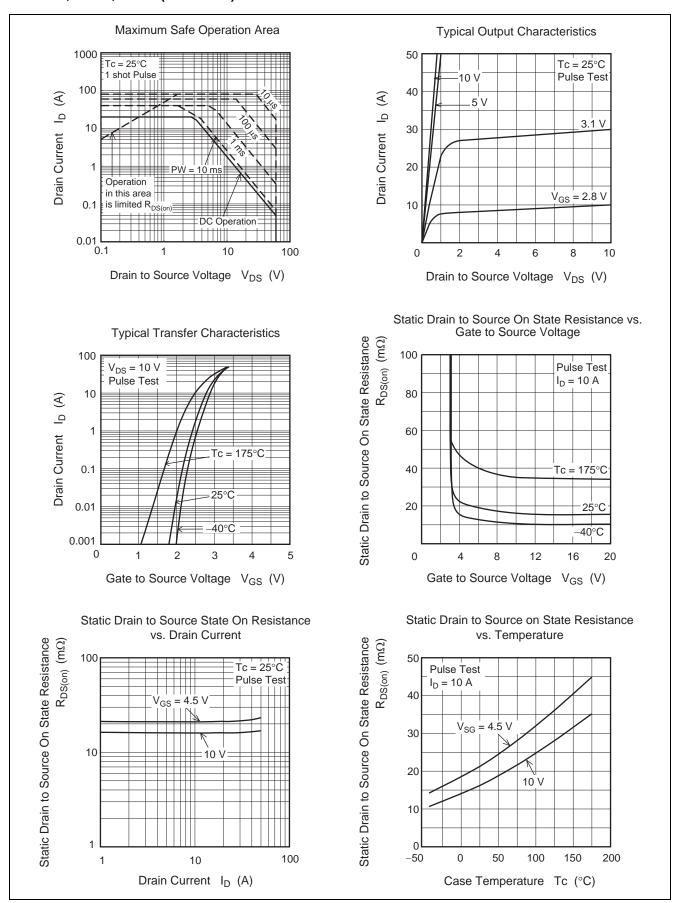
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Zero gate voltage drain current	I _{DSS}	_	_	-10	μΑ	$V_{DS} = -60 \text{ V}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±10	μΑ	$V_{GS} = -20 \text{ V} / +5 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-1.0	_	-2.5	V	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	32	40	mΩ	$I_D = -10 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note6}}$
resistance		_	42	64	mΩ	$I_D = -10 \text{ A}, V_{GS} = -4.5 \text{ V}^{\text{Note6}}$
Input capacitance	Ciss	_	2600	_	pF	$V_{DS} = -10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	330	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	240	_	pF	
Total gate charge	Qg	_	53	_	nC	$V_{DD} = -25 \text{ V}, V_{GS} = -10 \text{ V},$ $I_{D} = -20 \text{ A}$
Gate to source charge	Qgs	_	8.8	_	nC	
Gate to drain charge	Qgd	_	13	_	nC	
Turn-on delay time	t _{d(on)}	_	22	_	ns	$V_{GS} = -10 \text{ V}, I_{D} = -10 \text{ A},$
Rise time	t _r	_	17	_	ns	$\label{eq:def_D} \left \begin{array}{l} V_{DD} \cong -30 \ V, \ R_L = 3 \ \Omega, \\ R_G = 4.7 \ \Omega \end{array} \right $
Turn-off delay time	t _{d(off)}	_	100	_	ns	
Fall time	t _f	_	20	_	ns	
Body-drain diode forward voltage	V_{DF}	_	-0.95	-1.24	V	$I_F = -20 \text{ A}, V_{GS} = 0$ Note6
Body-drain diode reverse recovery	t _{rr}	_	50	_	ns	$I_F = -20 \text{ A}, V_{GS} = 0$
time						$di_F/dt = 100 A/\mu s$

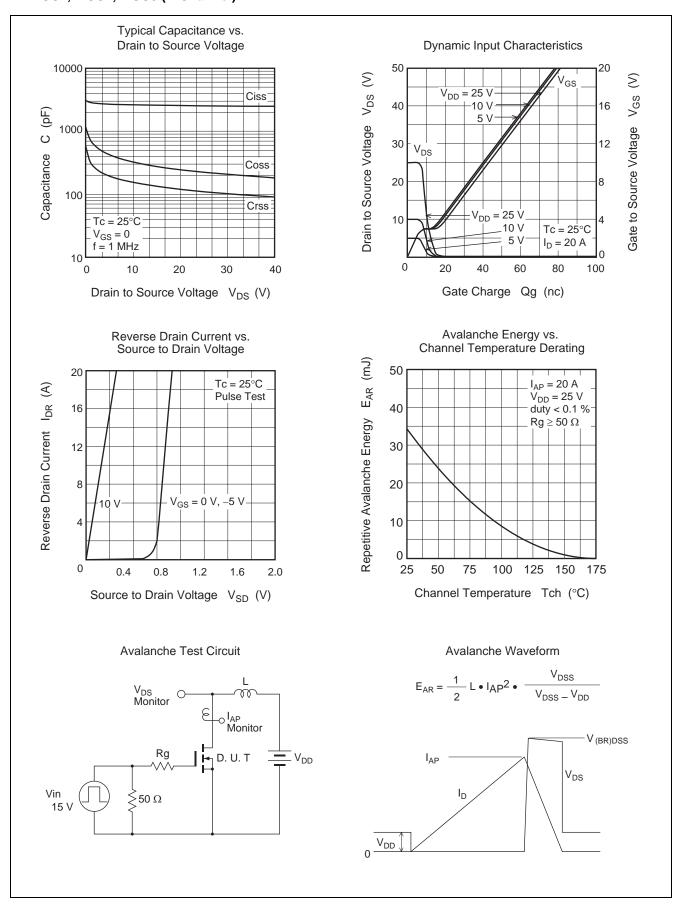
Note: 6. Pulse test

Main Characteristics

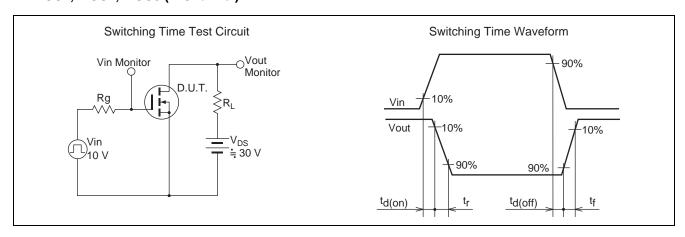
• MOS1, MOS2, MOS3 (N Channel)



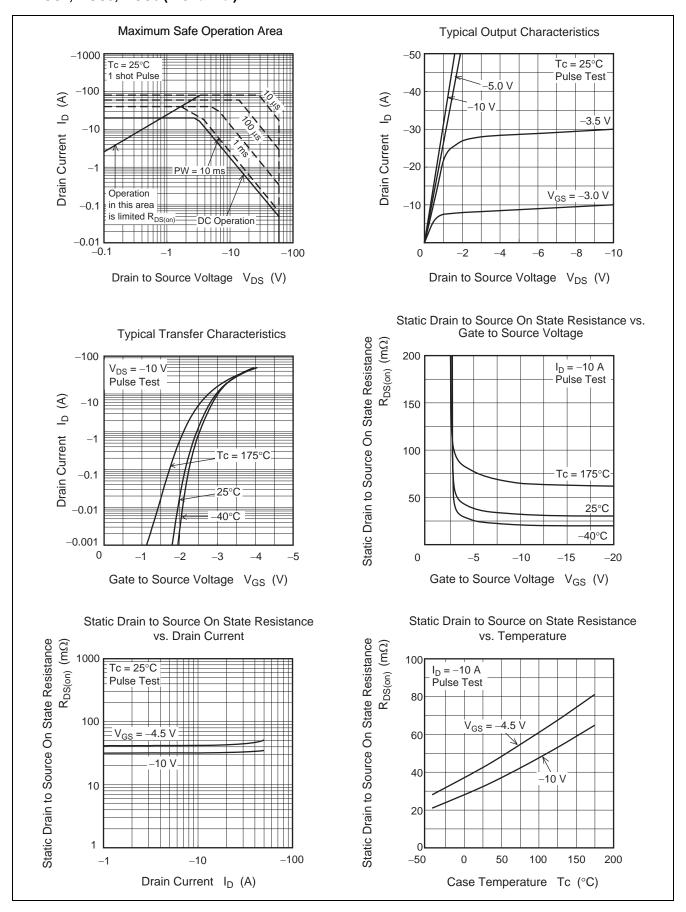
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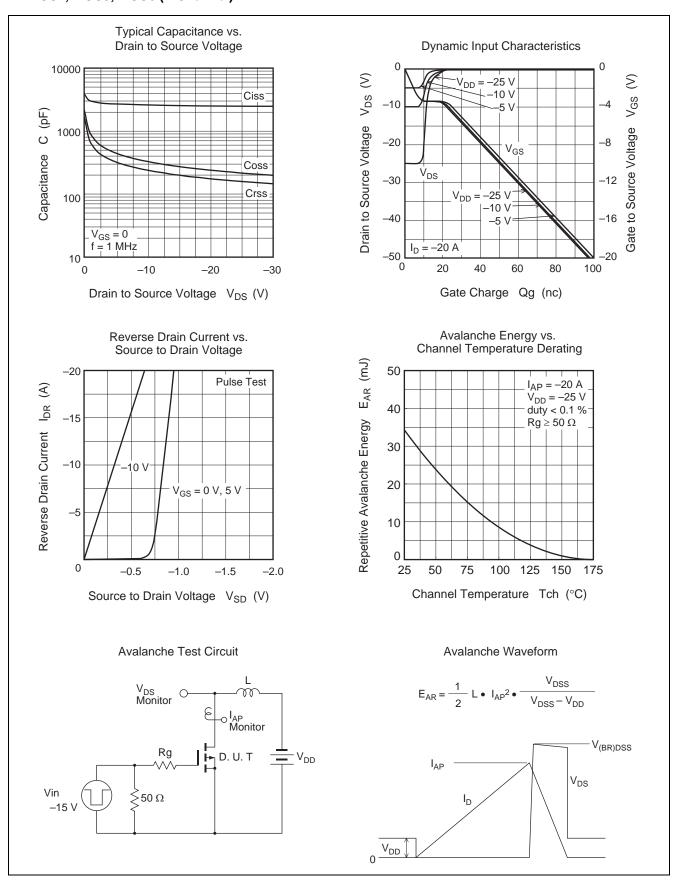
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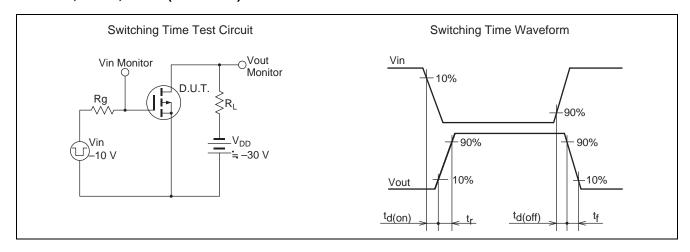
• MOS4, MOS5, MOS6 (P Channel)



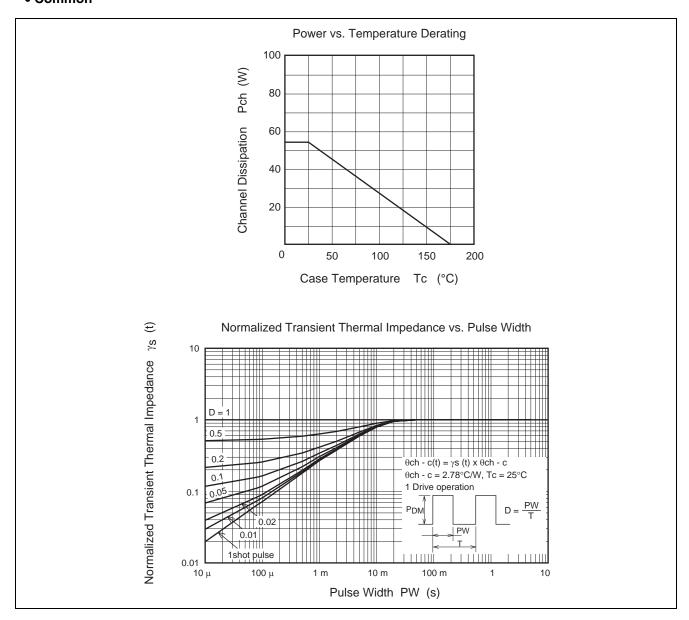
• MOS4, MOS5, MOS6 (P Channel)



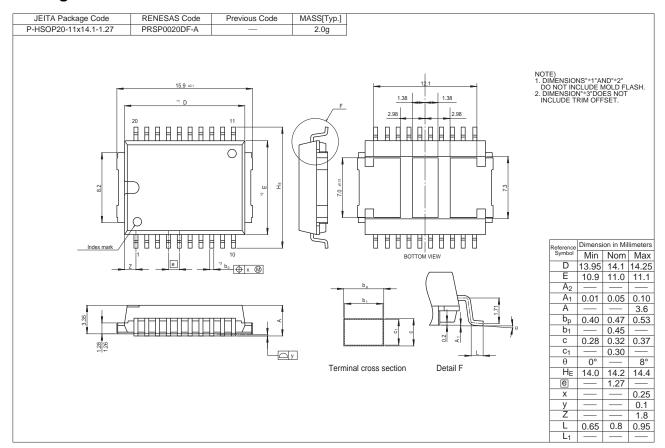
• MOS4, MOS5, MOS6 (P Channel)



• Common



Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJM0603JSC-00-12	700 pcs	Tray

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