



BT139-800E

4Q Triac

Rev. 04 — 24 March 2011

Product data sheet

1. Product profile

1.1 General description

Planar passivated sensitive gate four quadrant triac in a SOT404 (D2PAK) surface-mountable plastic package intended for use in general purpose bidirectional switching and phase control applications. This sensitive gate "series E" triac is intended to be interfaced directly to microcontrollers, logic integrated circuits and other low power gate trigger circuits.

1.2 Features and benefits

- Direct triggering from low power drivers and logic ICs
- High blocking voltage capability
- Planar passivated for voltage ruggedness and reliability
- Sensitive gate
- Triggering in all four quadrants

1.3 Applications

- General purpose phase control
- General purpose switching

1.4 Quick reference data

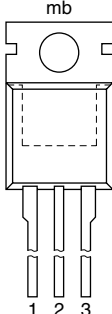

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	-	800	V
I_{TSM}	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25\text{ °C}$; $t_p = 20\text{ ms}$; see Figure 4 ; see Figure 5	-	-	155	A
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{mb} \leq 99\text{ °C}$; see Figure 1 ; see Figure 2 ; see Figure 3	-	-	16	A
Static characteristics						
I_{GT}	gate trigger current	$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2+ G+; $T_j = 25\text{ °C}$; see Figure 7	-	2.5	10	mA
		$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2+ G-; $T_j = 25\text{ °C}$; see Figure 7	-	4	10	mA
		$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2- G-; $T_j = 25\text{ °C}$; see Figure 7	-	5	10	mA
		$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2- G+; $T_j = 25\text{ °C}$; see Figure 7	-	11	25	mA



2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1		
2	T2	main terminal 2		
3	G	gate		
mb	T2	mounting base; main terminal 2		

SOT78 (TO-220AB)

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BT139-800E	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	800	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{mb} \leq 99\text{ }^{\circ}\text{C}$; see Figure 1 ; see Figure 2 ; see Figure 3	-	16	A
I_{TSM}	non-repetitive peak on-state current	full sine wave; $T_{j(\text{init})} = 25\text{ }^{\circ}\text{C}$; $t_p = 20\text{ ms}$; see Figure 4 ; see Figure 5	-	155	A
		full sine wave; $T_{j(\text{init})} = 25\text{ }^{\circ}\text{C}$; $t_p = 16.7\text{ ms}$	-	170	A
I^2t	I^2t for fusing	$t_p = 10\text{ ms}$; sine-wave pulse	-	120	A^2s
di_T/dt	rate of rise of on-state current	$I_T = 20\text{ A}$; $I_G = 0.2\text{ A}$; $dI_G/dt = 0.2\text{ A}/\mu\text{s}$; T2+ G+	-	50	$\text{A}/\mu\text{s}$
		$I_T = 20\text{ A}$; $I_G = 0.2\text{ A}$; $dI_G/dt = 0.2\text{ A}/\mu\text{s}$; T2+ G-	-	50	$\text{A}/\mu\text{s}$
		$I_T = 20\text{ A}$; $I_G = 0.2\text{ A}$; $dI_G/dt = 0.2\text{ A}/\mu\text{s}$; T2- G-	-	50	$\text{A}/\mu\text{s}$
		$I_T = 20\text{ A}$; $I_G = 0.2\text{ A}$; $dI_G/dt = 0.2\text{ A}/\mu\text{s}$; T2- G+	-	10	$\text{A}/\mu\text{s}$
I_{GM}	peak gate current		-	2	A
V_{GM}	peak gate voltage		-	5	V
P_{GM}	peak gate power		-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	-	0.5	W
T_{stg}	storage temperature		-40	150	$^{\circ}\text{C}$
T_j	junction temperature		-	125	$^{\circ}\text{C}$

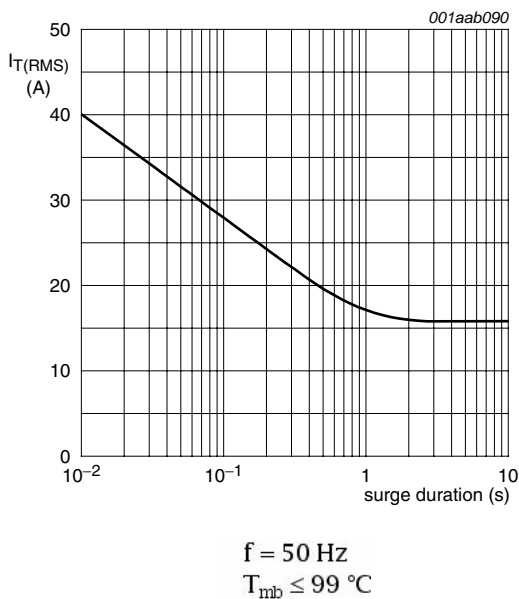


Fig 1. RMS on-state current as a function of surge duration; maximum values

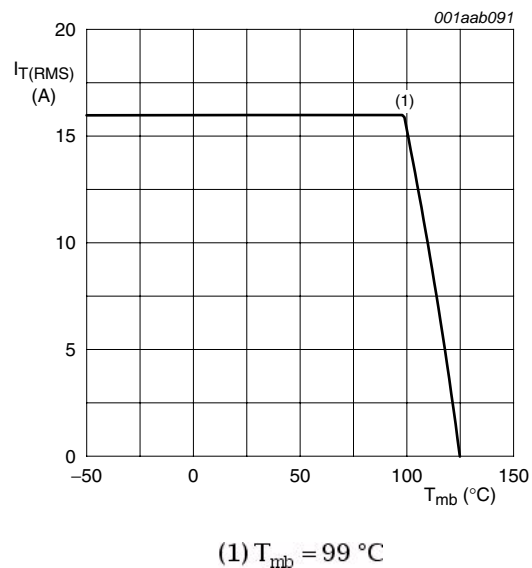


Fig 2. RMS on-state current as a function of mounting base temperature; maximum values

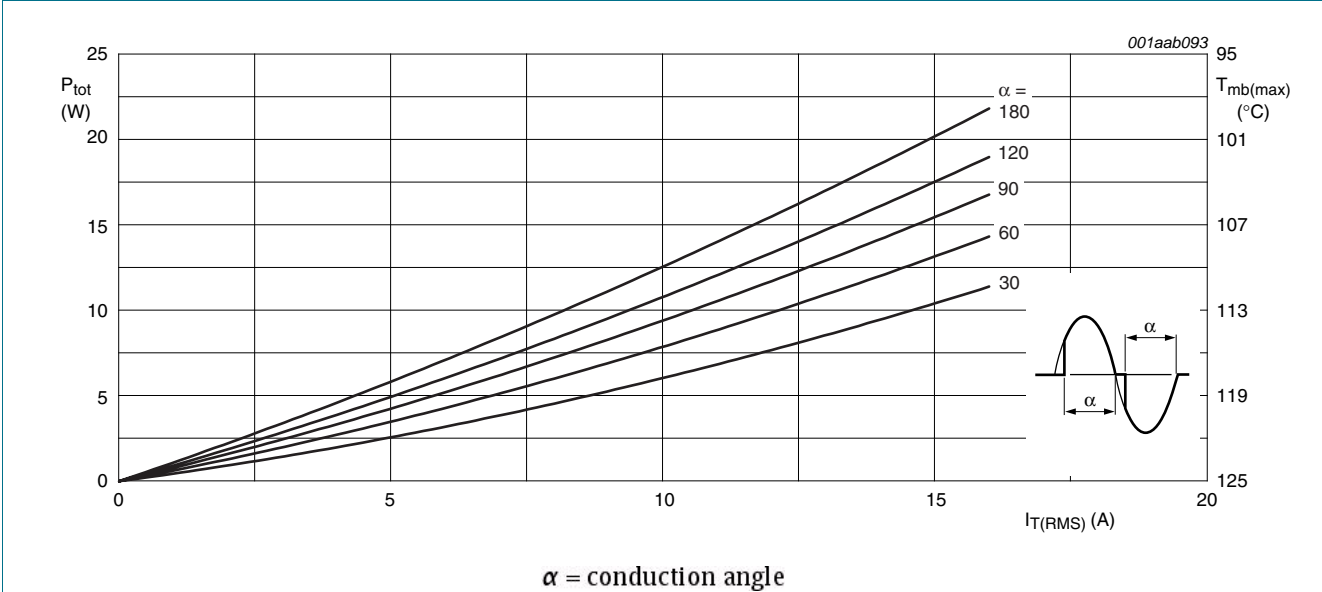


Fig 3. Total power dissipation as a function of RMS on-state current; maximum values.

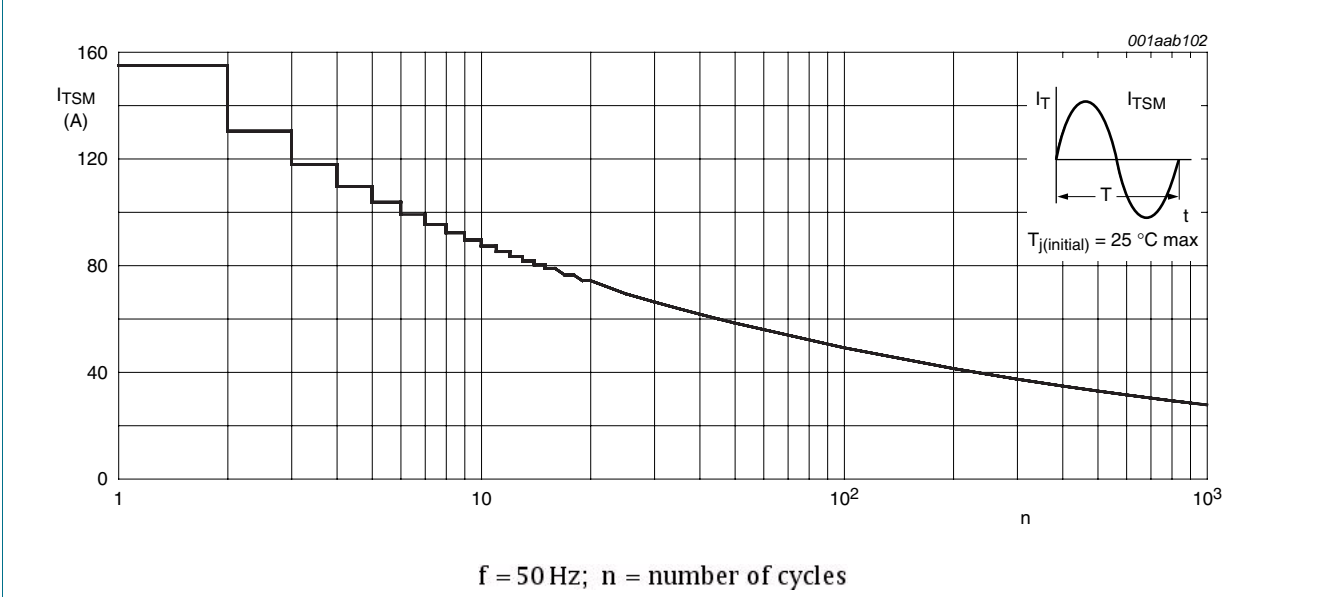
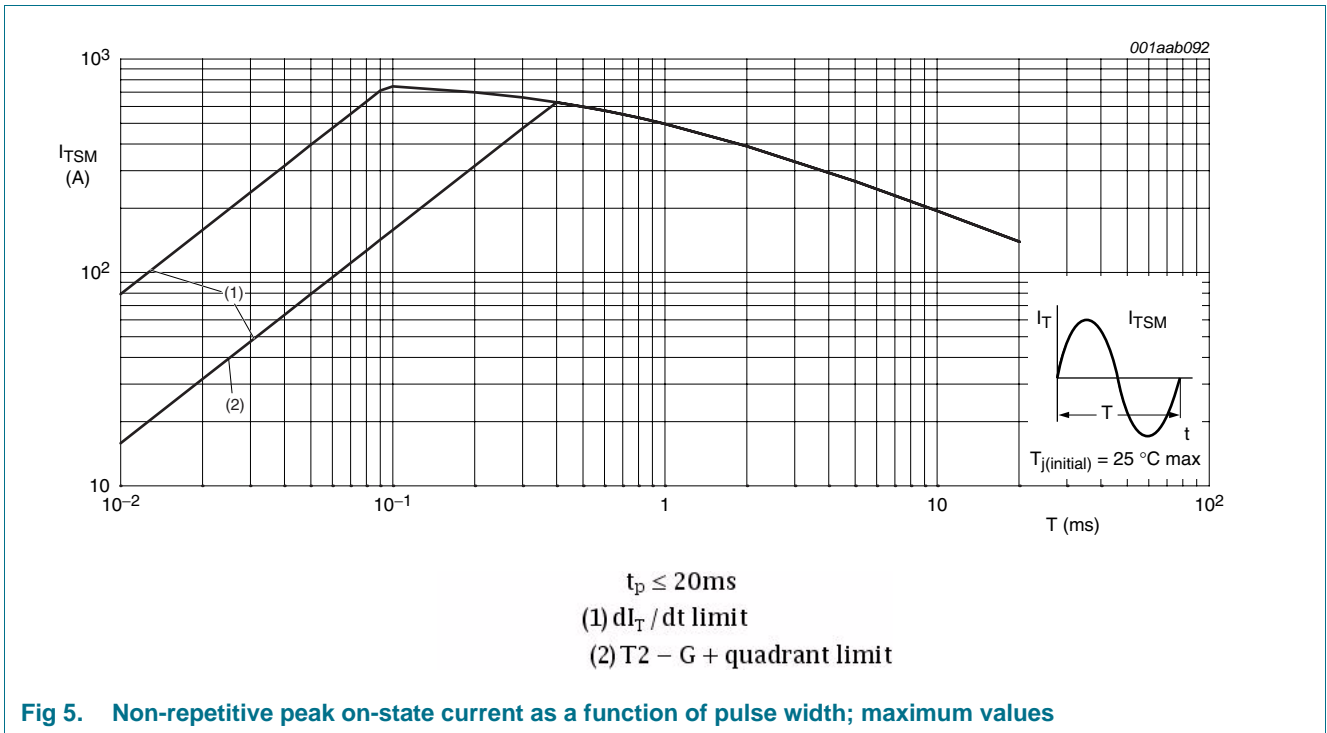


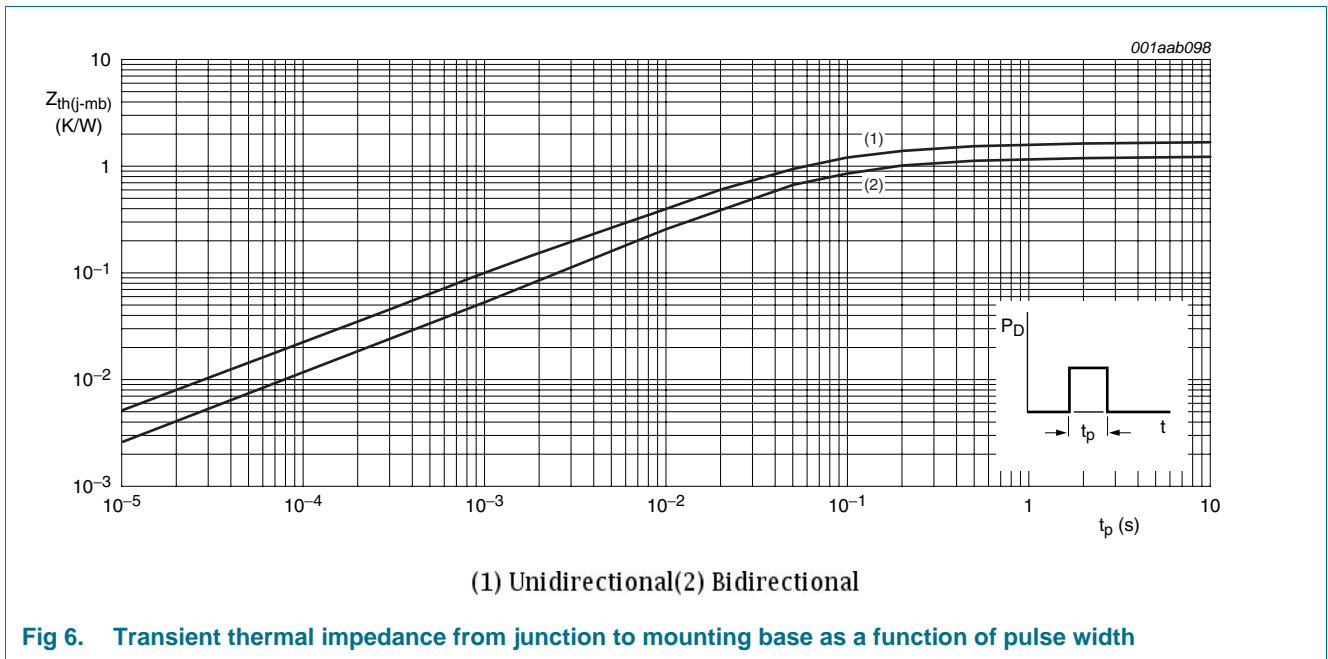
Fig 4. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values



5. Thermal characteristics

Table 5. Thermal characteristics

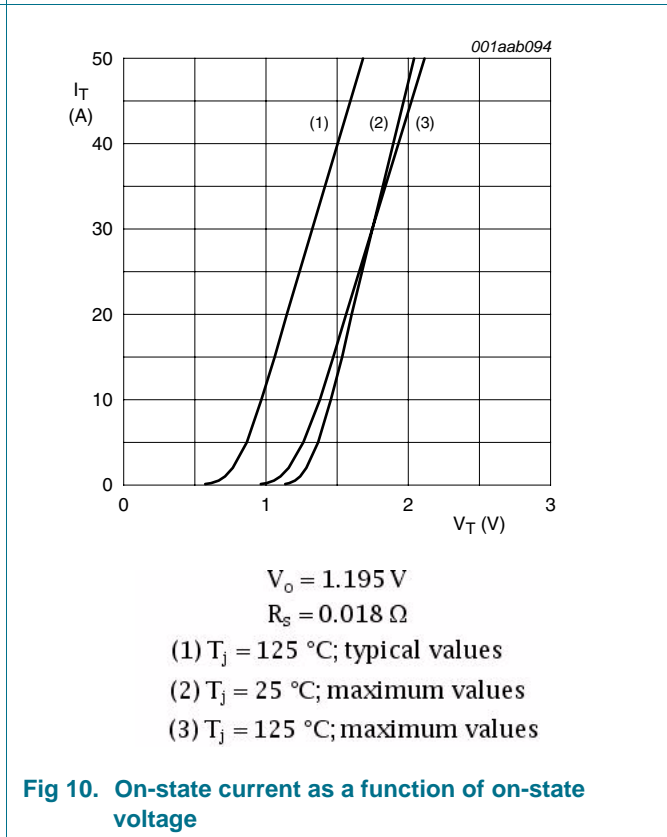
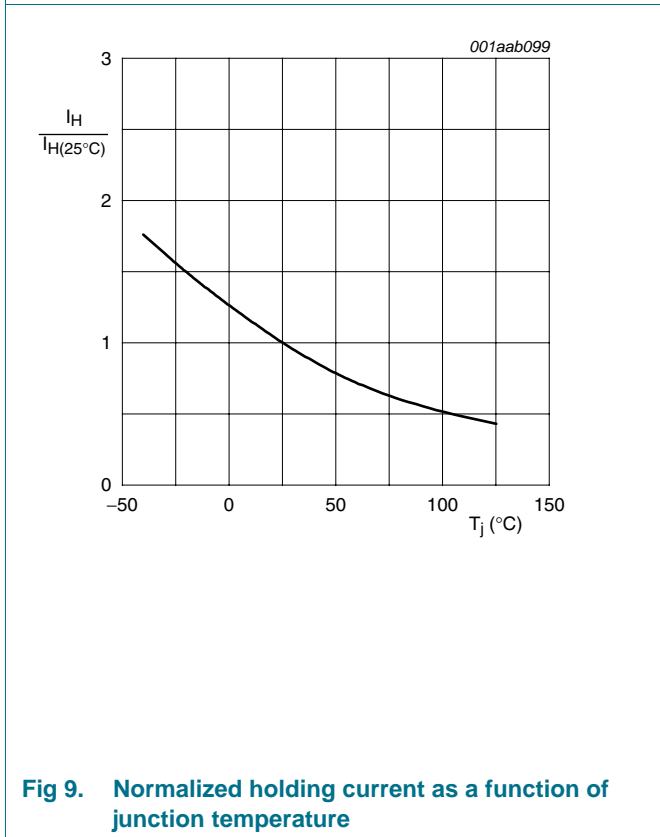
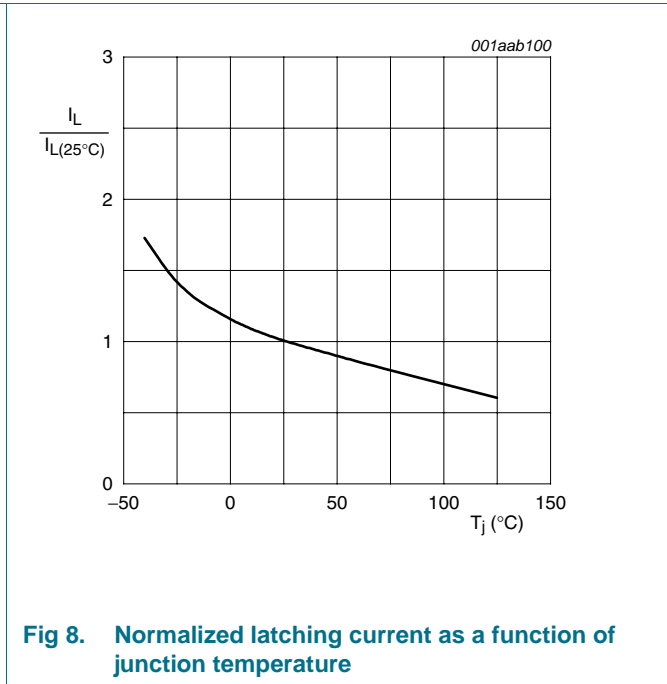
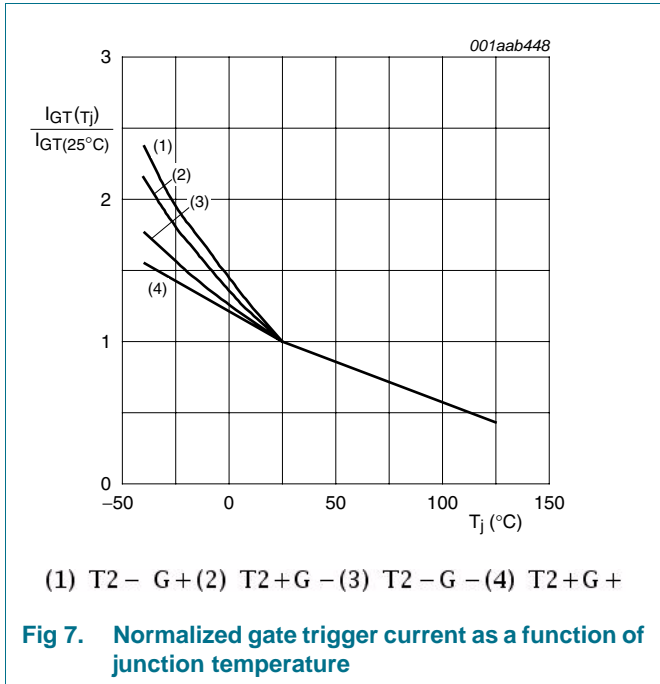
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	half cycle; see Figure 6	-	-	1.7	K/W
		full cycle; see Figure 6	-	-	1.2	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	-	60	-	K/W



6. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
I_{GT}	gate trigger current	$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2+ G+; $T_j = 25\text{ °C}$; see Figure 7	-	2.5	10	mA
		$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2+ G-; $T_j = 25\text{ °C}$; see Figure 7	-	4	10	mA
		$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2- G-; $T_j = 25\text{ °C}$; see Figure 7	-	5	10	mA
		$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2- G+; $T_j = 25\text{ °C}$; see Figure 7	-	11	25	mA
I_L	latching current	$V_D = 12\text{ V}$; $I_G = 0.1\text{ A}$; T2+ G+; $T_j = 25\text{ °C}$; see Figure 8	-	3.2	30	mA
		$V_D = 12\text{ V}$; $I_G = 0.1\text{ A}$; T2+ G-; $T_j = 25\text{ °C}$; see Figure 8	-	16	40	mA
		$V_D = 12\text{ V}$; $I_G = 0.1\text{ A}$; T2- G-; $T_j = 25\text{ °C}$; see Figure 8	-	4	30	mA
		$V_D = 12\text{ V}$; $I_G = 0.1\text{ A}$; T2- G+; $T_j = 25\text{ °C}$; see Figure 8	-	5.5	40	mA
I_H	holding current	$V_D = 12\text{ V}$; $T_j = 25\text{ °C}$; see Figure 9	-	4	45	mA
V_T	on-state voltage	$I_T = 20\text{ A}$; $T_j = 25\text{ °C}$; see Figure 10	-	1.2	1.6	V
V_{GT}	gate trigger voltage	$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; $T_j = 25\text{ °C}$; see Figure 11	-	0.7	1.5	V
		$V_D = 400\text{ V}$; $I_T = 0.1\text{ A}$; $T_j = 125\text{ °C}$; see Figure 11	0.25	0.4	-	V
I_D	off-state current	$V_D = 800\text{ V}$; $T_j = 125\text{ °C}$	-	0.1	0.5	mA
Dynamic characteristics						
dV_D/dt	rate of rise of off-state voltage	$V_{DM} = 536\text{ V}$; $T_j = 125\text{ °C}$; exponential waveform; gate open circuit	-	50	-	V/ μ s
t_{gt}	gate-controlled turn-on time	$I_{TM} = 20\text{ A}$; $V_D = 800\text{ V}$; $I_G = 0.1\text{ A}$; $di_G/dt = 5\text{ A}/\mu$ s	-	2	-	μ s



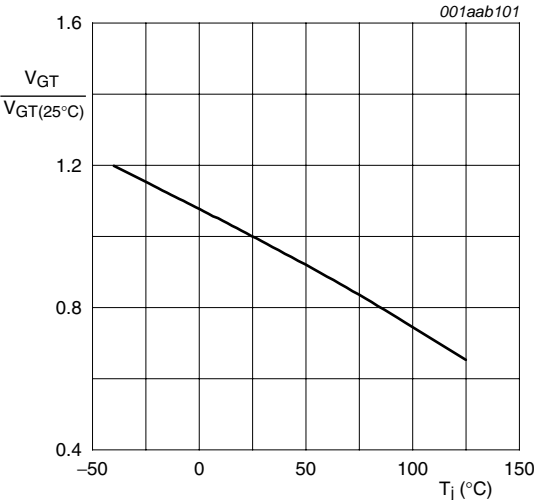


Fig 11. Normalized gate trigger voltage as a function of junction temperature

7. Package outline

Plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB

SOT78

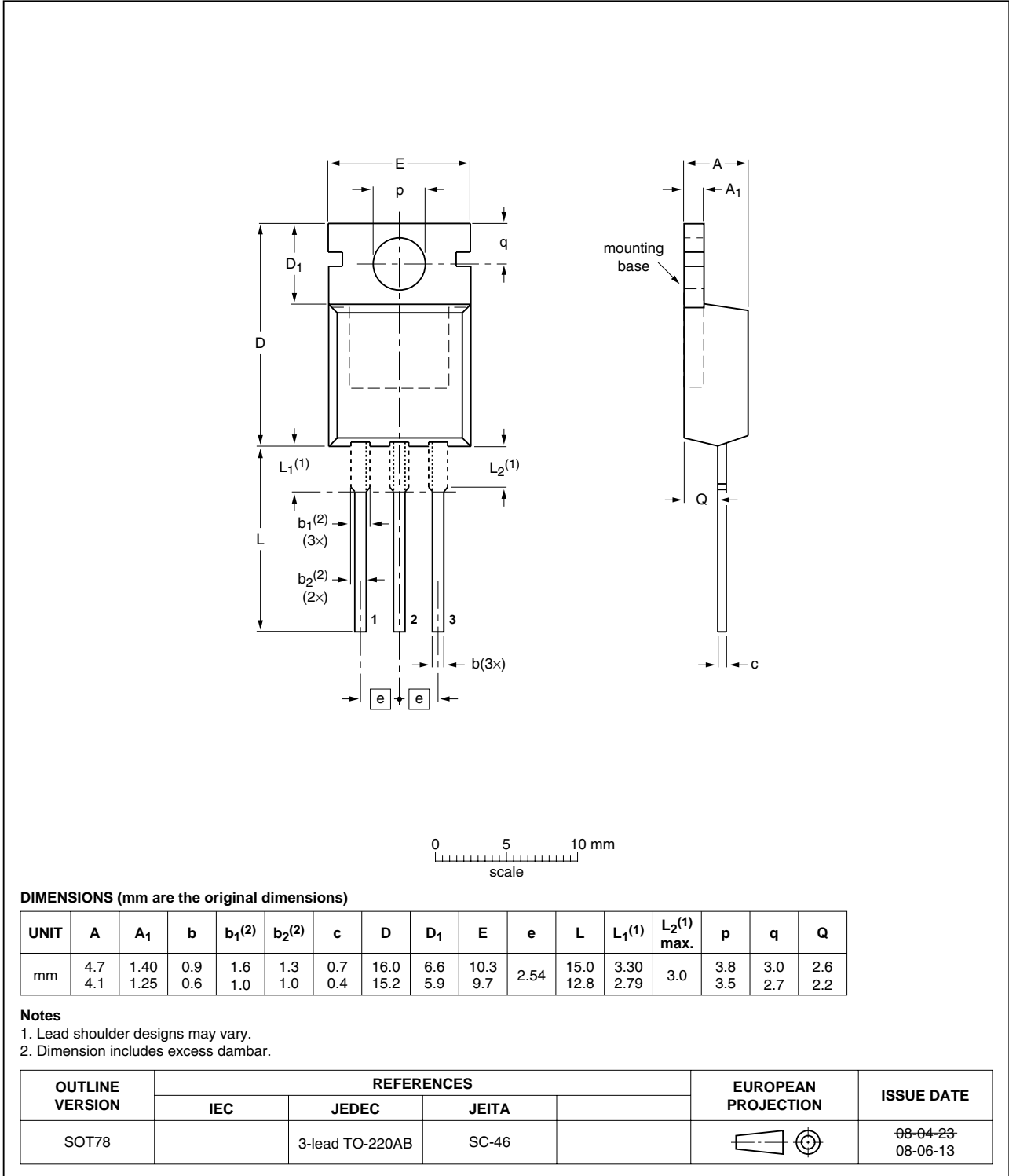


Fig 12. Package outline SOT78 (TO-220AB)

8. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BT139-800E v.4	20110324	Product data sheet	-	BT139_SER_E v.3
Modifications:	<ul style="list-style-type: none">• The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors.• Legal texts have been adapted to the new company name where appropriate.• Type number BT139-800E separated from data sheet BT139_SER_E v.3.			
BT139_SER_E v.3	20040923	Product data sheet	-	BT139_SERIES_E v.2

9. Legal information

9.1 Data sheet status

Document status ^[1] ^[2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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