Rectifier Diode



DS5987 - 1 January 2011 (LN28008)

FEATURES

- Double Side Cooling
- High Surge Capability

KEY PARAMETERS

V_{RRM}	6000V
I _{F(AV)}	630A
I _{FSM}	10500A

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V _{RRM} V	Conditions
DRD630G60 DRD630G58 DRD630G56 DRD630G52	6000 5800 5600 5200	$V_{RSM} = V_{RRM} + 100V$

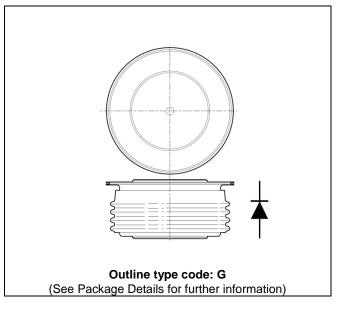


Fig. 1 Package outline

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DRD630G56 for a 5600V device



CURRENT RATINGS

$T_{case} = 75$ °C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units	
Double Si	de Cooled			l	
I _{F(AV)}	Mean forward current	Half wave resistive load	811	А	
I _{F(RMS)}	RMS value	-	1274	Α	
I _F	Continuous (direct) on-state current	-	1172	Α	
Single Sid	Single Side Cooled (Anode side)				
I _{F(AV)}	Mean forward current	Half wave resistive load	534	Α	
I _{F(RMS)}	RMS value	-	839	Α	
I _F	Continuous (direct) on-state current	-	727	Α	

T_{case} = 100°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Si	de Cooled			
I _{F(AV)}	Mean forward current	Half wave resistive load	630	А
I _{F(RMS)}	RMS value	-	990	Α
I _F	Continuous (direct) on-state current	-	900	Α
Single Sig	le Cooled (Anode side)			
I _{F(AV)}	Mean forward current	Half wave resistive load	410	Α
I _{F(RMS)}	RMS value	-	644	Α
l _F	Continuous (direct) on-state current	-	550	А



SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I _{FSM}	Surge (non-repetitive) on-state current	10ms half sine, T _{case} = 150°C	8.5	kA
l ² t	I ² t for fusing	$V_R = 50\% V_{RRM} - \frac{1}{4}$ sine	0.36	MA ² s
I _{FSM}	Surge (non-repetitive) on-state current	10ms half sine, T _{case} = 150°C	10.5	kA
l ² t	I ² t for fusing	$V_R = 0$	0.565	MA ² s

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions		Min.	Max.	Units
R _{th(j-c)}	Thermal resistance – junction to case	Double side cooled	DC	-	0.032	°C/W
		Single side cooled	Anode DC	-	0.064	°C/W
			Cathode DC	-	0.064	°C/W
R _{th(c-h)}	Thermal resistance – case to heatsink	Clamping force 12kN	Double side	-	0.008	°C/W
		(with mounting compound)	Single side	-	0.016	°C/W
T_{vj}	Virtual junction temperature	On-state (conducting)		-	160	°C
		Reverse (blocking)		-	150	°C
T _{stg}	Storage temperature range			-55	175	°C
Fm	Clamping force			11.5	13.5	kN

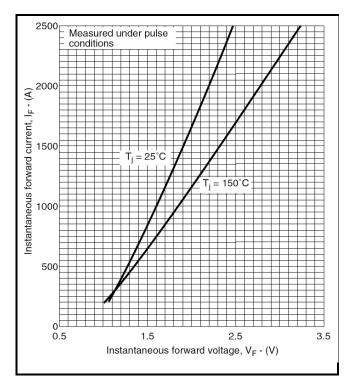
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CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
V _{FM}	Forward voltage	At 1800A peak, T _{case} = 25°C	-	2.1	V
I _{RM}	Peak reverse current	At V _{RRM} , T _{case} = 150°C	-	75	mA
Qs	Total stored charge	I _F = 1000A, dI _{RR} /dt =3A/μs	-	3000	μC
Irr	Peak reverse recovery current	$T_{case} = 150$ °C, $V_R = 100$ V	-	90	Α
V _{TO}	Threshold voltage	At T _{vj} = 150°C	-	0.9	V
r _T	Slope resistance	At T _{vj} = 150°C	-	0.93	mΩ

CURVES



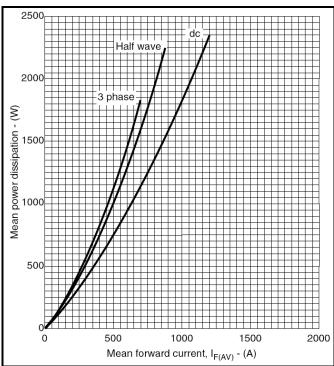


Fig.2 Maximum (limit) on-state characteristics

Fig.3 Dissipation curves

 V_{TM} EQUATION

 $V_{TM} = A + Bln (I_T) + C.I_T + D.\sqrt{I_T}$

Where A = 1.249986

B = -0.17646

C = 0.000524

D = 0.041024

these values are valid for $T_j = 150$ °C for $I_F 500$ A to 2500A



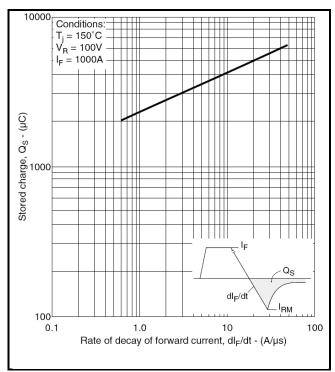


Fig.4 Total stored charge

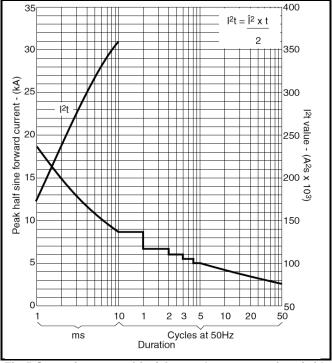


Fig.5 Surge (non-repetitive) forward current vs time (with $50\%~V_{\text{RRM}}$ at $T_{\text{case}}~150^{\circ}\text{C})$

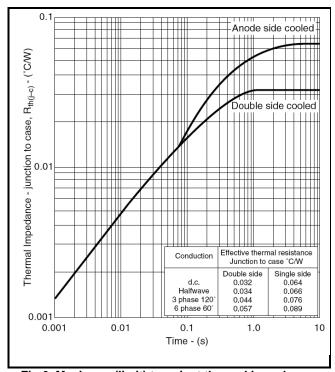
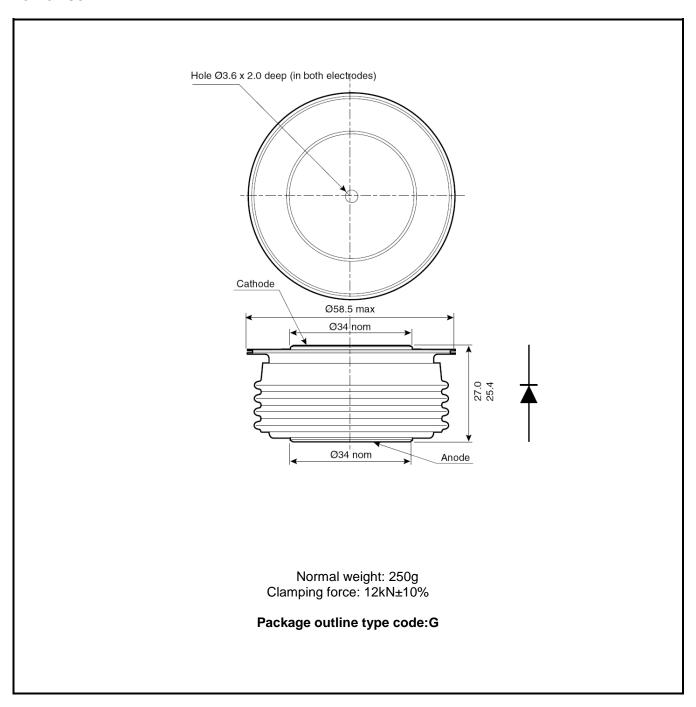


Fig.6 Maximum (limit) transient thermal impedancejunction to case



PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



Note:

Some packages may be supplied with gate and or tags.



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