Analog Power

P-Channel 30-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low $r_{DS(on)}$ and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low r_{DS(on)} provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe SOIC-8PP saves board space
- Fast switching speed
- High performance trench technology

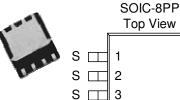
PRODUCT SUMMARY				
V _{DS} (V)	$r_{DS(on)} m(\Omega) \qquad I_D (A)$			
-30	9 @ $V_{GS} = -10V$	-20		
	$13 @ V_{GS} = -4.5V$	-17		

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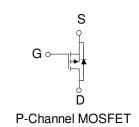
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ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Maximum	Units	
Drain-Source Voltage			-30	v	
Gate-Source Voltage			±20	v	
Continuous Drain Current ^a	T _A =25°C	T_	-20		
	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	тр	-16	А	
Pulsed Drain Current ^b			±50		
Continuous Source Current (Diode Conduction) ^a		Is	-2.1	А	
	T _A =25°C	PD	5.0	w	
Power Dissipation ^a	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	гD	3.2	**	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Maximum	Units		
a a a a a a	t <= 10 sec	Reja	25	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		65	°C/W	

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

b. Pulse width limited by maximum junction temperature

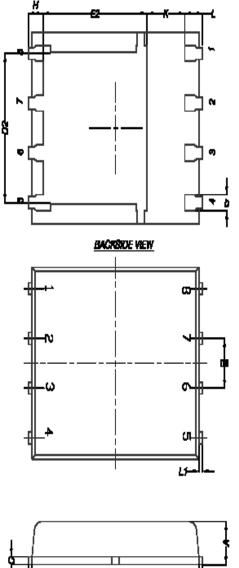
Devemeter	Complete Tast Canditians	Limits			11		
Parameter	Symbol	Symbol Test Conditions		Тур	Max	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 V, I_{D} = -250 uA$	-30			v	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \text{ uA}$	-1				
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0$ V, $V_{GS} = \pm 25$ V			±100	nA	
Zero Gate Voltage Drain Current	lace	$V_{DS} = -24 V, V_{GS} = 0 V$			-1	uA	
Zelo Gale Voltage Diam Guirent	I _{DSS}	$V_{DS} = -24 V, V_{GS} = 0 V, T_{J} = 55^{\circ}C$			-5		
On-State Drain Current ^A	I _{D(on)}	$V_{DS}=\text{-5 V},\;V_{GS}=\text{-10 V}$	-50			А	
Drain-Source On-Resistance ^A	r _{DS(on)}	V_{GS} = -10 V, I_D = -2 A			9	mΩ	
Drain-Source On-Resistance		V_{GS} = -4.5 V, I _D = -2 A			13		
Forward Tranconductance ^A	g _{fs}	V_{DS} = -15 V, I_D = -2 A		29		S	
Diode Forward Voltage	V _{SD}	$I_{S} = 2 A, V_{GS} = 0 V$		-0.8		V	
Dynamic ^b							
Total Gate Charge	Qg	V _{DS} = -15 V, V _{GS} = -5 V,		25			
Gate-Source Charge	Q _{gs}	$V_{DS} = -15 V, V_{GS} = -5 V,$ $I_D = -2 A$		11		nC	
Gate-Drain Charge	Q _{gd}	$I_{\rm D} = -2$ A		17			
Turn-On Delay Time	t _{d(on)}			15			
Rise Time	t _r	V_{DD} = -15 V, R_{L} = 6 Ω ,		13		nS	
Turn-Off Delay Time	t _{d(off)}	ID = -1 A, VGEN = -10 V		100			
Fall-Time	t _f			54		1	

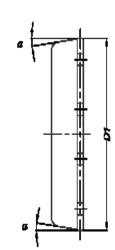
Notes

- a. Pulse test: $PW \le 300$ us duty cycle $\le 2\%$.
- b. Guaranteed by design, not subject to production testing.

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Package Information





DM.	MRLUMETERS			
	MON.	NOM.	MAX	
A	020	1.00	1.10	
Þ	0.33	0.41	0.61	
C	0.20	0.25	0.30	
D1	4.80	4.90	5.00	
D2	381	8.81	2.98	
Ε	5.90	6.00	6.10	
Ef	5.70	6.76	5.80	
E 2	3.96	3.58	278	
0	1.27 BSC			
H	Q.41	0.61	0.81	
ĸ	1.10	-	•	
L	0.51	0.67	0.71	
L1	0.06	Q.13	0.20	
a	۴	-	12*	

