

Features

- PT8A995 works as encoder and PT8A9961/9962/9963/9964 work as decoders
- Supply voltage: 3.3V to 6.0V
- Internal auto-shutdown function
- Internal over-load protection to meet HD271 safety specification (996x)
- Integrated RF circuit (995)
- Two analog channels, one for tachospeed and the other for steering
- 32 steps for each of two analog channels
- Fine tuning selection for two analog channels
- Few external components needed
- Closed loop adjustment for servo motor (996x)
- Built-in voltage regulator (996x)
- Package: 16-pin DIP (995P), 20-pin DIP (996xP), 20-pin SOIC (996xS), see Ordering Information.

Applications

- Remote Controller
- Toys
- Remote Measurement

Ordering Information

Part Number	Package
PT8A995P	16-pin DIP
PT8A9961P/9962P/9963P/9964P	20-pin DIP
PT8A9961S/9962S/9963S/9964S	20-pin SOIC

General Description

The PT8A995 and PT8A996x provide a simple and economic solution for basic Digital Proportional R/C car toys. The chipset uses CMOS LSI Analog and Mixed Signal technology. They provide high quality encoder/decoder circuit.

PT8A995 severs as an encoder. To obtain accurate timing easily, Manchester encoding is employed. Some digital modulation scheme can be used, e.g., general ASK, OOK, FSK and QPSK. OOK is easy to implement and low cost.

The PT8A996x provides a simple and cost effective solution for basic Digital Proportional R/C car toys with PT8A995 together. RF circuit in receiver side can demodulate RF signal. The demodulated signal will be amplified and filtered. After that, the base-band signal is formed. DPLL is adopted to generate sampling clock. An internal A/D converter is required to make servo motor work well. The inputs are SRV and OFFSET. Over-load protection mechanism is built-in to meet Toy Safety Requirement.

The PT8A996x provide four solutions for toy car by different capability Servo.

PT8A9961	PT8A9962	PT8A9963	PT8A9964
mini car	small car	middle car	large car

Block Diagram

Figure 1. Block Diagram of PT8A995

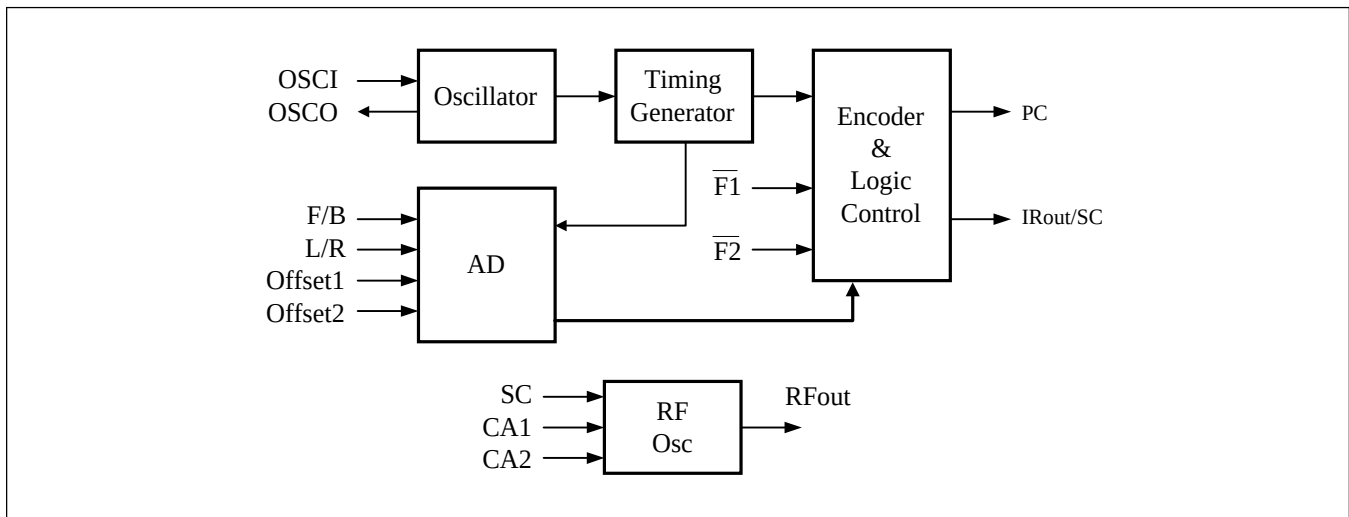
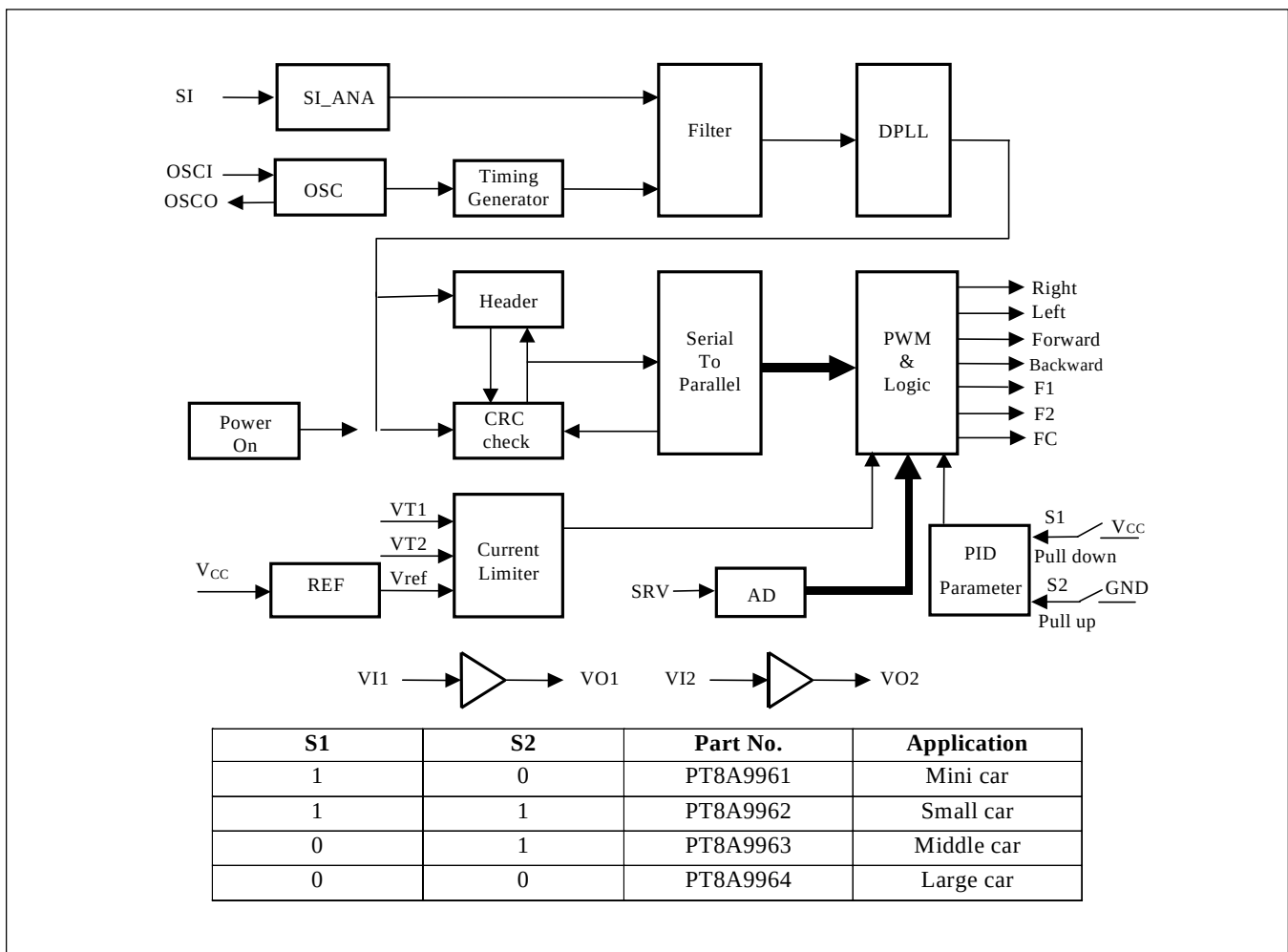
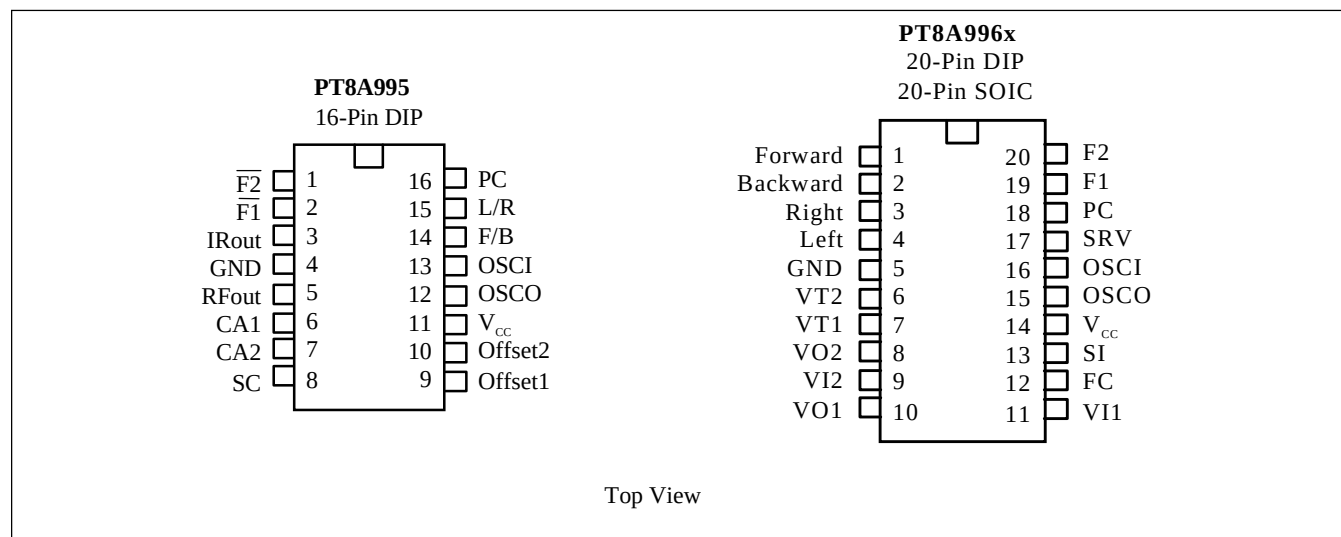


Figure 2. Block Diagram of PT8A9961/9962/9963/9964



Pin Configuration



Pin Description

Table 1. Pin Description of PT8A995

Pin No.	Pin name	Description
1	$\overline{F2}$	F2 function control pin
2	$\overline{F1}$	F1 function control pin
3	IRout	Output code with 38KHz carries
4	GND	Ground
5	RFout	Output code with 27M or 49MHz carries
6	CA1	27M or 49MHz crystal oscillator input
7	CA2	27M or 49MHz crystal oscillator output
8	SC	Output code
9	Offset1	Used for offset adjustment of speed input
10	Offset2	Used for offset adjustment of steering input
11	V_{CC}	power
12	OSCO	Crystal oscillator output
13	OSCI	Crystal oscillator input
14	F/B	Input for forward & backward speed
15	L/R	Input for steering angle left & right
16	PC	Power control output

Table 2. Pin Description of PT8A9961/9962/9963/9964

Pin No.	Pin Name	Description
1	Forward	Forward output
2	Backward	Backward output
3	Right	Right output
4	Left	Left output
5	GND	GND
6	VT2	F/B motor current limit protection input
7	VT1	L/R motor current limit protection input
8	VO2	The second stage amplify output
9	VI2	The second stage amplify input
10	VO1	The first stage amplify output
11	VI1	The first stage amplify input
12	FC	Futaba connection output
13	SI	Encode signal input
14	V _{cc}	Power
15	OSCO	Crystal oscillator output
16	OSCI	Crystal oscillator input
17	SRV	Servo motor feedback input
18	PC	Auto power off control
19	F1	Function1 control
20	F2	Function2 control

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested)

Storage Temperature	-25°C to +85°C
Ambient Temperature with Power Applied	0°C to +70°C
Supply Voltage to Ground Potential (Inputs & V _{cc} Only)	-0.5 to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only)	-0.5 to +7.0V
DC Input Voltage	-0.5 to +7.0V
DC Output Current	20mA
Power Dissipation	500mW

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Recommended Operation Conditions

Sym	Description	Test Conditions	Min	Typ	Max	Units
V_{CC}	Supply Voltage	-	4.0	-	5.5	V
V_{IH}	Input HIGH Voltage	-	$0.7V_{CC}$	-	V_{CC}	V
V_{IL}	Input LOW Voltage	-	0	-	$0.3V_{CC}$	V
F_{OSC}	Oscillator Frequency	-	400	455	500	kHz
T_A	Operation Temperature	-	0	-	70	°C

DC Electrical Characteristics

Sym	Description	Pin	Test Conditions	Min	Typ	Max	Units
I_{CC}	Supply Current		Clock = 455kHz, outputs unloaded	-	-	3	mA
I_{STB}	Standby Current		-	-	-	500	uA
I_{OH}	Output HIGH Current	Function pins	$V_{IN} = V_{CC} - 0.5V$	-8	-	-	mA
	Output HIGH Current	PC		-15	-	-	mA
I_{OL}	Output LOW Current	Function pins	$V_{OUT} = 0.5V$	8	-	-	mA
	Output LOW Current	PC		-	-	60	uA
I_{IH}	Input HIGH Current	VT1, VT2	$V_{IN} = V_{CC}$	-	-	60	uA
	Input HIGH Current	SI, OSC1		-	-	10	uA
	Input HIGH Current	Other input pins		-	-	1	uA
I_{IL}	Input LOW Current	SI, OSC1	$V_{IN} = 0V$	-	-	-10	uA
	Input LOW Current	Other input pins		-	-	-1	uA

Note: These specifications apply for $V_{CC} = 5.0V$ and $0^{\circ}C \leq T_A \leq 70^{\circ}C$, unless otherwise specified.

AC Electrical Characteristics

Sym	Description	Test Conditions	Min	Typ	Max	Units
f_{OSC}	Oscillator Frequency	$R_f = 200k\Omega$	400	455	500	kHz
f_t	Frequency Tolerance	-	-	-	1.5	%
t_R	Response Time*	$f_{OSC} = 455kHz$	-	-	90	ms
R_I	Inverter Input Resistance	Apply for VI1 & VI2, $R_f = 2.2M\Omega$	50	-	-	k Ω
R_O	Inverter Output Resistance	Apply for VO1 & VO2	-	-	10	k Ω
V_T	Overload Protection	-	0.3	0.4	0.5	V

Note: These specifications apply for $V_{CC} = 5.0V$ and $0^{\circ}C \leq T_A \leq 70^{\circ}C$, unless otherwise specified.

* The time from the transmitter's control to the receiver's response.

Figure 3. Application of PT8A995 with External Modulation

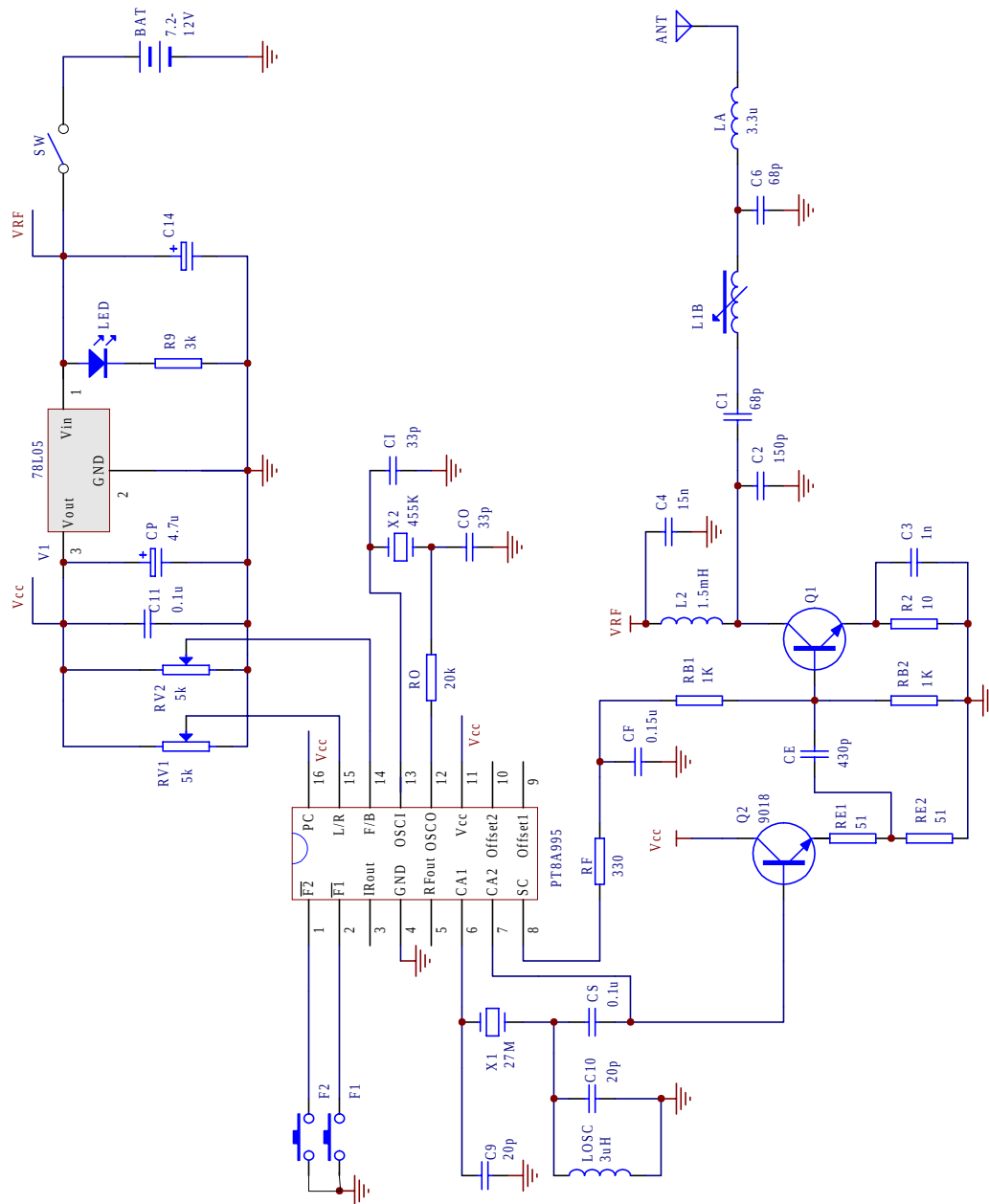


Figure 4. Futaba-S3003 Servo Application Circuit of PT8A9961/9962/9963/9964

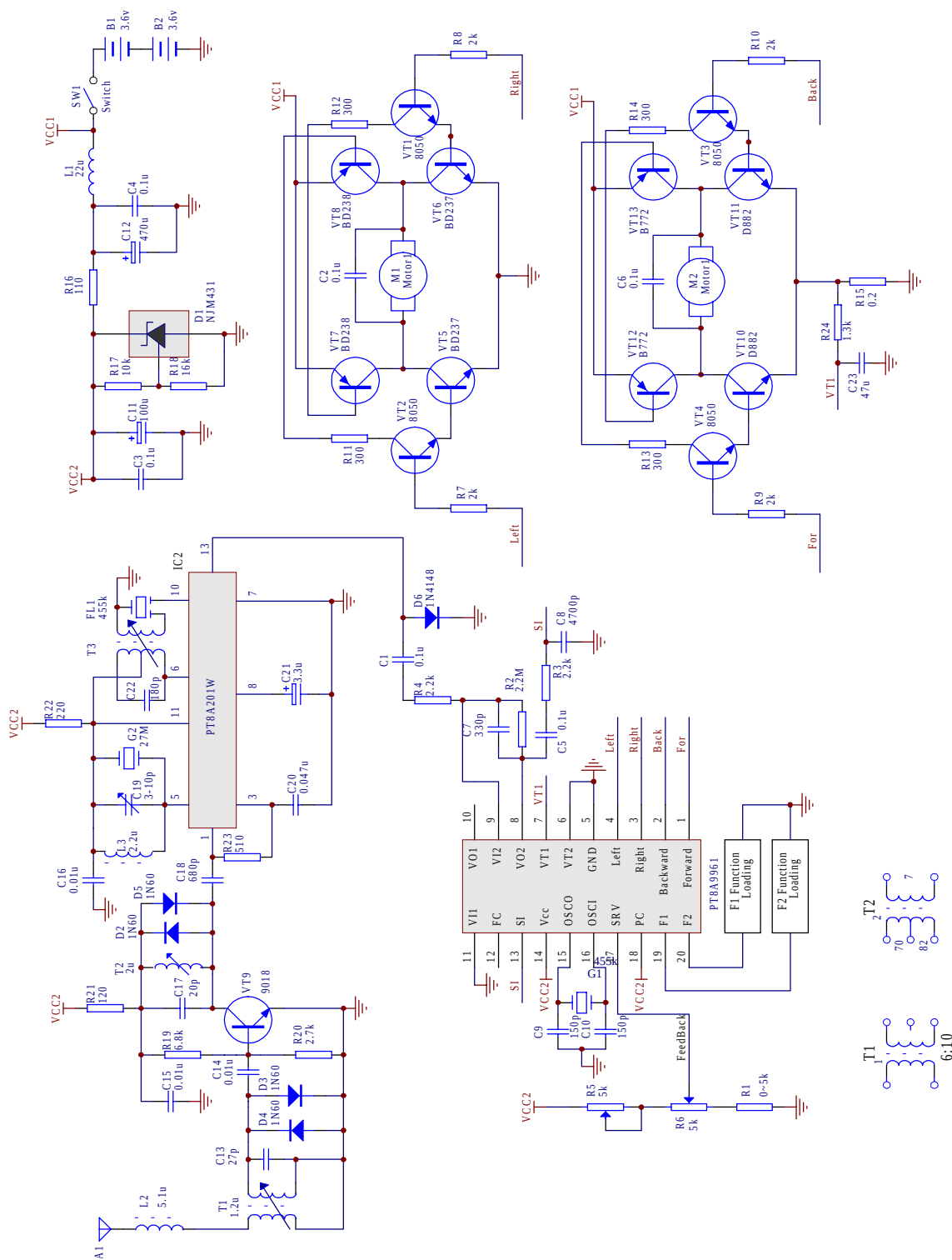
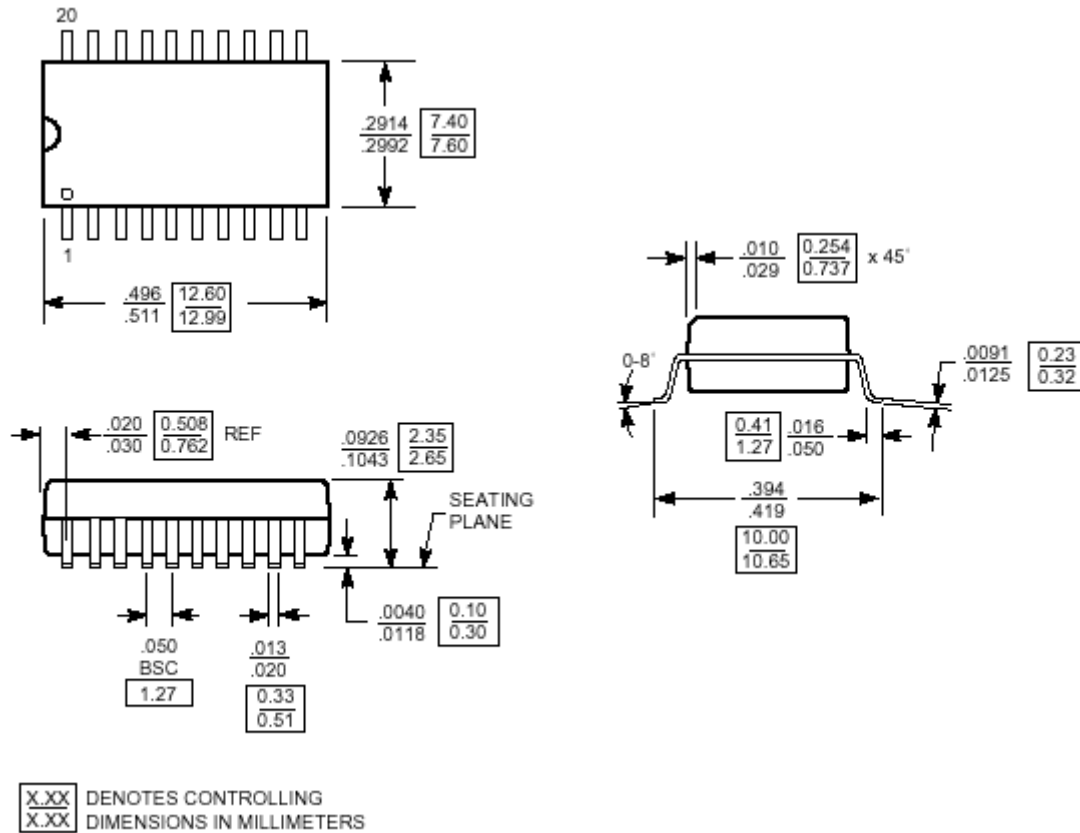


Figure 5. Mechanical Diagram of 20-pin SOIC



**Notes**

Pericom Technology Inc.

Email: support@pti.com.cn Web Site: www.pti.com.cn, www.pti-ic.com

China: No. 20 Building, 3/F, 481 Guiping Road, Shanghai, 200233, China
Tel: (86)-21-6485 0576 Fax: (86)-21-6485 2181**Asia Pacific:** Unit 1517, 15/F, Chevalier Commercial Centre, 8 Wang Hoi Rd, Kowloon Bay, Hongkong
Tel: (852)-2243 3660 Fax: (852)-2243 3667**U.S.A.:** 2380 Bering Drive, San Jose, California 95131, USA
Tel: (1)-408-435 0800 Fax: (1)-408-435 1100

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