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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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semiconductors may lead to personal injury, fire or property damage.
 Remember to give due consideration to safety when making your circuit designs, with appropriate
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(iii) prevention against any malfunction or mishap.

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Silicon N Channel Power MOS FET High Speed Power Switching

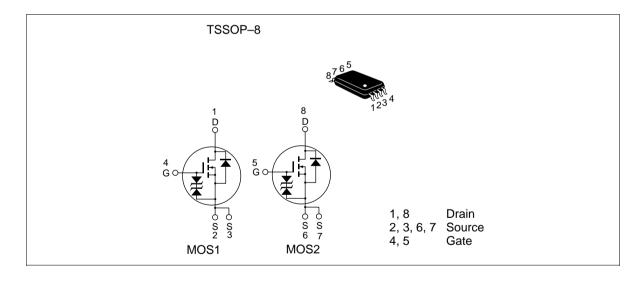


ADE-208-660A (Z) 2nd. Edition Feb. 1999

Features

- Low on-resistance
- Capable of 4 V gate drive
- Low drive current
- High density mounting

Outline



Absolute Maximum Ratings $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	100	V
Gate to source voltage	$V_{\sf GSS}$	± 20	V
Drain current	I _D	1	A
Drain peak current	Note1	4	A
Body-drain diode reverse drain current	I _{DR}	1	A
Channel dissipation	Pch Note2	1.0	W
Channel dissipation	Pch Note3	1.5	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	- 55 to + 150	°C

Note: 1. PW \leq 10 μ s, duty cycle \leq 1 %

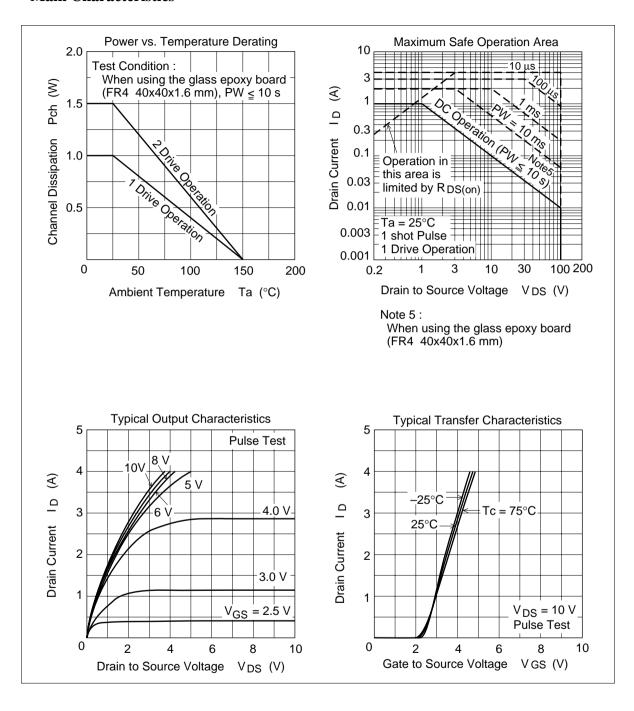
- 2. 1 Drive operation : When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW \leq 10s
- 3. 2 Drive operation : When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW≤ 10s

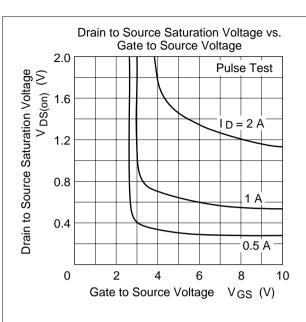
Electrical Characteristics ($Ta = 25^{\circ}C$)

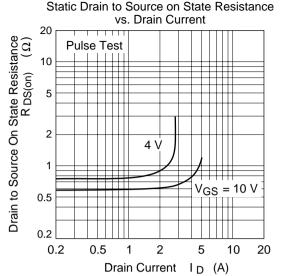
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	100	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	± 20	_	_	V	$I_{G} = \pm 100 \mu\text{A}, V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	± 10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltege drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 100 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.3	_	2.3	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	0.56	0.75	Ω	$I_D = 0.5 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
resistance	R _{DS(on)}	_	0.72	1.0	Ω	$I_D = 0.5 \text{ A}, V_{GS} = 4 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}	0.7	1.1	_	S	$I_D = 0.5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	90	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	42	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	20	_	pF	f = 1MHz
Turn-on delay time	t _{d(on)}	_	11	_	ns	$V_{GS} = 4 \text{ V}, I_{D} = 0.5 \text{ A}$
Rise time	t _r	_	24	_	ns	$V_{DD} \cong 10 \text{ V}$
Turn-off delay time	t _{d(off)}	_	14	_	ns	
Fall time	t _f	_	11	_	ns	
Body-drain diode forward voltage	V_{DF}	_	0.84	1.1	V	$I_F = 1 A$, $V_{GS} = 0$ Note4
Body-drain diode reverse recovery time	t _{rr}		85		ns	$I_F = 1 \text{ A}, V_{GS} = 0$ diF/ dt = 20 A/ μ s
N . D						

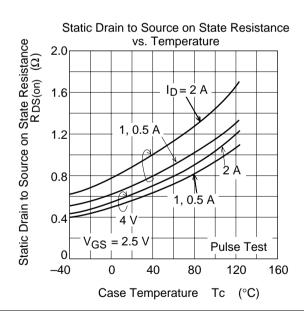
Note: 4. Pulse test

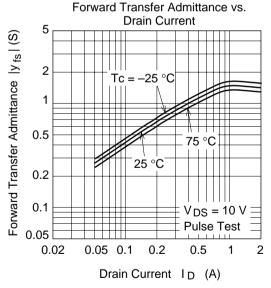
Main Characteristics

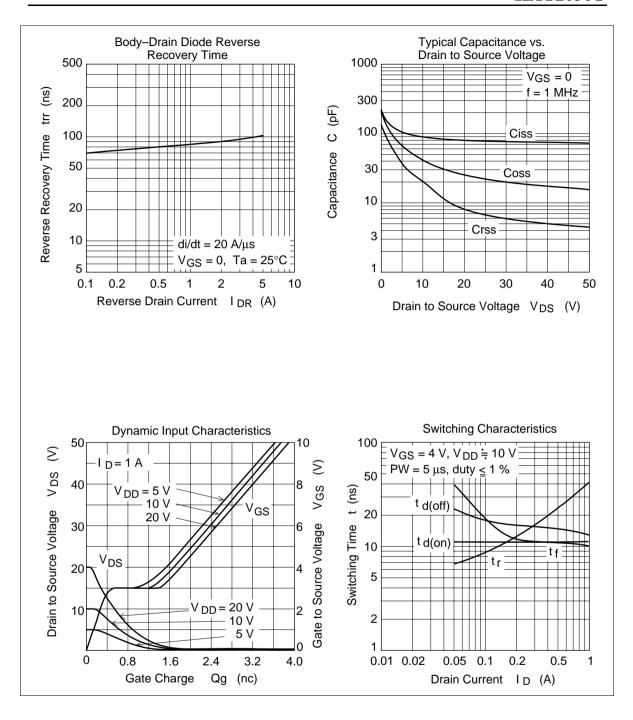


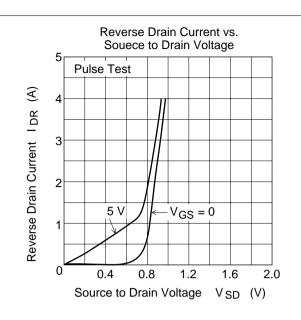


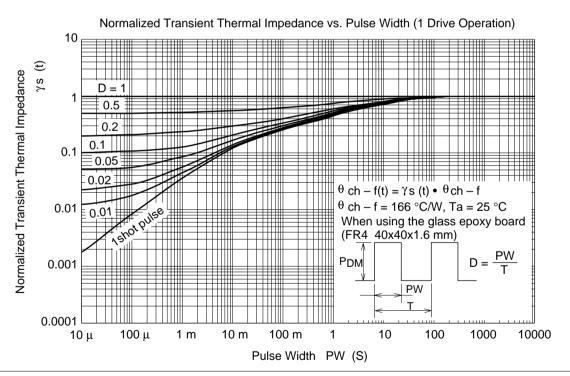


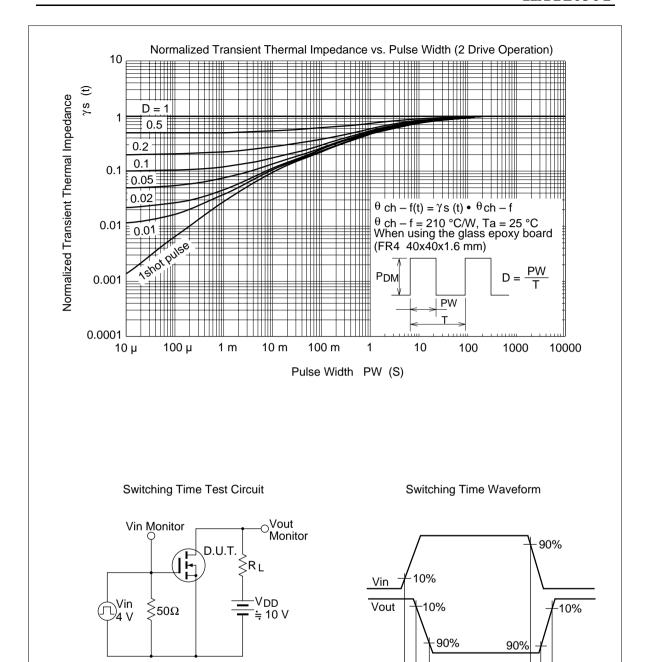












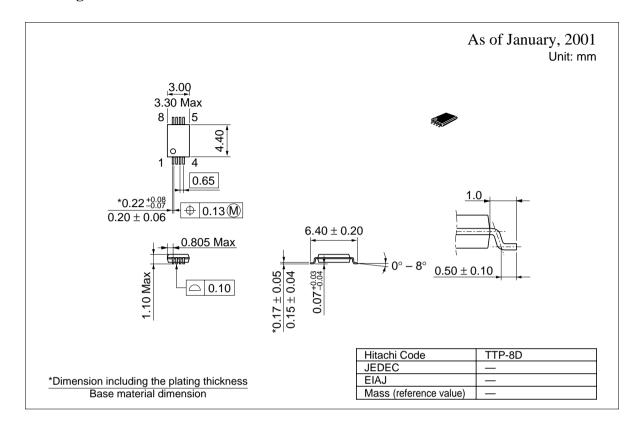
td(on)

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Package Dimensions



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