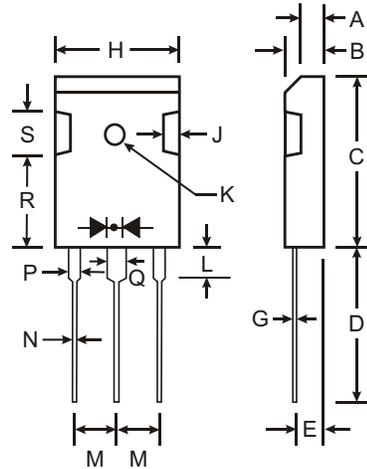


### Features

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- High Surge Capability
- High Current Capability and Low Forward Voltage Drop
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications
- Plastic Material: UL Flammability Classification Rating 94V-0

### Mechanical Data

- Case: Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Moisture sensitivity: Level 1 per J-STD-020A
- Polarity: As Marked on Body
- Marking: Type Number
- Weight: 5.6 grams (approx.)



| TO-3P                |       |       |
|----------------------|-------|-------|
| Dim                  | Min   | Max   |
| A                    | 1.88  | 2.08  |
| B                    | 4.68  | 5.36  |
| C                    | 20.63 | 22.38 |
| D                    | 18.5  | 21.5  |
| E                    | 2.1   | 2.4   |
| G                    | 0.51  | 0.76  |
| H                    | 15.38 | 16.25 |
| J                    | 1.90  | 2.70  |
| K                    | 2.9Ø  | 3.65Ø |
| L                    | 3.78  | 4.50  |
| M                    | 5.2   | 5.7   |
| N                    | 0.89  | 1.53  |
| P                    | 1.82  | 2.46  |
| Q                    | 2.92  | 3.23  |
| R                    | 11.70 | 12.84 |
| S                    | —     | 6.10  |
| All Dimensions in mm |       |       |

### Maximum Ratings and Electrical Characteristics @ T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.  
For capacitive load, derate current by 20%.

| Characteristic   | Symbol   | MBR 3030PT                | MBR 3035PT | MBR 3040PT | MBR 3045PT | MBR 3050PT                   | MBR 3060PT | Unit |
|--|--|---------------------------|------------|------------|------------|------------------------------|------------|------|
| Peak Repetitive Reverse Voltage<br>Working Peak Reverse Voltage<br>DC Blocking Voltage   | V <sub>RRM</sub><br>V <sub>RWM</sub><br>V <sub>R</sub> | 30                        | 35         | 40         | 45         | 50                           | 60         | V    |
| RMS Reverse Voltage  | V <sub>R(RMS)</sub>                                    | 21                        | 24.5       | 28         | 31.5       | 35                           | 42         | V    |
| Average Rectified Output Current<br>Total Device (See Fig. 1)<br>@ T <sub>C</sub> = 125°C  | I <sub>O</sub>   | 30                        |            |            |            |                              |            | A    |
| Non-Repetitive Peak Forward Surge Current<br>8.3ms Single half sine-wave superimposed on rated load<br>(JEDEC Method)  | I <sub>FSM</sub>                                       | 200                       |            |            |            |                              |            | A    |
| Forward Voltage Drop<br>per element (Note 3)<br>@ I <sub>F</sub> = 20A, T <sub>C</sub> = 25°C<br>@ I <sub>F</sub> = 20A, T <sub>C</sub> = 125°C<br>@ I <sub>F</sub> = 30A, T <sub>C</sub> = 25°C<br>@ I <sub>F</sub> = 30A, T <sub>C</sub> = 125°C | V <sub>FM</sub>  | —<br>0.60<br>0.76<br>0.72 |            |            |            | 0.75<br>0.65<br>0.80<br>0.75 |            | V    |
| Peak Reverse Current<br>at Rated DC Blocking Voltage, per element<br>@ T <sub>C</sub> = 25°C<br>@ T <sub>C</sub> = 125°C   | I <sub>RM</sub>  | 1.0<br>60                 |            |            |            | 5.0<br>100                   |            | mA   |
| Typical Total Capacitance<br>(Note 2)  | C <sub>T</sub>   | 500                       |            |            |            |                              |            | pF   |
| Typical Thermal Resistance Junction to Case<br>(Note 1)  | R <sub>θJc</sub>                                       | 1.4                       |            |            |            |                              |            | °C/W |
| Voltage Rate of Change (Rated V <sub>R</sub> )   | dV/dt  | 10,000                    |            |            |            |                              |            | V/μs |
| Operating Temperature Range  | T <sub>j</sub>   | -65 to +150               |            |            |            |                              |            | °C   |
| Storage Temperature Range  | T <sub>STG</sub>                                       | -65 to +175               |            |            |            |                              |            | °C   |

- Notes:
1. Thermal resistance junction to case mounted on heatsink.
  2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.
  3. Pulse width ≤300 μs, duty cycle ≤2%.

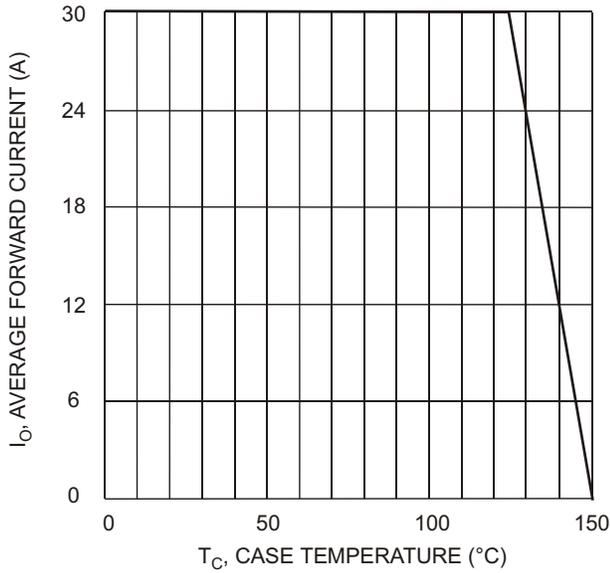


Fig. 1 Forward Current Derating Curve, total device

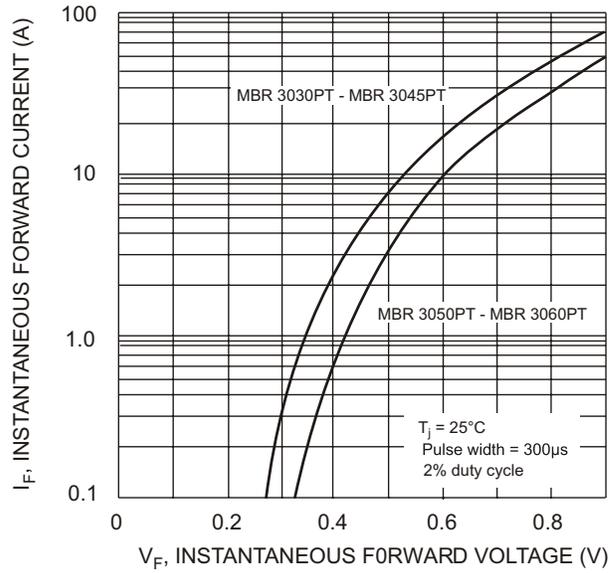


Fig. 2 Typical Forward Characteristics, per element

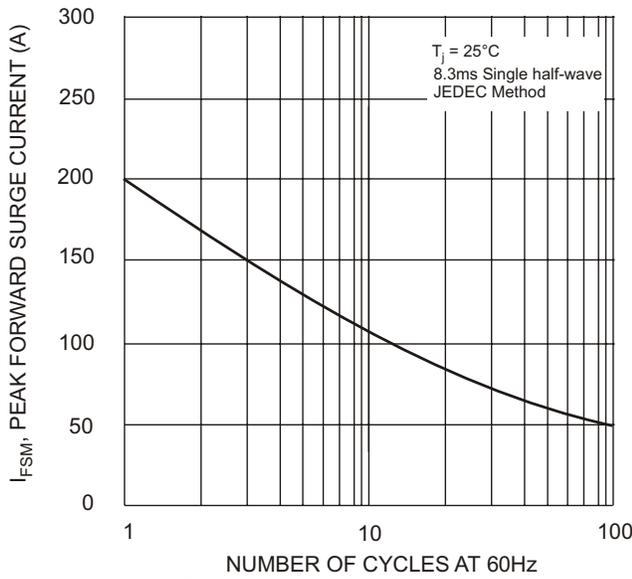


Fig. 3 Max Non-Repetitive Surge Current

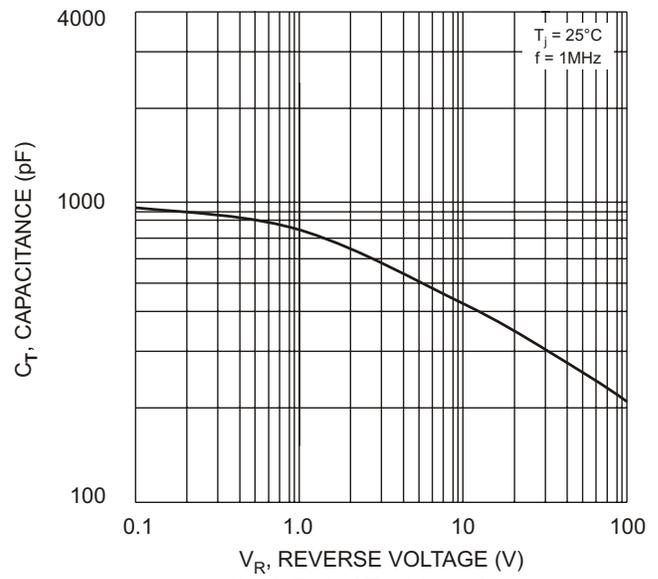


Fig. 4 Typical Total Capacitance

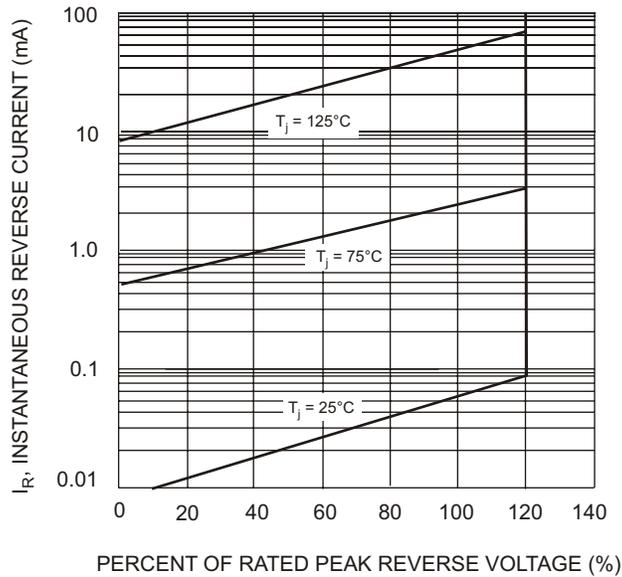


Fig. 5 Typical Reverse Characteristics, per element