



3A SCHOTTKY BARRIER RECTIFIER

B3L30LP

#### Features

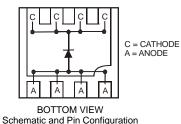
- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- Low Forward Voltage Drop
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications
- High Forward Surge Current Capability
- Lead Free by Design, RoHS Compliant (Note 1)
- "Green" Device (Note 3)



Bottom View

## **Mechanical Data**

- Case: DFN3030-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish NiPdAu over Copper lead frame. Solderable per MIL-STD-202, Method 208
- Polarity: See Diagram
- Marking Information: See Page 2
- Ordering Information: See Page 2
- Weight: 0.0172 grams (approximate)



#### Maximum Ratings @T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	30	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	21	V
Average Rectified Output Current	lo	3.0	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave Superimposed on Rated Load	I <sub>FSM</sub>	30	А

## **Thermal Characteristics**

Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance Junction to Soldering Point	$R_{ heta JS}$	—	3	°C/W
Thermal Resistance Junction to Ambient Air (Note 2)	$R_{ ext{ heta}JA}$	130	—	°C/W
Power Dissipation (Note 5)		—	2.5	
(Note 6)	PD		4.0	W
(Note 7)		—	4.5	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150		°C

#### **Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 4)	V <sub>(BR)R</sub>	30	_	_	V	I <sub>R</sub> = 5.0mA
Forward Voltage		—	0.28	_	V	$I_F = 0.5A, T_J = 25^{\circ}C$
			0.30	0.35		I <sub>F</sub> = 1.0A, T <sub>J</sub> = 25°C
	VF		0.18	0.29		I <sub>F</sub> = 1.0A, T <sub>J</sub> = 125°C
			0.33	0.40		I <sub>F</sub> = 2.0A, T <sub>J</sub> = 25°C
			0.22	0.37		I <sub>F</sub> = 2.0A, T <sub>J</sub> = 125°C
			0.35	0.45		I <sub>F</sub> = 3.0A, T <sub>J</sub> = 25°C
		_	0.26	0.42		I <sub>F</sub> = 3.0A, T <sub>J</sub> = 125°C
Deveree Current (Note 1)	I-	_	0.27	1.0	mA	$T_J = 25^{\circ}C, V_R = 30V$
Reverse Current (Note 4)	I <sub>R</sub>	—	55	90	mA	$T_{J} = 100^{\circ}C, V_{R} = 30V$

Notes: 1. No purposefully added lead.

2. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf. T<sub>A</sub> = 25°C.

3. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.

4. Short duration pulse test used to minimize self-heating effect.

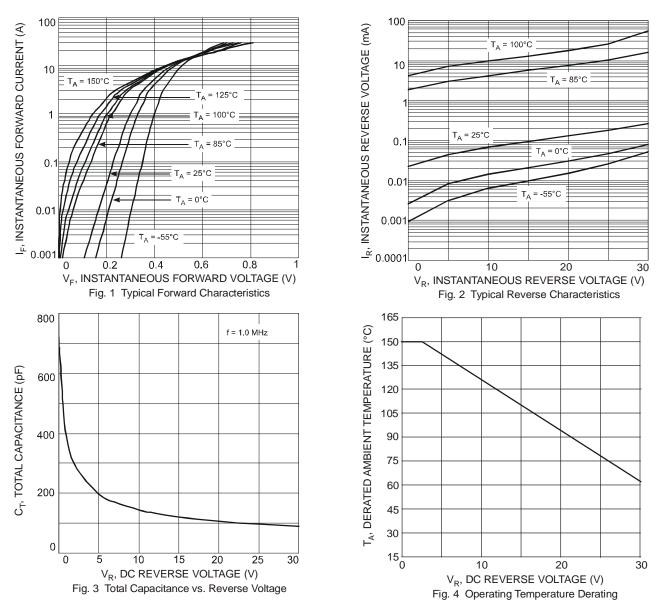
5. Device mounted on FR-4 PCB, 25mm<sup>2</sup> pad area.

6. Device mounted on FR-4 PCB, 75mm<sup>2</sup> pad area.

7. Aluminum PCB with copper mounting pad area of 75mm<sup>2</sup>.

B3L30LP Document number: DS30915 Rev. 7 - 2





## Ordering Information (Note 6)

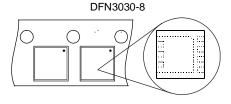
Part Number	Case	Packaging
B3L30LP-7	DFN3030-8	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

### **Marking Information**

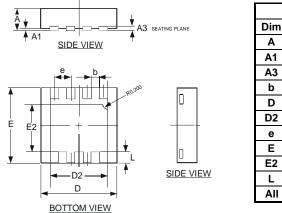


S33 = Product marking code YYWW = Date code marking YY = Last digit of year ex: 06 for 2006 WW = Week code 01 to 52



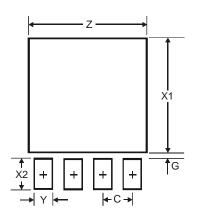


#### **Package Outline Dimensions**



DFN3030-8				
Dim	Min	Max	Тур	
Α	0.57	0.63	0.60	
A1	0	0.05	0.02	
A3			0.15	
b	0.29	0.39	0.34	
D	2.90	3.10	3.00	
D2	2.19	2.39	2.29	
е			0.65	
Е	2.90	3.10	3.00	
E2	1.64	1.84	1.74	
L	0.30	0.60	0.45	
All D	All Dimensions in mm			

# Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.59
G	0.11
X1	2.49
X2	0.65
Y	0.39
С	0.65

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