Preferred Device

Dual Series Schottky Barrier Diodes

These Schottky barrier diodes are designed for high speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand-held and portable applications where space is limited.

Features

- Pb-Free Package is Available
- Extremely Fast Switching Speed
- Low Forward Voltage 0.35 Volts (Typ) @ $I_F = 10 \text{ mAdc}$

MAXIMUM RATINGS (T_J = 125°C unless otherwise noted)

Rating	Symbol	Value	Unit
Reverse Voltage	V_R	30	V
Forward Power Dissipation @ T _A = 25°C Derate above 25°C	P _F	225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	508 (Note 1) 311 (Note 2)	°C/W
Forward Current (DC)	I _F	200 Max	mA
Junction Temperature	TJ	125 Max	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

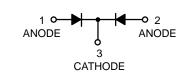
- 1. FR-4 @ Minimum Pad.
- 2. FR-4 @ 1.0 x 1.0 inch Pad.



ON Semiconductor®

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30 VOLT DUAL COMMON CATHODE SCHOTTKY BARRIER DIODES



MARKING DIAGRAM



SOT-23 CASE 318 STYLE 9



5C = Device Code M = Date Code

ORDERING INFORMATION

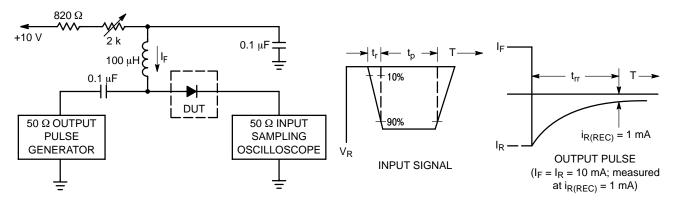
Device	Package	Shipping [†]
BAT54CLT1	SOT-23	3000/Tape & Reel
BAT54CLT1G	SOT-23 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted) (EACH DIODE)

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Breakdown Voltage (I _R = 10 μA)	V _{(BR)R}	30	-	-	V
Total Capacitance (V _R = 1.0 V, f = 1.0 MHz)	C _T	-	7.6	10	pF
Reverse Leakage (V _R = 25 V)	I _R	-	0.5	2.0	μAdc
Forward Voltage (I _F = 0.1 mAdc)	V _F	-	0.22	0.24	Vdc
Forward Voltage (I _F = 30 mAdc)	V _F	-	0.41	0.5	Vdc
Forward Voltage (I _F = 100 mAdc)	V _F	-	0.52	0.8	Vdc
Reverse Recovery Time $(I_F = I_R = 10 \text{ mAdc}, I_{R(REC)} = 1.0 \text{ mAdc}, Figure 1)$	t _{rr}	-	-	5.0	ns
Forward Voltage (I _F = 1.0 mAdc)	V _F	-	0.29	0.32	Vdc
Forward Voltage (I _F = 10 mAdc)	V _F	-	0.35	0.40	Vdc
Forward Current (DC)	IF	-	-	200	mAdc
Repetitive Peak Forward Current	I _{FRM}	-	_	300	mAdc
Non-Repetitive Peak Forward Current (t < 1.0 s)	I _{FSM}	_	_	600	mAdc



Notes: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (I_F) of 10 mA.

- 2. Input pulse is adjusted so I_{R(peak)} is equal to 10 mA.
- 3. t_p » t_{rr}

Figure 1. Recovery Time Equivalent Test Circuit

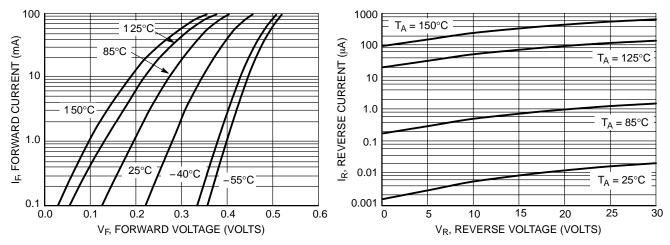


Figure 2. Forward Voltage

Figure 3. Leakage Current

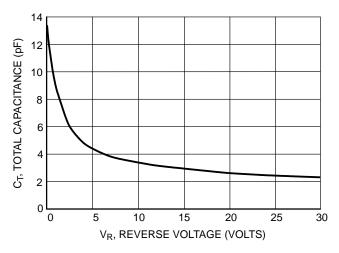
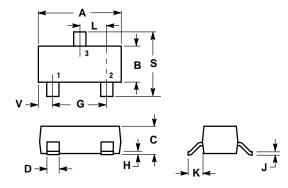


Figure 4. Total Capacitance

PACKAGE DIMENSIONS

SOT-23 (TO-236) PLASTIC PACKAGE CASE 318-08 ISSUE AJ



NOTES

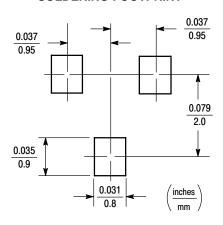
- DIMENSIONING AND TOLERANCING PER ANSI
 V14 5M 1082
- Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.
- 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
- 318–03 AND –07 OBSOLETE, NEW STANDARD 318–08.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.1102	0.1197	2.80	3.04
В	0.0472	0.0551	1.20	1.40
С	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
Н	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
K	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
٧	0.0177	0.0236	0.45	0.60

STYLE 9:

- PIN 1. ANODE 2. ANODE
 - ANODE
 CATHODE

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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