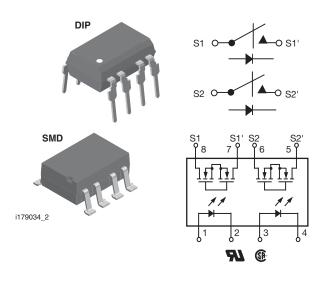


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Dual 1 Form A Solid-State Relay



DESCRIPTION

The LH1556 dual 1 form A relays are SPST normally open switches that can replace electromechanical relays in many applications. They are constructed using a GaAIAs LED for actuation control and an integrated monolithic die for the switch output. The die, fabricated in a high-voltage dielectrically isolated technology is comprised of a photodiode array, switch control circuitry, and MOSFET switches. In addition, the LH1556 SSRs employ current-limiting circuitry, enabling them to pass lightning surge testing as per ANSI/TIA-968-B and other regulatory surge requirements when overvoltage protection is provided.

FEATURES

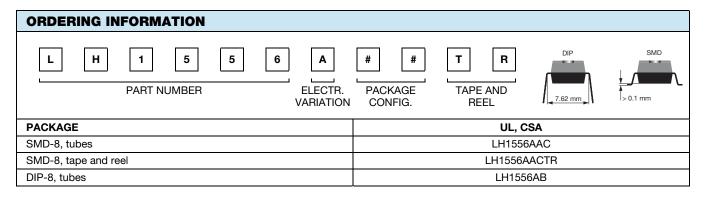
- Dual channel (LH1546)
- Current limit protection
- Isolation test voltage 5300 V_{RMS}
- Typical R_{ON} 28 Ω
- Load voltage 350 V
- Load current 120 mA
- High surge capability
- Clean bounce free switching
- Low power consumption
- SMD lead available on tape and reel
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

APPLICATIONS

- General telecom switching
 - On/off hook control
- Ring relay
- Dial pulse
- Ground start
- Ground fault protection
- Instrumentation
- Industrial controls

AGENCY APPROVALS

- UL1577: file no. E52744 system code H, double protection
- CSA: certification no. 093751





RoHS

COMPLIANT



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT					
INPUT									
LED continuous forward current		١ _F	50	mA					
LED reverse voltage	I _R ≤ 10 μA	V _R	8.0	V					
OUTPUT									
DC or peak AC load voltage	$I_L \le 50 \ \mu A$	VL	350	V					
Continuous DC load current, one pole operating		١L	120	mA					
Continuous DC load current, two poles operating		ΙL	110	mA					
SSR									
Peak load current (single shot)	t = 100 ms	I _P	(1)	mA					
Ambient temperature range		T _{amb}	- 40 to + 85	°C					
Storage temperature range		T _{stg}	- 40 to + 150	°C					
Pin soldering temperature ⁽²⁾	t = 10 s max.	T _{sld}	260	°C					
Input to output isolation voltage		V _{ISO}	5300	V _{RMS}					
Output power dissipation (continuous)		P _{diss}	550	mW					

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

⁽¹⁾ Refer to current limit performance application note for a discussion on relay operation during transient currents.

⁽²⁾ Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT								
LED forward current, switch turn-on	I _L = 100 mA, t = 10 ms	I _{Fon}		1.1	2.0	mA		
LED forward current, switch turn-off	$V_L = \pm 350 V$	I _{Foff}	0.2	1.0		mA		
LED forward voltage	I _F = 10 mA	V _F	1.15	1.26	1.45	V		
OUTPUT								
On-resistance AC/DC: pin 4 (\pm) to 6 (\pm)	$I_F = 5.0 \text{ mA}, I_L = 50 \text{ mA}$	R _{ON}		28	35	Ω		
On-resistance DC: pin 4, 6 (+) to 5 (-)	$I_F = 5.0 \text{ mA}, I_L = 100 \text{ mA}$	R _{ON}		7.0	10	Ω		
Off-resistance	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	R _{OFF}	0.5	300		GΩ		
Current limit AC/DC	$I_F = 5.0 \text{ mA}, V_L = \pm 6.0 \text{ V}, t = 5.0 \text{ ms}$	I _{LMT}	170	210	250	mA		
Off-state leakage current	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	Ι _Ο		0.35	200	nA		
	$I_F = 0 \text{ mA}, V_L = \pm 350 \text{ V}$	Ι _Ο		0.096	1.0	μA		
Output capacitance pin 4 to 6	$I_F = 0 \text{ mA}, V_L = 1.0 \text{ V}$	Co		18		pF		
	$I_F = 0 \text{ mA}, V_L = 50 \text{ V}$	Co		6.7		pF		
Switch offset	I _F = 5.0 mA	V _{OS}		0.3		μV		
TRANSFER								
Capacitance (input to output)	V _{ISO} = 1.0 V	CIO		0.67		pF		

Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Turn-on time	I _F = 5.0 mA, I _L = 50 mA	t _{on}		1.14	3.0	ms	
Turn-off time	$I_{F} = 5.0 \text{ mA}, I_{L} = 50 \text{ mA}$	t _{off}		0.71	3.0	ms	

Rev. 1.7, 25-Jul-11

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TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

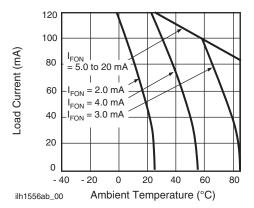


Fig. 1 - Recommended Operating Conditions

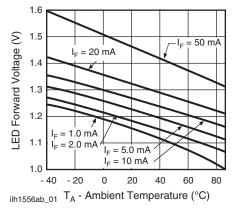


Fig. 2 - LED Voltage vs. Temperature

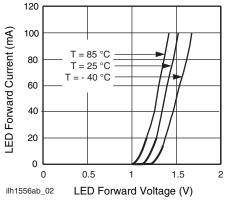


Fig. 3 - LED Forward Current vs. LED Forward Voltage

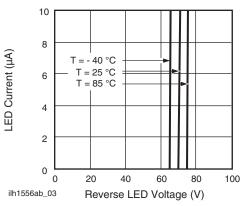


Fig. 4 - LED Reverse Current vs. LED Reverse Voltage

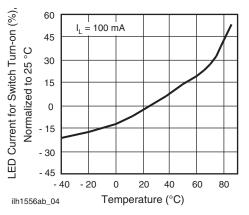


Fig. 5 - LED Current for Switch Turn-on vs. Temperature

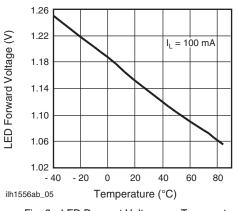


Fig. 6 - LED Dropout Voltage vs. Temperature

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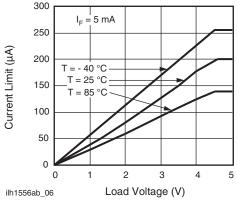
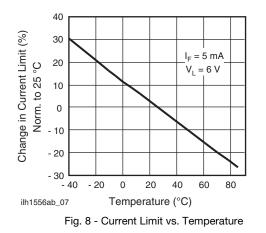


Fig. 7 - Load Current vs. Load Voltage



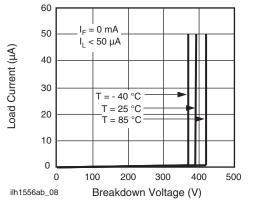


Fig. 9 - Switch Breakdown Voltage vs. Load Current

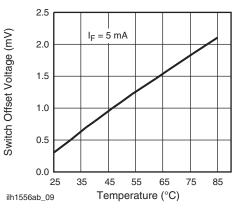
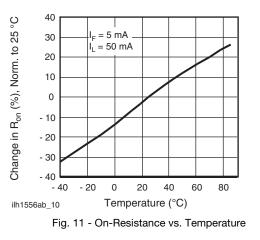


Fig. 10 - Switch Offset Voltage vs. LED Current



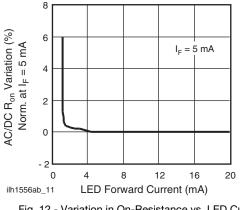


Fig. 12 - Variation in On-Resistance vs. LED Current

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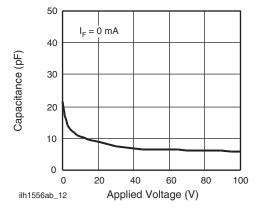


Fig. 13 - Switch Capacitance vs. Applied Voltage

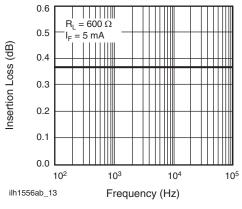
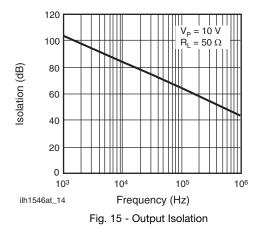


Fig. 14 - Insertion Loss vs. Frequency



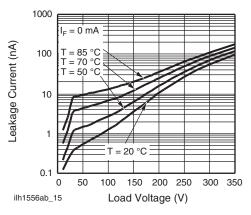


Fig. 16 - Leakage Current vs. Applied Voltage at Elevated Temperatures

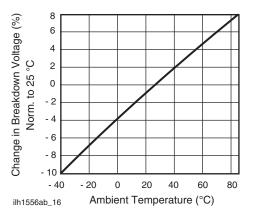


Fig. 17 - Switch Breakdown Voltage vs. Temperature

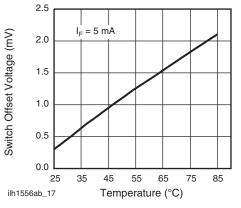


Fig. 18 - Switch Offset Voltage vs. Temperature

5 For technical questions, contact: <u>optocoupleranswers@vishay.com</u> Document Number: 83842

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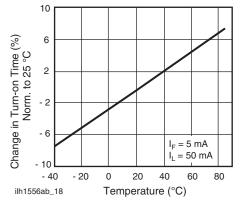
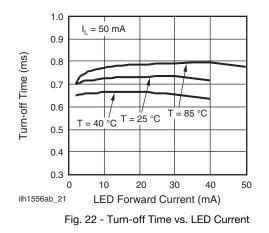


Fig. 19 - Turn-on Time vs. Temperature



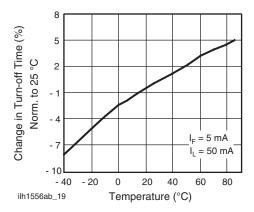


Fig. 20 - Turn-off Time vs. Temperature

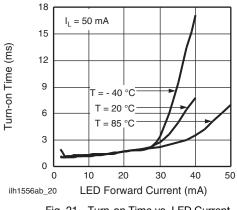


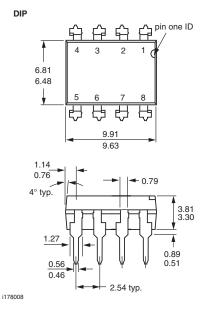
Fig. 21 - Turn-on Time vs. LED Current

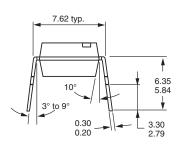
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PACKAGE DIMENSIONS in millimeters

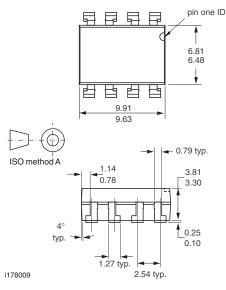


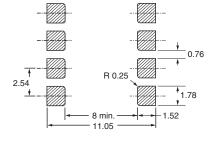


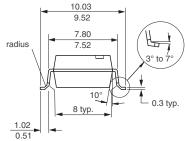
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ISO method A

SMD







PACKAGE MARKING (example)



Note

• Tape and reel suffix (TR) is not part of the package marking.

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